

#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

BV <sub>DSS</sub>	Max Rds(ON)	Max I <sub>D</sub> T <sub>A</sub> = +25°C (Note 7)
60V	$250m\Omega @ V_{GS} = 10V$	1.4A
607	$350 \text{m}\Omega$ @ V <sub>GS</sub> = 4.5V	1.2A

### **Description and Applications**

This MOSFET utilizes a unique structure that combines the benefits of low on-resistance with a fast switching speed, making it ideal for high-efficiency power-management applications.

- DC-DC converters
- Power-management functions
- Relay and solenoid driving
- Motor controls

#### **Features and Benefits**

- Low On-Resistance
- · Fast Switching Speed
- Low Threshold
- Low Gate Charge
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

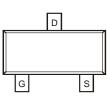
#### **Mechanical Data**

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish @3
- Weight: 0.008 grams (Approximate)



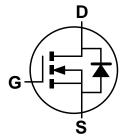


Top View



SOT23

Top View Pin Out



**Equivalent Circuit** 

#### Ordering Information (Note 4)

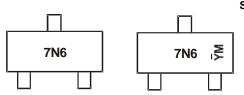
Part Number	Package	Packing		
Fait Number	Package	Qty.	Carrier	
ZXMN6A07FTA	SOT23	3,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



### **Marking Information**



SOT23

7N6 = Product Type Marking Code  $\overline{Y}M = Date Code Marking$  $\overline{Y}$  = Year (ex: K = 2023) M = Month (ex: 9 = September)

Date Code Key

Year	2010		2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	Х		K	L	М	N	Р	R	S	Т	U	V
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

### **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Ch	aracteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	60	V	
Gate-Source Voltage			Vgs	±20	V	
Continuous Drain Current	Vgs = 10V	$T_A = +70^{\circ}C$	(Note 6) (Note 6) (Note 5)	lo	1.4 1.1 1.2	А
Pulsed Drain Current (Note 7)				IDM	6.9	Α
Continuous Source Current (Body Diode) (Note 6)				Is	1	Α
Pulsed Source Current (Bod	y Diode) (Note	e 7)		Ism	6.9	Α

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 5)	Pp	625	mW	
Linear Derating Factor	1 0	5	mW/°C	
Power Dissipation (Note 6)		Pn	806	mW
Linear Derating Factor		PU	6.4	mW/°C
Thormal Posistance Junation to Ambient	(Note 5)	D	200	
Thermal Resistance, Junction to Ambient (Note 6		$R_{\theta JA}$	155	°C/W
Thermal Resistance, Junction to Ambient (Note 8)		RøJL	194	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

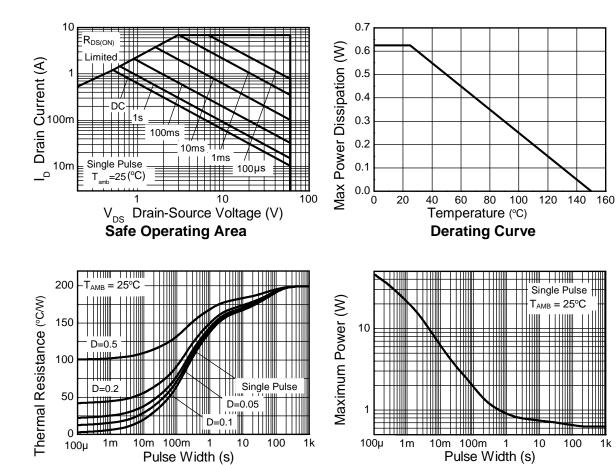
Notes:

- 5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
  6. For a device surface mounted on FR4 PCB measured at t ≤ 5secs.
  7. Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02 pulse width = 300µs pulse current limited by maximum junction temperate.

- 8. Thermal resistance from junction to solder-point (at the end of the drain lead).



#### Thermal Characteristics (continued)



**Transient Thermal Impedance** 

**Pulse Power Dissipation** 



# **Electrical Characteristics** (@ $T_A = \pm 25$ °C, unless otherwise specified.)

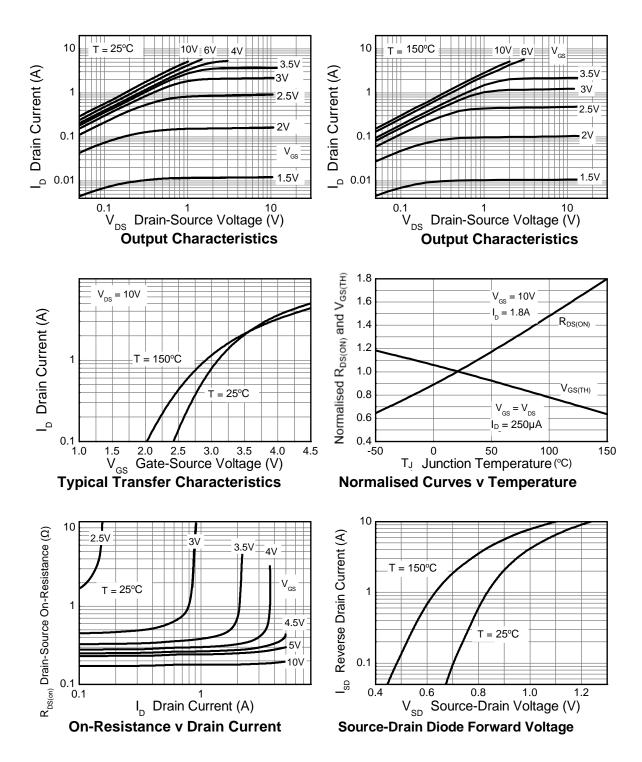
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1	μΑ	$V_{DS} = 60V$ , $V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$
ON CHARACTERISTICS						
Gate Threshold Voltage	Vgs(TH)	1.0	_	3.0	V	$I_D = 250\mu A$ , $V_{DS} = V_{GS}$
Static Drain-Source On-Resistance (Note 9)	RDS(ON)			0.250	Ω	$V_{GS} = 10V, I_D = 1.8A$
Static Brain-Source On-Resistance (Note 9)	KDS(ON)			0.350	32	$V_{GS} = 4.5V, I_{D} = 1.3A$
Forward Transconductance (Notes 9 and 11)	GFS		2.3	_	S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 1.8A
Diode Forward Voltage (Note 9)	VsD		0.8	0.95	V	$T_J = +25$ °C, $I_S = 0.45$ A, $V_{GS} = 0$ V
Reverse Recovery Time (Note 11)	trr		20.5	_	ns	$T_J = +25^{\circ}C$ , $I_F = 1.8A$ ,
Reverse Recovery Charge (Note 11)	$Q_{RR}$	_	21.3	_	nC	di/dt = 100A/μs
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	Ciss		166	_		101/11/101/
Output Capacitance	Coss	_	19.5	_	pF	$V_{DD} = 40V$ , $V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	1	8.7			1 – 1.000112
Turn-On Delay Time (Note 10)	t <sub>D(ON)</sub>	1	1.8			
Turn-On Rise Time (Note 10)	tR		1.4	_		$V_{DD} = 30V, I_{D} = 1.8A,$
Turn-Off Delay Time (Note 10)	tD(OFF)	_	4.9	_	ns	$R_G \cong 6.0\Omega$ , $V_{GS} = 10V$
Turn-Off Fall Time (Note 10)	tϝ	_	2.0	_		
Total Gate Charge (Note 10)	Qg	_	1.65	_	nC	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 5V, I <sub>D</sub> = 1.8A
Total Gate Charge (Note 10)	Qg	_	3.2	_		V 00V V 40V
Gate-Source Charge (Note 10)	Qgs	_	0.67	_	nC	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V,
Gate-Drain Charge (Note 10)	Qgd	_	0.82	_		I <sub>D</sub> = 1.8A

Notes:

Measured under pulsed conditions. Pulse width = 300µs. Duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.
 For design aid only, not subject to production testing.

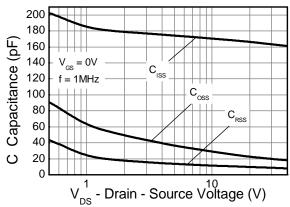


### **Typical Characteristics**

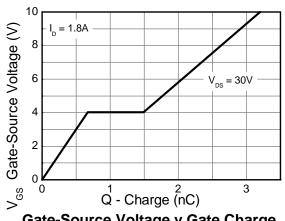




## Typical Characteristics (continued)

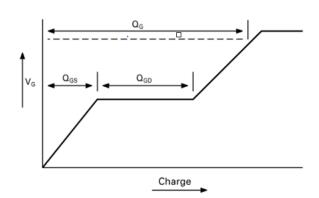


**Capacitance v Drain-Source Voltage** 

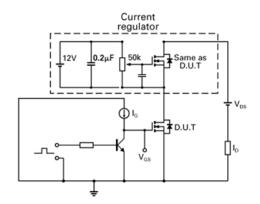


**Gate-Source Voltage v Gate Charge** 

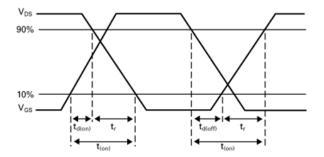
### **Test Circuits**



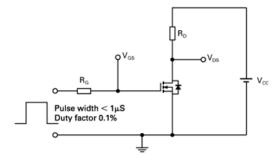
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

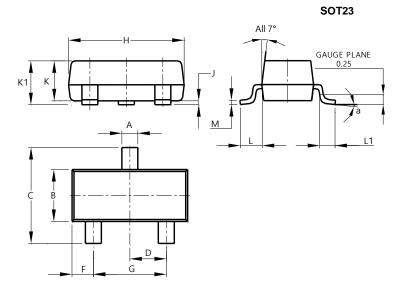


Switching time test circuit



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

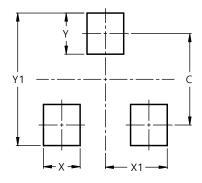


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	0°	8°				
All	Dimens	ions in	mm			

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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