TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

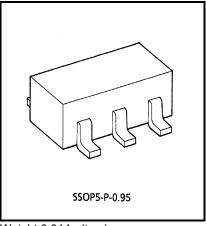
TA75S393F

Single Voltage Comparator

This device of voltage comparator that designed to operate from a single power supply over a wide range of voltage.

Normal operation from dual supplies is also to be guaranteed on voltage range from ± 1 V to ± 18 V.

V_{CC} is necessary at least more 1.5 V volts than the input common mode voltage. The output can be connected to other open collector outputs to achieve Wired-OR relationship.



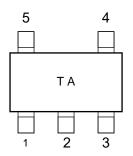
Weight:0.014g (typ.)

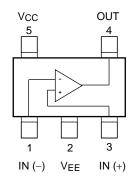
Features

- Small package.
- Single supply voltage range or dual supplies: 2 VDC to 36 VDC or ±1 VDC to ±18 VDC...
- Low supply current: 0.4 mA (typ.)
- Low input offset voltage: ±2 mV (typ.)
- Wide input common mode voltage range: 0 VDC to VCC -1.5 VDC
- Output compatible with TTL, DTL, MOS and COMS logic system.
- The output can be connected to achieve Wired-OR relation.

Marking (top view)

Pin Connection (top view)

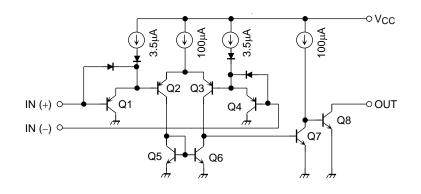




Start of commercial production 1991-01

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Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Supply voltage	V_{CC},V_{EE}	±18 or 36	V	
Differential input voltage	DVIN	±36	V	
Input voltage	VIN	-0.3 to V _{CC}	V	
Power dissipation (Note 1)	PD	200	mW	
Operating temperature	Topr	-40 to 85	°C	
Storage temperature	T _{stg}	-55 to 125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Unit rating

Electrical Characteristics (V_{CC} = 5 V, V_{EE} = GND, Ta = 25°C)

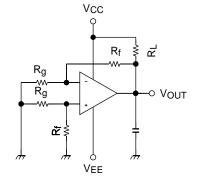
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	VIO	1	—	_	2	5	mV
Input offset current	lio	2	—	_	5	50	nA
Input bias current	lı	2	—	_	25	250	nA
Common mode input voltage	C _{MVIN}	_	—	0	_	Vcc - 1.5	V
Supply current	lcc	3	No load	_	0.4	0.8	mA
Voltage gain	Gv	_	R _L = 15 kΩ		200		V/mV
Sink current	lsink	4	IN (+) = 0 V, IN (-) = 1 V V _{OL} = 1.5 V	6	16	_	mA
Output voltage ("L" Level)	V _{OL}	5	IN (+) = 0 V, IN (–) = 1 V I _{sink} = 3 mA	_	0.2	0.4	V
Output leak current	ILEAK	_	IN (+) = 1 V, IN (-) = 0 V V _O = 5 V	_	0.1	_	nA
Response time	t _{rsp}	6	$R_L = 5.1 \text{ k}\Omega, C_L = 15 \text{ pF}$	_	1.3	_	μS

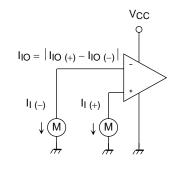
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Test Circuit

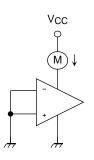
(1) Vio

(2) II, IIO

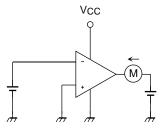




(3) ICC

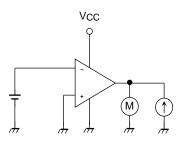


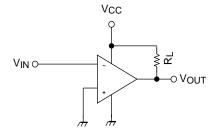




(5) VOL

(6) trsp





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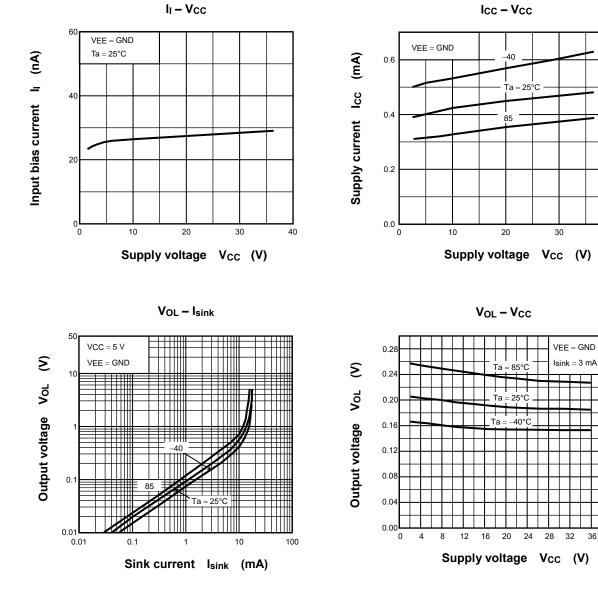
TA75S393F

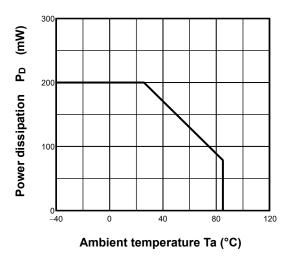
40

32 36 40

(V)

Characteristics Curves (Note)



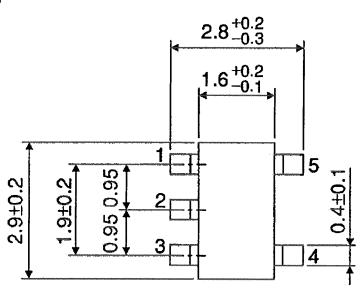


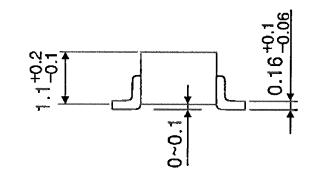
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

SSOP5-P-0.95





Weight : 0.014 g (typ.)

Unit : mm

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