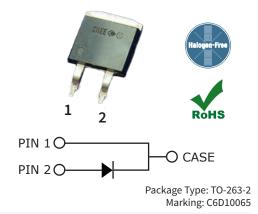
6th Generation 650 V, 10 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Low Leakage Current (I_p)

Applications

- Industrial Power Supplies
- Switch Mode Power Supplies
- Server / Telecom Power Supplies
- Power Factor Correction
- Solar Inverter
- Uninterruptible Power Supply

Maximum Ratings $(T_c = 25^{\circ}C \text{ unless otherwise specified})$

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	$V_{_{ m RRM}}$	650				
DC Blocking Voltage	V_{DC}	650	- V			
		36		T _J = 25 °C		
Continuous Forward Current	l _F .	18	- - - A	T _J = 125 °C	Fig. 3	
		10		T _J = 155 °C		
Repetitive Peak Forward Surge Current	l -	39		$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
		22		$T_c = 110 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
Non-Repetitive Peak Forward Surge Current	I _{FSM} -	80		$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	Fig. 8	
		68		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
	I _{F, Max} –	1020	-	$T_c = 25 {}^{\circ}\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$		
		960	-	$T_c = 110 {}^{\circ}\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$		
Power Dissipation	P _{tot} -	108	- W	T _J = 25 °C		
		47		T __ = 110 °C	- Fig. 4	

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Units	Test Conditions	Note	
Famous ad Walks as	tage $V_F = \begin{array}{c} 1.27 & 1.40 \\ \hline 1.37 & 1.50 \end{array}$ V	1.27	1.40	V	I _F = 10 A, T _J = 25 °C	Fig. 1	
Forward Voltage		— v	I _F = 10 A, T _J = 175 °C	- Fig. 1			
Reverse Current	I _R	2	20		V _R = 650 V, T _J = 25 °C	- Fig. 2	
		12	200	— μΑ	V _R = 650 V, T _J = 175 °C		
Total Capacitive Charge	Q _c	34		nC	V _R = 400 V, T _J = 25 °C	Fig. 5	
		611			$V_R = 0 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$		
Total Capacitance	С	67		— pF	V _R = 200 V, T _J = 25 °C, f = 1 MHz	- Fig. 6	
		53			$V_R = 400 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$	_	
Capacitance Stored Energy	E _c	5.2		μJ	V _R = 400 V	Fig. 7	

Note:

 $\label{thm:continuous} \mbox{SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.}$

Thermal & Mechanical Characteristics

Parameter	Symbol	Тур.	Units	Note	
Thermal Resistance, Junction to Case	$R_{\theta, JC}$	1.38	°C/W		
Operating Junction & Storage Temperature	$T_{_{\!J}},T_{_{\!stg}}$	-55 to +175	°C	Fig. 9	

Typical Performance

Figure 1. Forward Characteristics

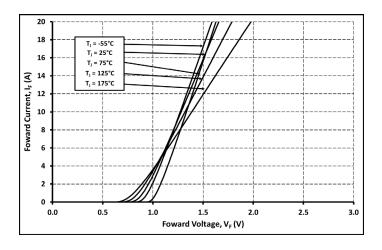


Figure 3. Current Derating

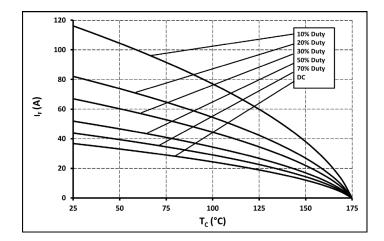


Figure 5. Total Capacitance Charge vs. Reverse

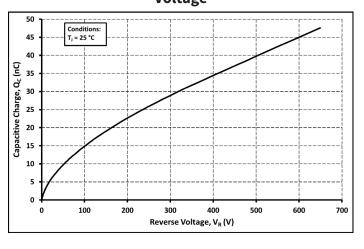


Figure 2. Reverse Characteristics

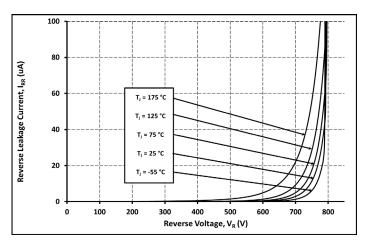


Figure 4. Power Derating

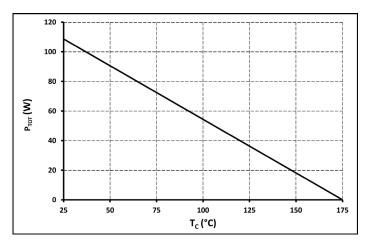
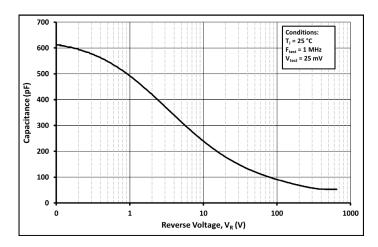


Figure 6. Capacitance vs. Reverse Voltage



4

Typical Performance

Figure 7. Capacitance Stored Energy

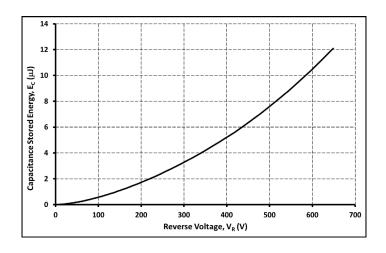


Figure 8. Non-Repetitive Peak Forward Surge Current (Sine Wave)

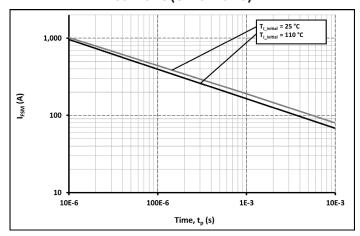
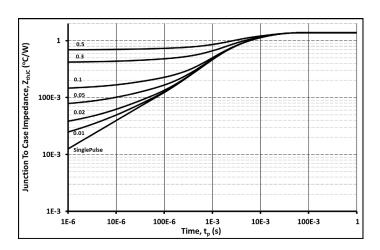


Figure 9. Transient Thermal Impedance

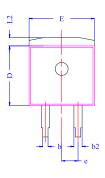


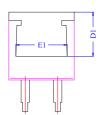
Electrostatic Discharge (ESD) Classifications

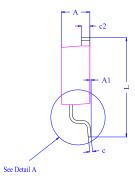
Parameter	Symbol	Class
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

Package Dimensions

Package: TO-263-2 All dimensions in mm.



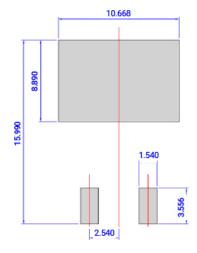






Dim	Min	Тур	Max		
A	4.32	4.445	4.57		
A1		0.20	0.25		
b	0.71	0.825	0.94		
b2	1.15	1.275	1.4		
С	0.356	0.4955	0.635		
c2	1.22	1.31	1.4		
D	8.89	9.145	9.4		
D1	6.48	6.78	6.88		
Е	10.04	10.16	10.28		
E1	7.535	7.980	8.425		
e	2.54				
L	14.73	15.24	15.75		
L1	2.29	2.54	2.79		
L2	1.15	1.27	1.39		
θ	0°	4°	8°		

Recommended Solder Pad Layout



Learn more about recommended soldering profiles in this application note.

Notes

This document and the information contained herein are subject to change without notice. Any such change shall be evidenced by the publication of an updated version of this document by Cree. No communication from any employee or agent of Cree or any third party shall effect an amendment or modification of this document. No responsibility is assumed by Cree for any infringement of patents or other rights of third parties which may result from use of the information contained herein. No license is granted by implication or otherwise under any patent or patent rights of Cree.

Not withstanding any application-specific information, guidance, assistance, or support that Cree may provide, the buyer of this product is solely responsible for determining the suitability of this product for the buyer's purposes, including without limitation for use in the applications identified in the next bullet point, and for the compliance of the buyers' products, including those that incorporate this product, with all applicable legal, regulatory, and safety-related requirements.

This product has not been designed or tested for use in, and is not intended for use in, applications in which failure of the product would reasonably be expected to cause death, personal injury, or property damage, including but not limited to equipment implanted into the human body, life-support machines, cardiac defibrillators, and similar emergency medical equipment, aircraft navigation, communication, and control systems, aircraft power and propulsion systems, air traffic control systems, and equipment used in the planning, construction, maintenance, or operation of nuclear facilities.

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact your Cree representative to ensure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

For more information, please contact:

4600 Silicon Drive Durham, North Carolina, USA 27703 www.wolfspeed.com/power