



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) max	I _{D мах} Та = +25°С
30V	30mΩ @ V _{GS} = 10V	5.3A
500	40mΩ @ V _{GS} = 4.5V	4.6A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Load Switch
- DC-DC Converters
- Power Management Functions

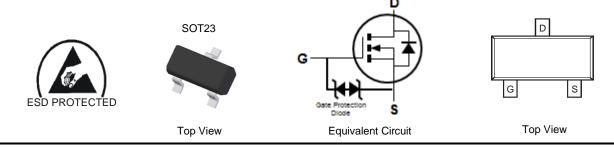
Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.009 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3069L-7	SOT23	3,000/Tape & Reel
DMN3069L-13	SOT23	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

69L	ΥM

69L = Product Type Marking Code $Y or <math>\overline{Y} = Year (ex: H = 2020)$ M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	F	G	Н		J	K	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	30	V	
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V	ID	5.3 4.3	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	Ідм	30	A	
Maximum Body Diode Forward Current (Note 6)		ls	1.35	A

Thermal Characteristics

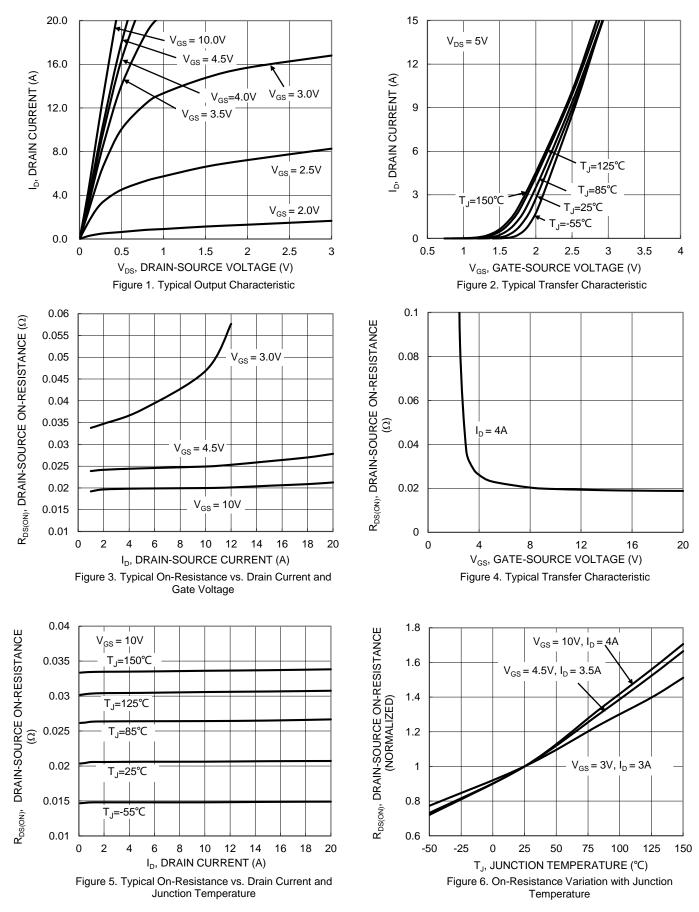
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		Po	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	158.8	°C/W
Total Power Dissipation (Note 6)		PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	97.6	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

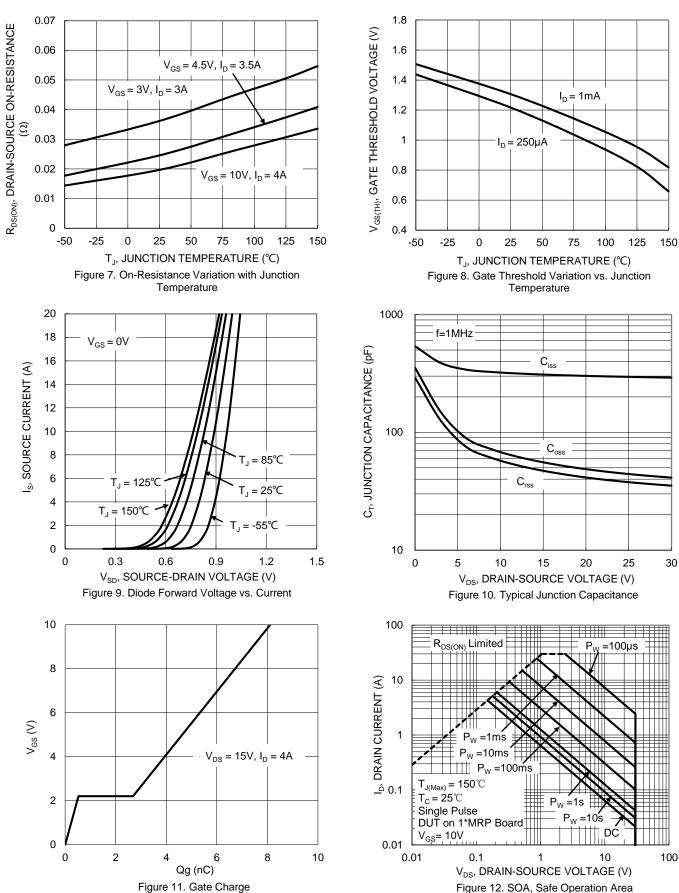
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	L	1		1	1	
Drain-Source Breakdown Voltage	BV _{DSS}	30	_		V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	V _{DS} = 24V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	1.8	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	RDS(ON)		20 25	30 40	mΩ	V _{GS} = 10V, I _D = 4.0A V _{GS} = 4.5V, I _D =3.5A
Source-Drain Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	$V_{GS} = 0V, I_S = 1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	309	_	pF	
Output Capacitance	Coss	—	56	_	pF	V _{DS} = 15V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	—	47	_	pF	
Gate Resistance	Rg	—	4.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 10V)	Qg	—	8.1	_	nC	
Total Gate Charge ($V_{GS} = 4.5V$)	Qg	—	4.3	_	nC	
Gate-Source Charge	Qgs	—	0.5	_	nC	Vbs = 15V, lb = 4A
Gate-Drain Charge	Q _{gd}	—	2.2	_	nC	
Turn-On Delay Time	tD(ON)	—	1.6	—	ns	
Turn-On Rise Time	t _R	—	2.3	—	ns	V _{DD} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	—	10.8	—	ns	$R_L = 15\Omega, R_g = 6\Omega$
Turn-Off Fall Time	t _F	_	5.2	_	ns	7

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:



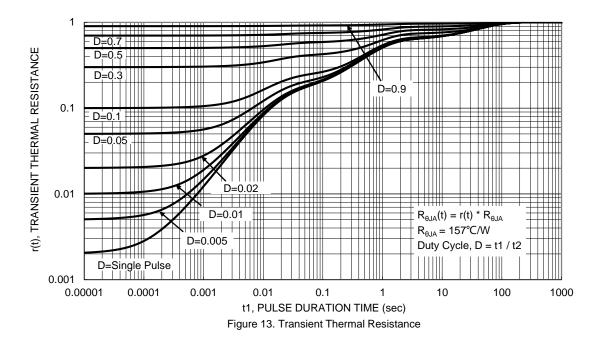








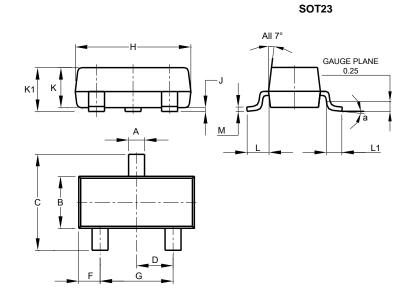






Package Outline Dimensions

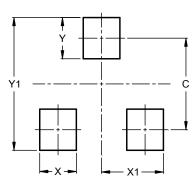
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
ĸ	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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