

WP7113YT

DESCRIPTION

T-1 3/4 (5mm) Solid State Lamp



PACKAGE DIMENSIONS

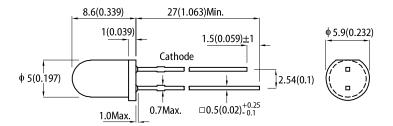
 The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode

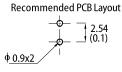
FEATURES

- Low power consumption
- Popular T-1 3/4 diameter package
- · General purpose leads
- · Reliable and rugged
- · Long life solid state reliability
- · Available on tape and reel
- · RoHS compliant

APPLICATIONS

- Status indicator
- Illuminator
- · Signage applications
- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting





Notes

1. All dimensions are in millimeters (inches).

An uninersions are in final interest (inclusion).
 Tolerance is ±0.25(0.01⁺) unless otherwise noted.
 Lead spacing is measured where the leads emerge from the package.
 The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SELECTION GUIDE

Part Number	Emitting Color		lv (mcd) @ 10mA ^[2]		Viewing Angle ^[1]
Fait Number	(Material)	Lens Type	Min.	Тур.	201/2
WP7113YT	Yellow (GaAsP/GaP)	Yellow Transparent	30	80	20°

Notes

- 41/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
 2. Luminous intensity / luminous flux: +/-15%.
 3. Luminous intensity value is traceable to CIE127-2007 standards.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Ok. a k	Ensitting Oslan	Value		11
Parameter	Symbol	Emitting Color	Тур.	Гур. Max.	Unit
Wavelength at Peak Emission I_F = 10mA	λ_{peak}	Yellow	590	-	nm
Dominant Wavelength I _F = 10mA	λ_{dom} ^[1]	Yellow	588	-	nm
Spectral Bandwidth at 50% Φ REL MAX I_F = 10mA	Δλ	Yellow	35	-	nm
Capacitance	С	Yellow	20	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	Yellow	1.95	2.4	V
Reverse Current ($V_R = 5V$)	I _R	Yellow	-	10	μA
Temperature Coefficient of λ_{peak} I_F = 10mA, -10°C \leq T \leq 85°C	TC _{λpeak}	Yellow	0.12	-	nm/°C
Temperature Coefficient of λ_{dom} I_F = 10mA, -10°C \leq T \leq 85°C	TC _{λdom}	Yellow	0.07	-	nm/°C
Temperature Coefficient of V_F I_F = 10mA, -10°C \leq T \leq 85°C	TCv	Yellow	-2	-	mV/°C

Notes:

The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd : ±1nm.)
 Forward voltage: ±0.1V.
 Wavelength value is traceable to CIE127-2007 standards.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

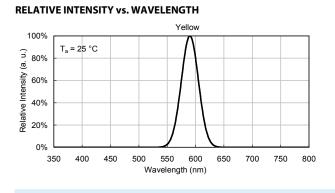
ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Symbol	Value	Unit	
Power Dissipation	P _D	75	mW	
Reverse Voltage	V _R	5	V	
Junction Temperature	Tj	110	°C	
Operating Temperature	T _{op}	-40 to +85	°C	
Storage Temperature	T _{stg}	-40 to +85	°C	
DC Forward Current	l _F	30	mA	
Peak Forward Current	I _{FM} ^[1]	140	mA	
Electrostatic Discharge Threshold (HBM)	-	8000	V	
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[2]	560	°C/W	
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[2]	380	°C/W	
Lead Solder Temperature ^[3]		260°C For 3 Seconds		
Lead Solder Temperature ^[4]		260°C For 5 Seconds		

Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. R_{In JA}, R_{In JS} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad). 3. 2mm below package base.

5. Smm below package base.
 Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA



25

2.0

1.5

1.0

0.5

0.0

0

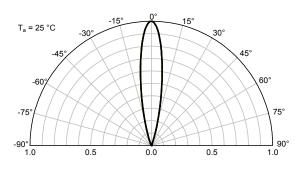
T_a = 25 °C

4

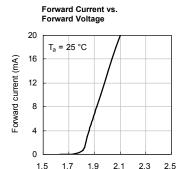
Luminous intensity normalised

at 10mA

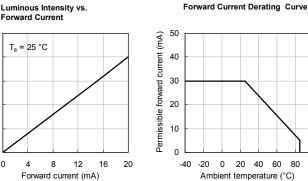
SPATIAL DISTRIBUTION

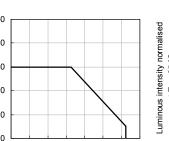


25



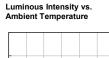
YELLOW

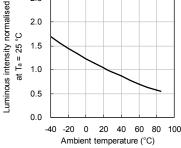




60

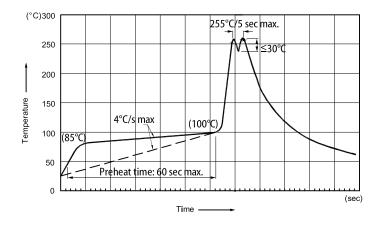
80 100





RECOMMENDED WAVE SOLDERING PROFILE

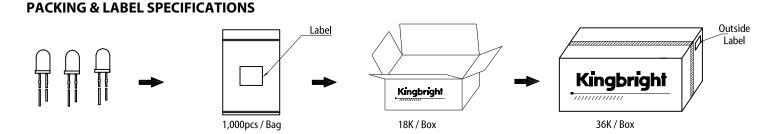
Forward voltage (V)

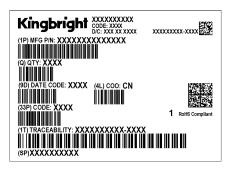


Notes

- Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260 $^\circ\mathrm{C}$

- Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
 Do not apply stress to the epoxy resin while the temperature is above 85°C.
 Fixtures should not incur stress on the component when mounting and during soldering process. SAC 305 solder alloy is recommended.
 No more than one wave soldering pass.





PRECAUTIONS

Storage conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature \leq 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at $85 \sim 100^{\circ}$ C.

LED Mounting Method

LED

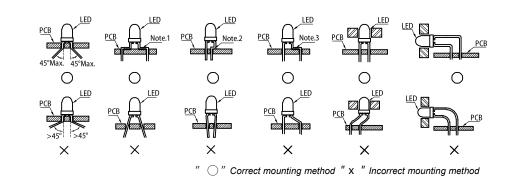
Stand-of

(Fig. 1)

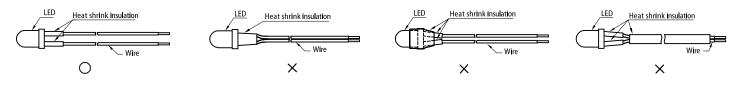
Space

 The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. Note 1-3: Do not route PCB trace in the contact area between the

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.



2. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure.



- 3. Use stand-offs (Fig.1) or spacers (Fig.2) to securely position the LED above the PCB.
 - 4. Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend (*Fig. 3*,*Fig. 4*).
 - 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (*Fig. 5*)

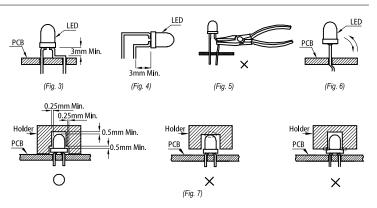
LED

(Fig. 2)

WP7113YT

Lead Forming Procedures

- 1. Do not bend the leads more than twice. (Fig. 6)
- 2. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering. (Fig. 7)
- 3. The tip of the soldering iron should never touch the lens epoxy.
- 4. Through-hole LEDs are incompatible with reflow soldering.
- 5. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications. 2
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