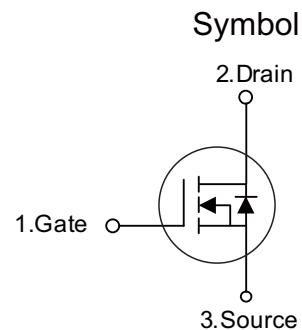


■ PRODUCT CHARACTERISTICS

| | |
|--|------|
| VDSS | 650V |
| R _{DS(on)Typ} (@V _{GS} = 10 V) | 2.2Ω |
| Q _{g@type} | 15nC |
| ID | 5A |



■ APPLICATIONS

- Electronic ballast
- High frequency switching
- LED power supply

■ FEATURES

- * High Switching Speed



■ ORDER INFORMATION

| Order codes | | Package | Packing |
|--------------|----------|---------|----------------|
| Halogen-Free | Halogen | | |
| N/A | MOT5N65F | TO-220F | 50 pieces/Tube |
| N/A | MOT5N65A | TO-220 | 50 pieces/Tube |

■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

| PARAMETER | | SYMBOL | RATINGS | UNIT |
|------------------------------------|------------------------|------------------|------------|------|
| Drain-Source Voltage | | V _{DSS} | 650 | V |
| Gate-Source Voltage | | V _{GSS} | ±30 | V |
| Drain Current | Continuous | I _D | 5 | A |
| | Pulsed (Note 2) | I _{DM} | 10 | A |
| Avalanche Energy | Single Pulsed (Note 3) | E _{AS} | 112 | mJ |
| Peak Diode Recovery dv/dt (Note 4) | | dv/dt | 3.2 | V/ns |
| Power Dissipation | TO-220 | P _D | 106 | W |
| | TO-220F | | 36 | W |
| Junction Temperature | | T _J | +150 | °C |
| Storage Temperature | | T _{STG} | -55 ~ +150 | °C |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 10mH, I_{AS} = 4.73A, V_{DD} = 50V, R_G = 25 Ω Starting T_J = 25°C

4. I_{SD} ≤ 7.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

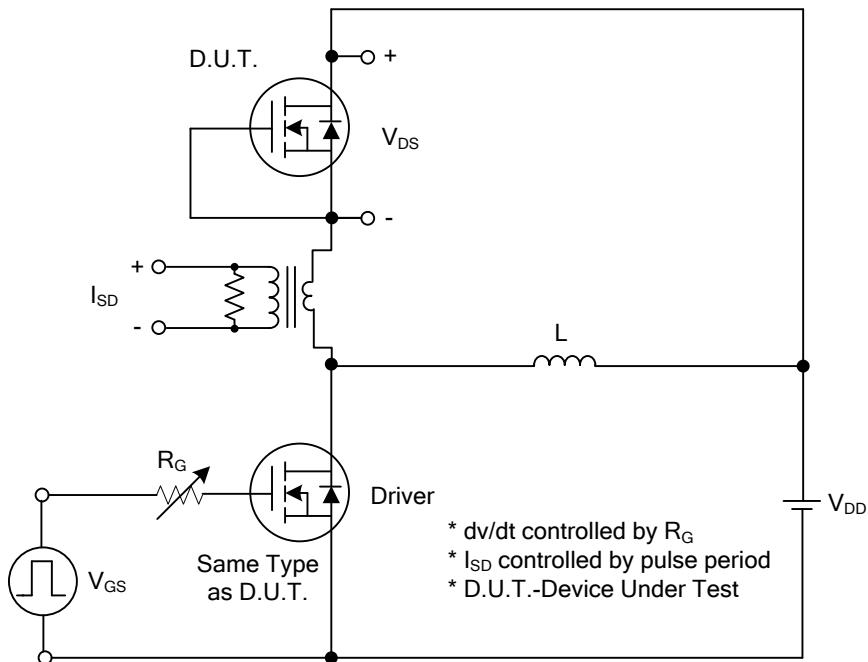
■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise noted)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|--|----------------------------|--|--|------|-----|---------------|
| Off characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$ | 650 | - | - | V |
| Drain-Source Leakage Current | I_{DSS} | $\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$ | - | - | 10 | μA |
| Gate-Source Leakage Current | Forward | $\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$ | - | - | 100 | nA |
| | Reverse | | $\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$ | - | - | -100 |
| On characteristics | | | | | | |
| Gate Threshold Voltage | $\text{V}_{\text{GS(TH)}}$ | $\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$ | 2.0 | - | 4.0 | V |
| Static Drain-Source On-State Resistance | $\text{R}_{\text{DS(ON)}}$ | $\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.5\text{A}$ | - | 2.2 | 2.4 | Ω |
| Dynamic characteristics | | | | | | |
| Input Capacitance | C_{ISS} | $\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1.0\text{ MHz}$ | - | 623 | - | pF |
| Output Capacitance | C_{OSS} | | - | 62 | - | pF |
| Reverse Transfer Capacitance | C_{RSS} | | - | 2.9 | - | pF |
| Switching characteristics | | | | | | |
| Total Gate Charge (Note 1) | Q_G | $\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.0\text{A}$ $\text{I}_G=1\text{mA}$ (Note 1, 2) | - | 15 | - | nC |
| Gate-source Charge | Q_{GS} | | - | 5.6 | - | nC |
| Gate-Drain Charge | Q_{GD} | | - | 2.5 | - | nC |
| Turn-on Delay Time (Note 1) | $\text{t}_{\text{D(ON)}}$ | $\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=0.5\text{A},$ $\text{R}_G=25\Omega$ (Note 1, 2) | - | 4.4 | - | ns |
| Rise Time | t_R | | - | 24 | - | ns |
| Turn-off Delay Time | $\text{t}_{\text{D(OFF)}}$ | | - | 122 | - | ns |
| Fall-Time | t_F | | - | 25 | - | ns |
| Source-drain diode ratings and characteristics | | | | | | |
| Maximum Body-Diode Continuous Current | I_S | | - | - | 5 | A |
| Maximum Body-Diode Pulsed Current | I_{SM} | | - | - | 10 | A |
| Drain-Source Diode Forward Voltage (Note 1) | V_{SD} | $\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=5.0\text{A}$ | - | - | 1:4 | V |
| Reverse Recovery Time (Note 1) | t_{rr} | $\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=5.0\text{A},$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$ (Note 1) | - | 328 | - | ns |
| Reverse Recovery Charge | Q_{rr} | | - | 2.65 | - | μC |

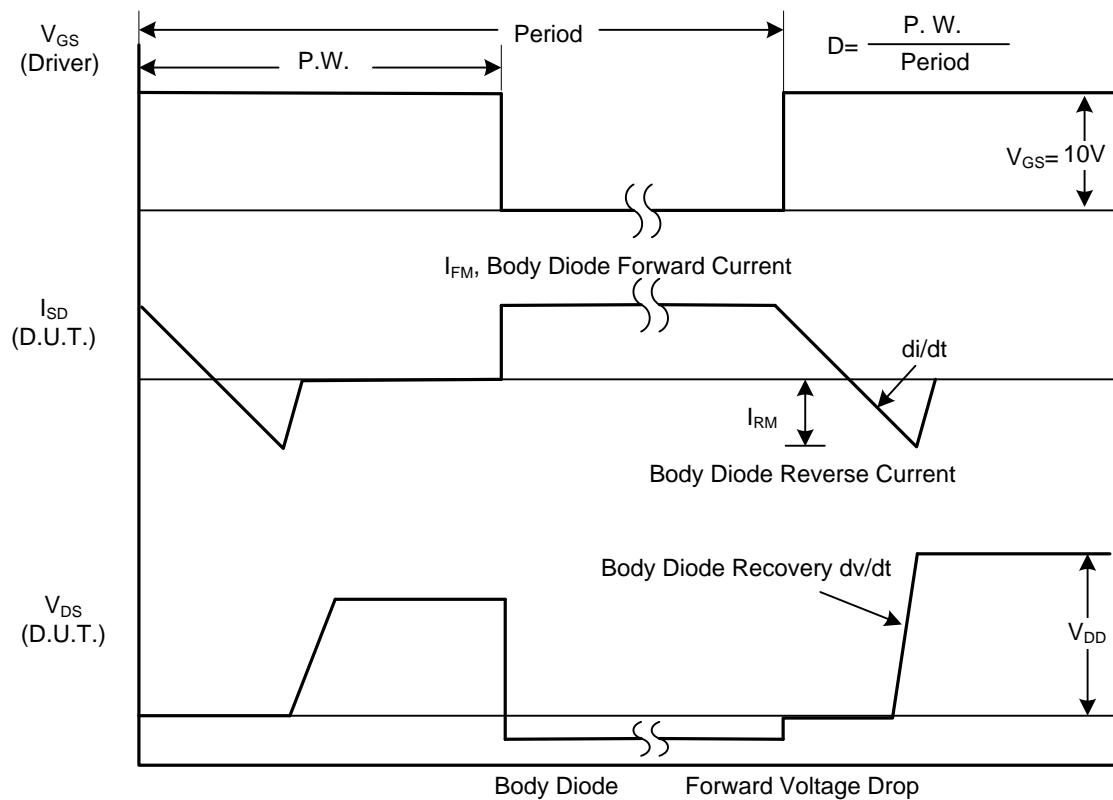
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

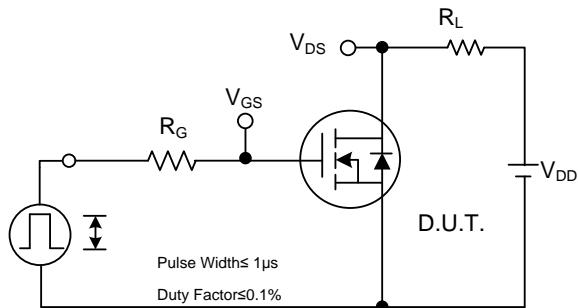
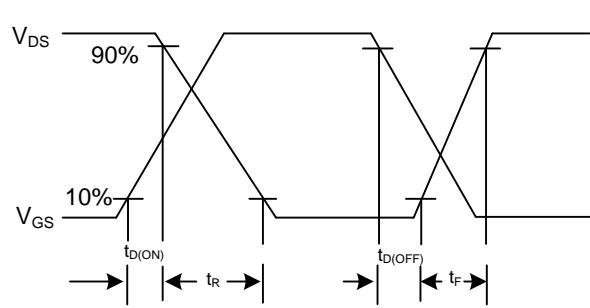
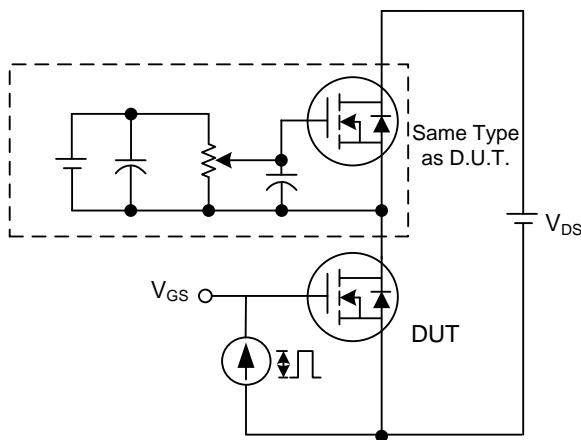
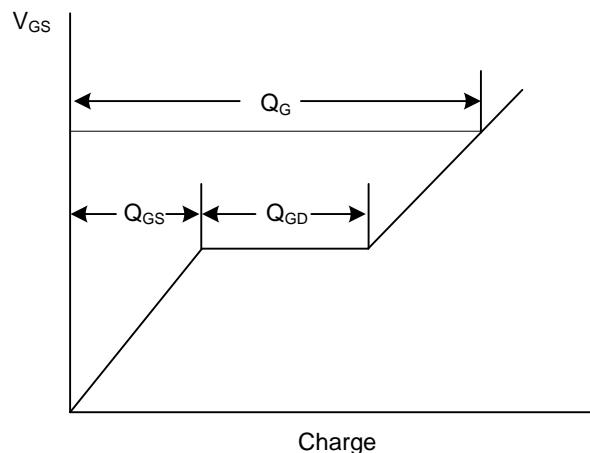
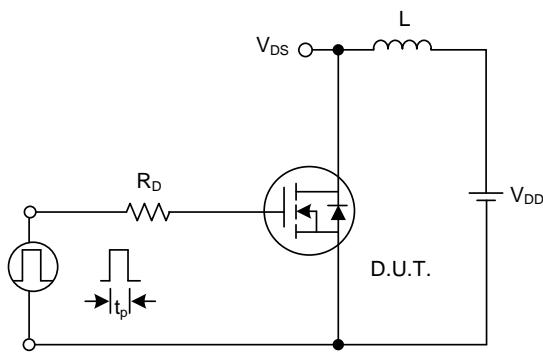
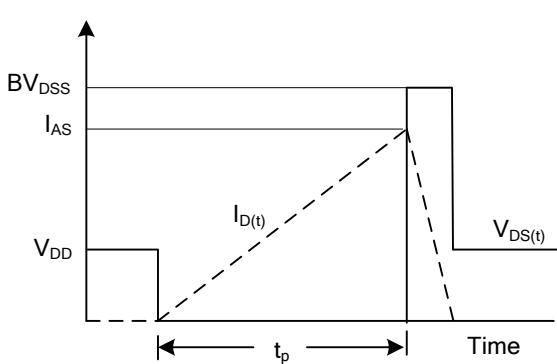
■ TEST CIRCUITS AND WAVEFORMS



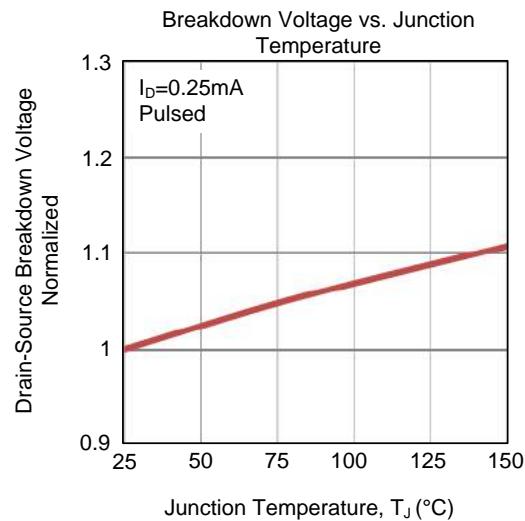
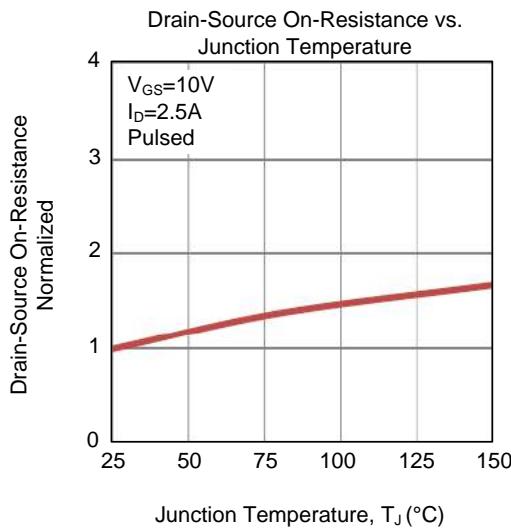
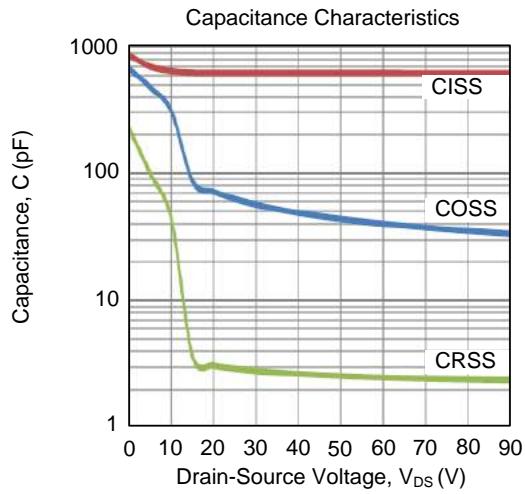
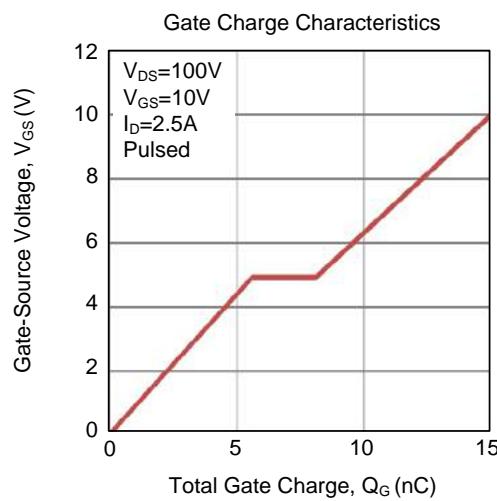
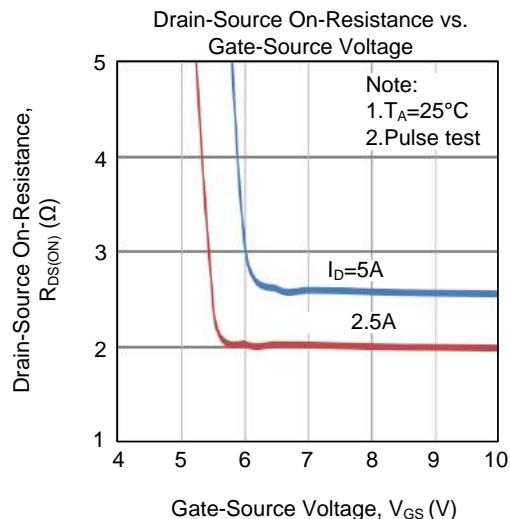
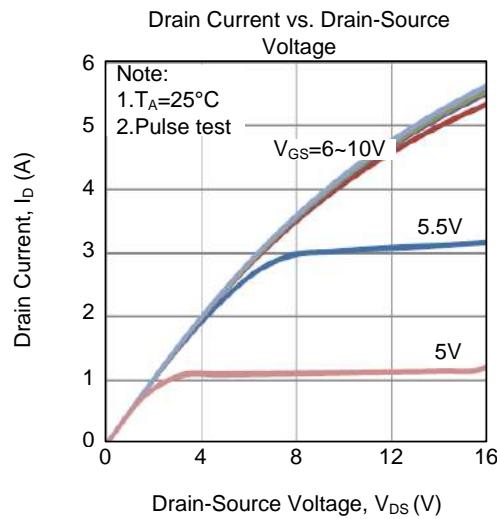
Peak Diode Recovery dv/dt Test Circuit



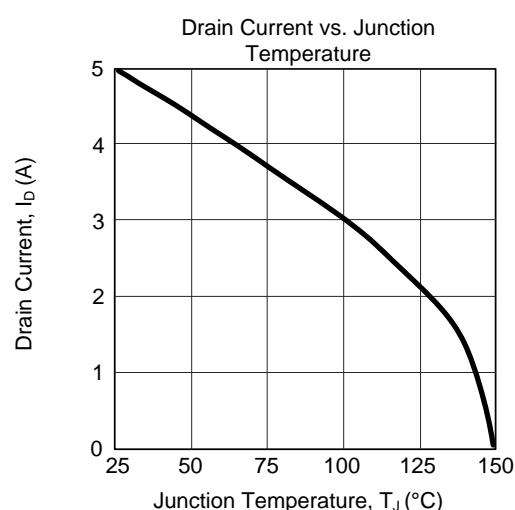
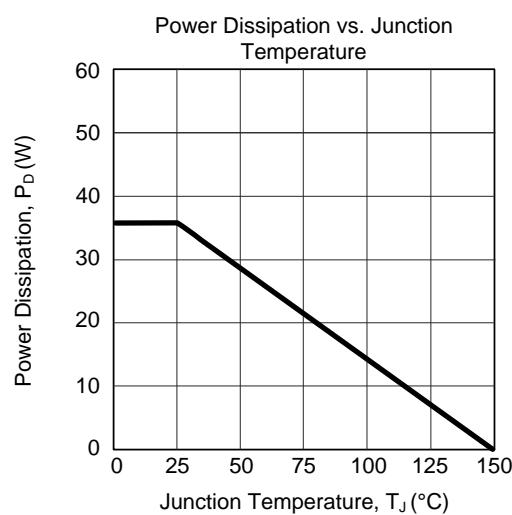
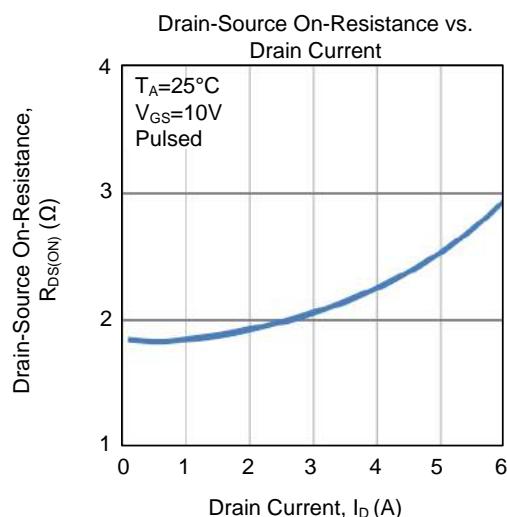
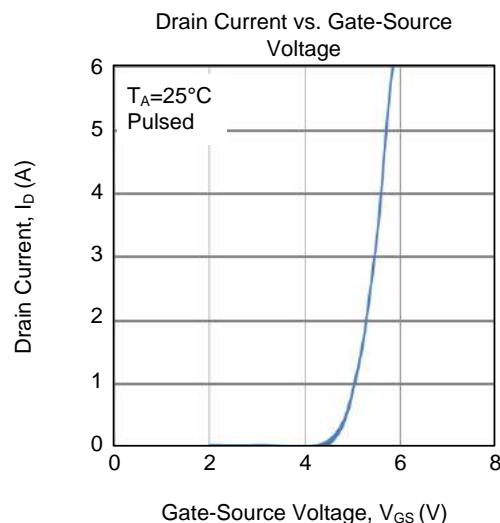
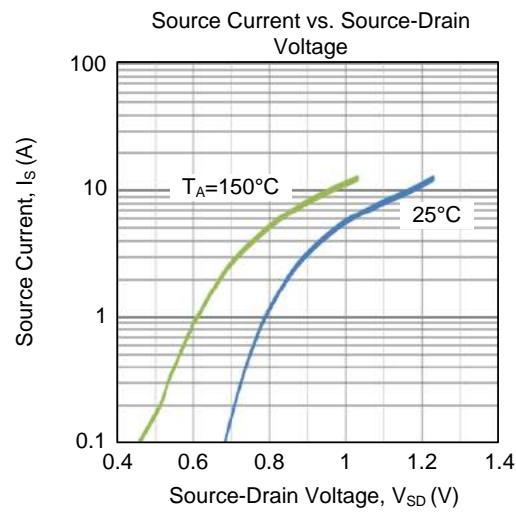
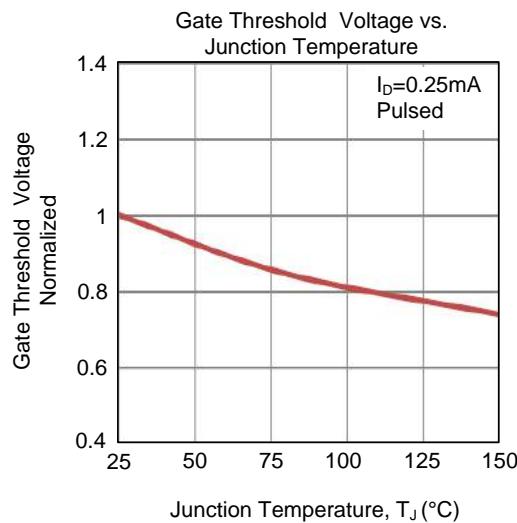
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS(Cont.)

Switching Test Circuit

Switching Waveforms

Gate Charge Test Circuit

Gate Charge Waveform

Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

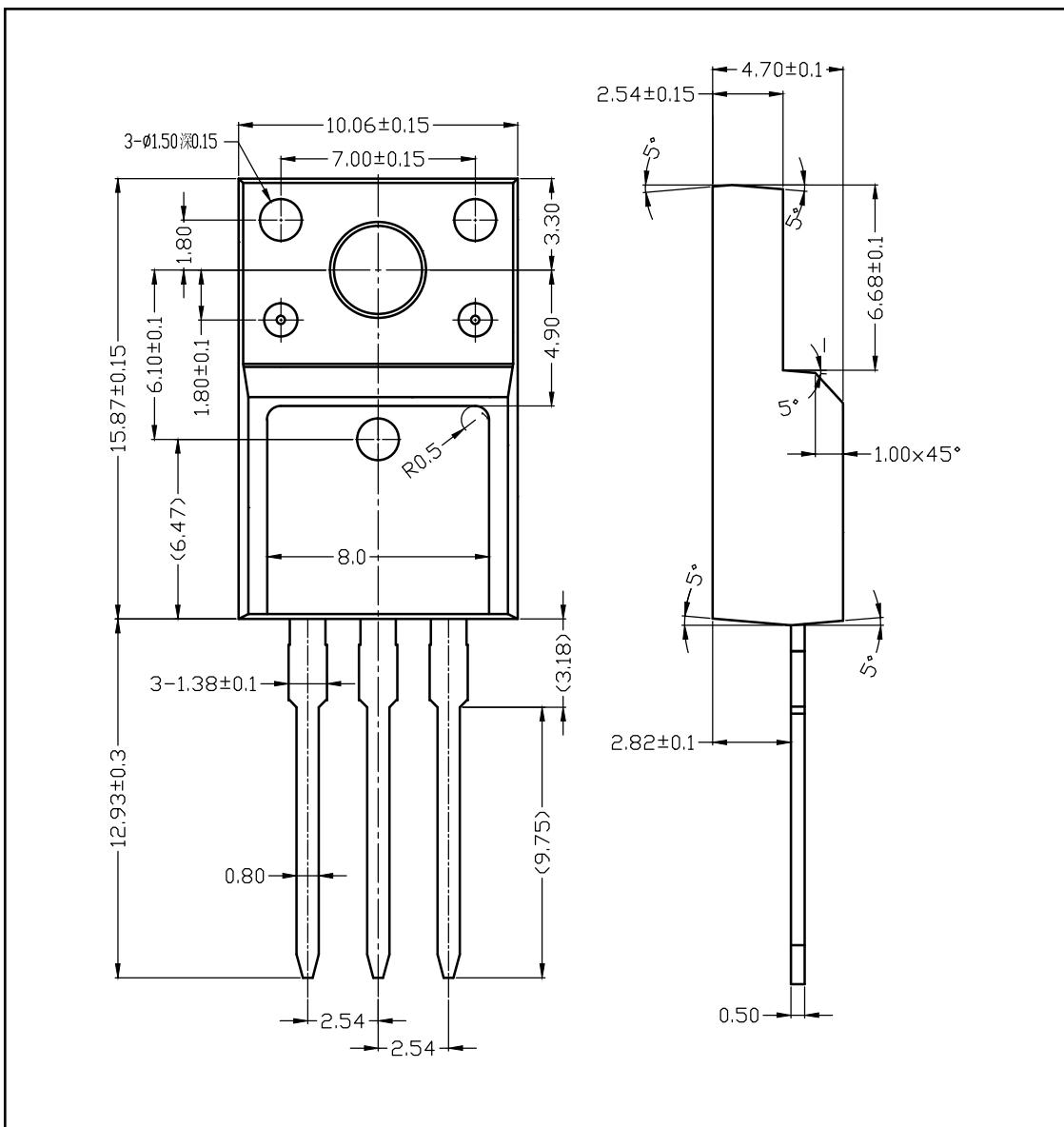
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)



■ TO-220F-3L PACKAGE OUTLINE DIMENSIONS



■ TO-220-3L PACKAGE OUTLINE DIMENSIONS

