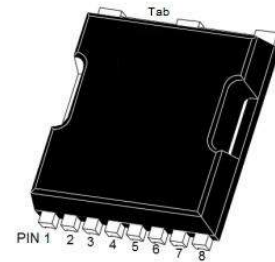


■ PRODUCT CHARACTERISTICS

VDSS	40V
$R_{DS(on)}$ typ. (@ $V_{GS}=10V$ )	0.83mΩ
ID	400A

■ FEATURES

Surface-mounted package Advanced trench cell design Super trench

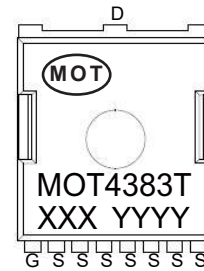


TOLL-8L

■ APPLICATIONS

High power system inverter  
Light electric vehicles  
BMS  
Drones

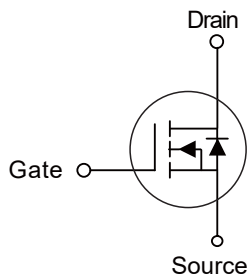
Pin configuration (Top view)



XXX = Lot Number  
YYYY = Year Week

Marking

■ SYMBOL



Order information

Device	Package	Shipping
MOT4383T/TR	TOLL-8L	2000/Tape&Reel

**■ ABSOLUTE MAXIMUM RATINGS** ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Conditions	Min	Max	Unit
Drain-Source Voltage	$V_{DS}$	$T_C = 25^\circ\text{C}$	40	-	V
Gate-Source Voltage	$V_{GS}$	$T_C = 25^\circ\text{C}$	-	$\pm 20$	V
Drain Current ( DC ) *	$I_D$	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	400	A
Drain Current ( Pulsed ) ***	$I_{DM}$	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	1600	A
Drain power dissipation	$P_{tot}$	$T_C = 25^\circ\text{C}$	-	600	W
Storage Temperature	$T_{stg}$		-55	175	$^\circ\text{C}$
Junction Temperature	$T_J$		-	175	$^\circ\text{C}$
Continuous-Source Current	$I_S$	$T_C = 25^\circ\text{C}$	-	400	A
Single Pulsed Avalanche Energy	$E_{AS}$	$V_{DD} = 40\text{ V}, L = 1\text{ mH}$	-	1352	mJ
Thermal Resistance- Junction to Ambient**	$R_{\theta JA}$		-	32.8	$^\circ\text{C/W}$
Thermal Resistance- Junction to Case**	$R_{\theta JC}$		-	0.25	

**■ ELECTRICAL CHARACTERISTICS** ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{ V}, I_{DS} = 250\ \mu\text{A}$	40	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{DS} = 250\ \mu\text{A}$	1	-	2	V
Drain Leakage Current	$I_{DSS}$	$V_{DS} = 32\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = 0\text{ V}, V_{DS} = \pm 20\text{ V}$	-	-	$\pm 100$	nA
On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_{DS} = 50\text{ A}$	-	0.83	1.0	m $\Omega$
		$V_{GS} = 4.5\text{ V}, I_{DS} = 20\text{ A}$	-	1.1	1.3	
<b>Diode Characteristics</b>						
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{SD} = 50\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.3	V
Reverse Recovery Time	$t_{rr}$	$I_{DS} = 50\text{ A}, V_{GS} = 0\text{ V}$	-	97	-	nS
Reverse Recovery Charge	$Q_{rr}$	$di_{SD}/dt = 100\text{ A}/\mu\text{s}$	-	162	-	nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 20\text{ V}$ Frequency = 1 MHz	-	8296	-	pF
Output Capacitance	$C_{oss}$		-	3294	-	
Reverse Transfer Capacitance <sup>b</sup>	$C_{rss}$		-	55	-	
Turn-on Delay Time	$t_d(on)$	$V_{DS} = 20\text{ V}, V_{GEN} = 10\text{ V},$ $R_G = 4.5\ \Omega, R_L = 0.4\ \Omega,$ $I_{DS} = 50\text{ A}$	-	19	-	nS
Turn-on Rise Time	$t_r$		-	84	-	
Turn-off Delay Time	$t_d(off)$		-	153	-	
Turn-off Fall Time	$t_f$		-	126	-	
<b>Gate Charge Characteristics<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V},$ $I_{DS} = 50\text{ A}$	-	148	-	nC
Gate-Source Charge	$Q_{gs}$		-	26	-	
Gate-Drain Charge	$Q_{gd}$		-	25	-	

Notes :

 \* Pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ 

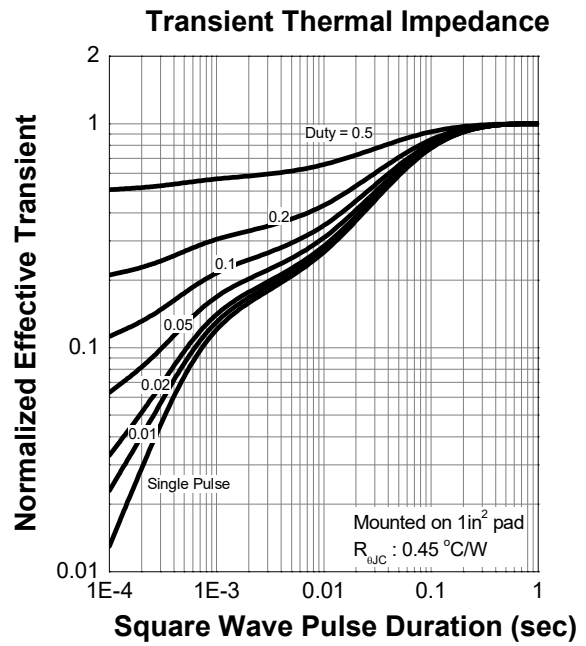
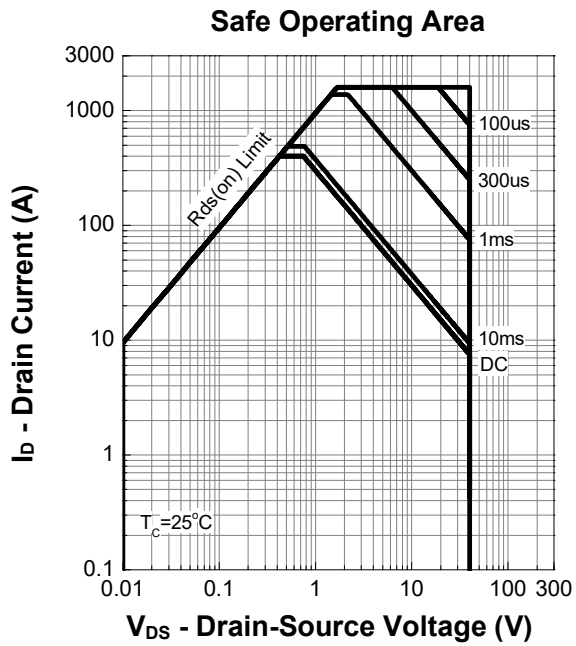
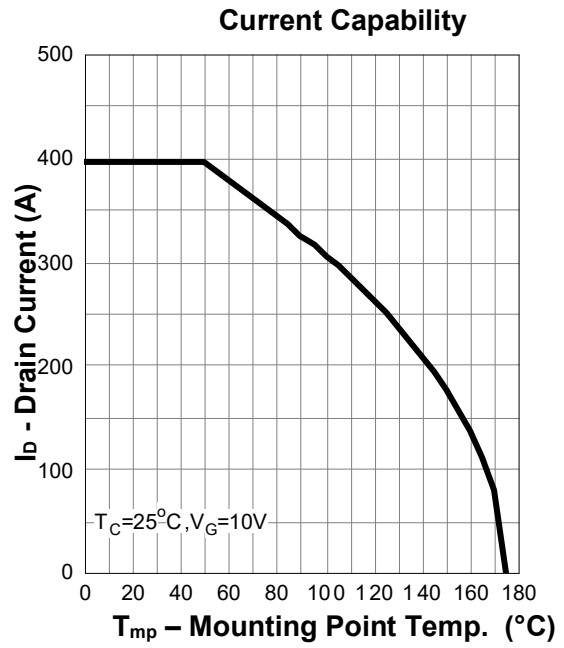
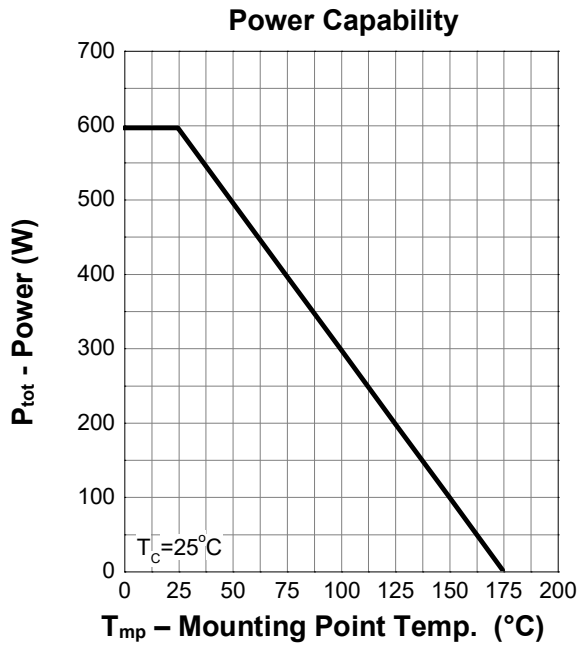
\*\* Surface Mounted on minimum footprint pad area.

\*\*\* Limited by bonding wire

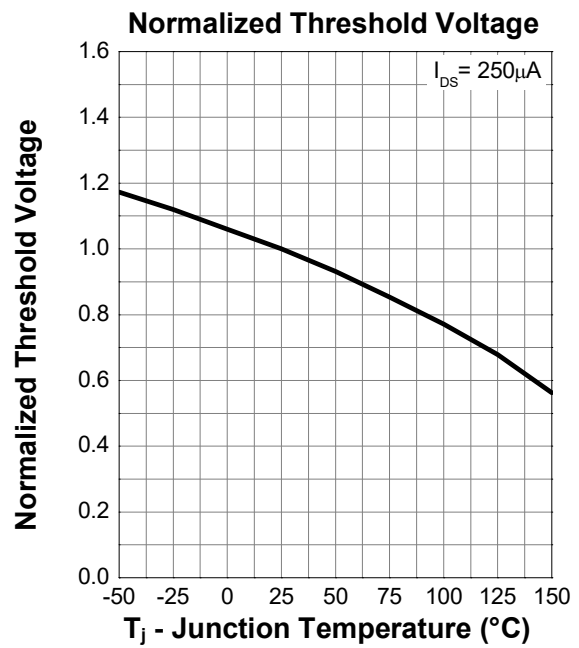
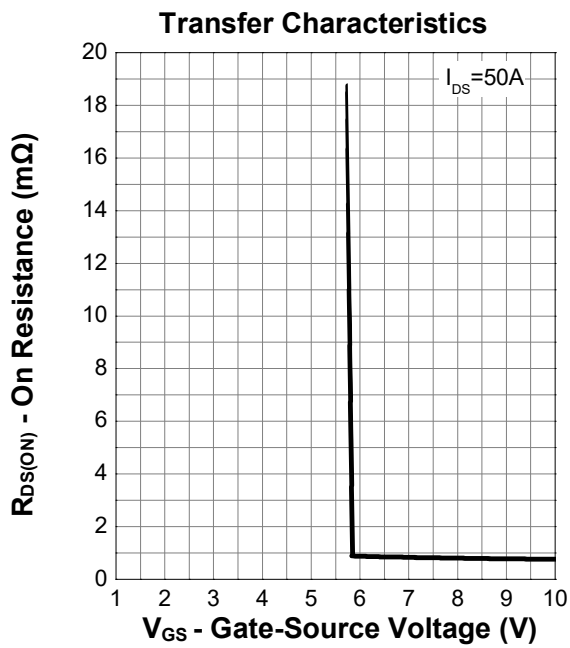
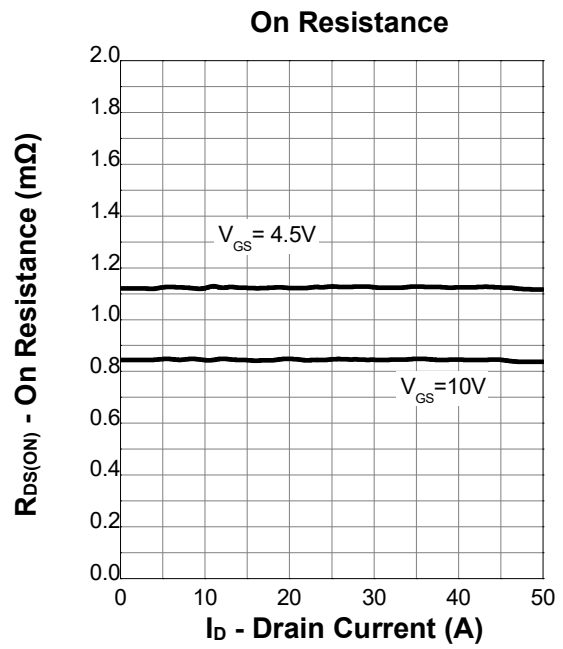
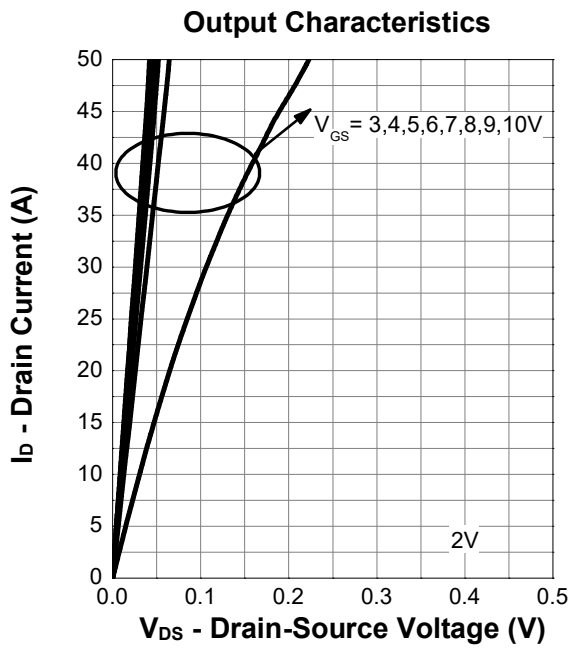
 a : Pulse test ; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ 

b : Guaranteed by design, not subject to production testing

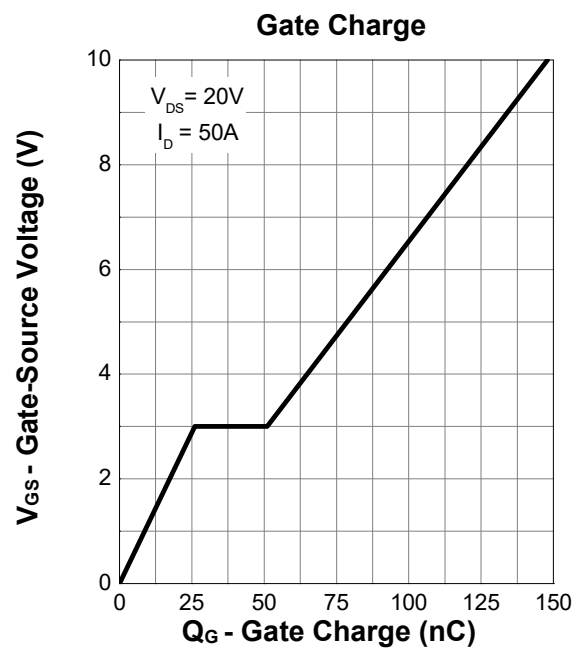
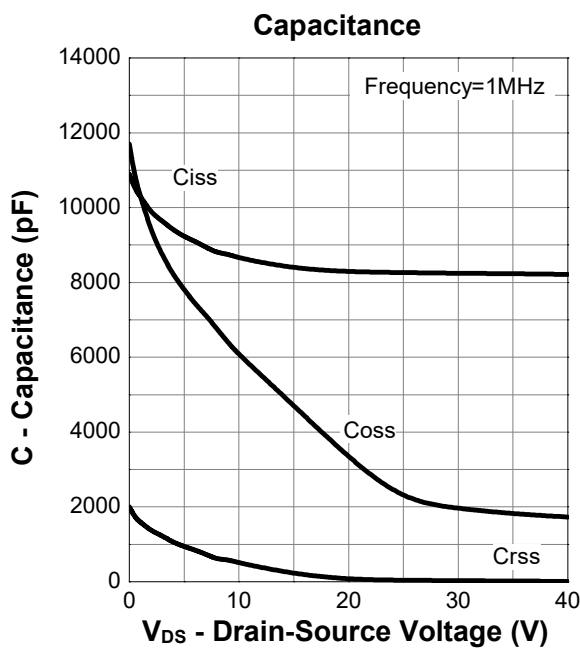
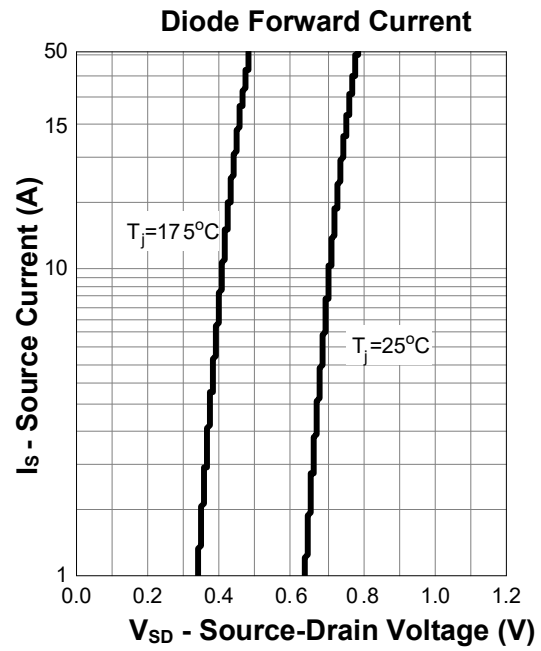
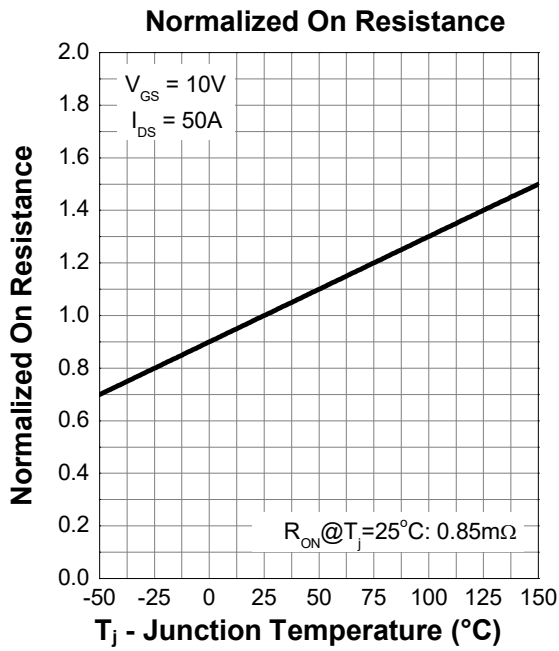
■ TYPICAL CHARACTERISTICS



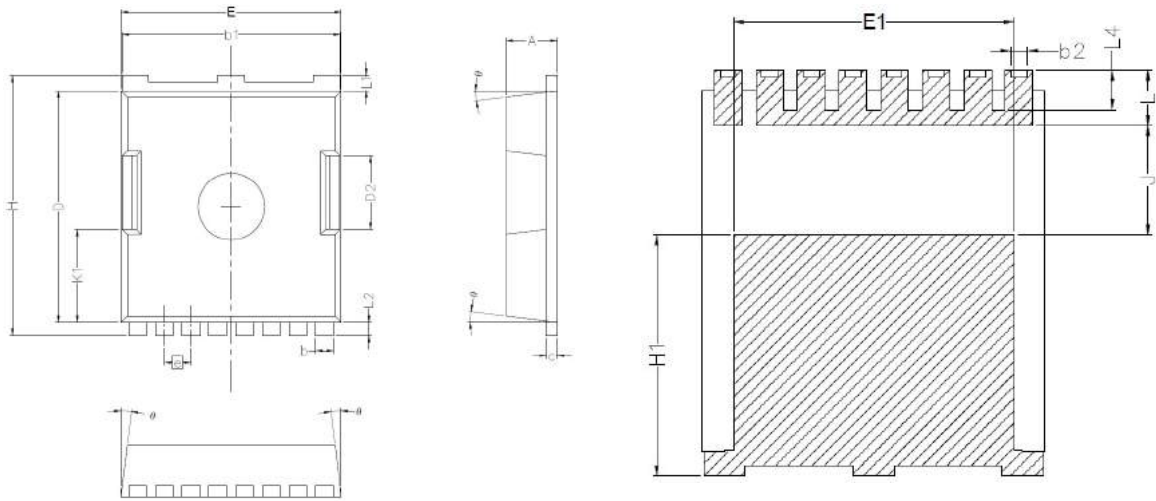
■ TYPICAL CHARACTERISTICS(Cont.)



■ TYPICAL CHARACTERISTICS(Cont.)



■ TOLL-8L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimesions In Millimeters	
	Min.	Max.
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
$\theta$	4°	10°

- The information contained hSurface-mounted package Advnced terch cell design Super trencherein is subject to change without notice.
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Shenzhen Base:

Renmao Industrial Park, No. 2 Songgang Avenue, Bao'an District, Shenzhen

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