

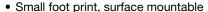
High Performance Schottky Rectifier, 2 A

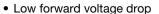


SMA (DO-214AC)

PRODUCT SUMMARY				
Package	SMA (DO-214AC)			
I _{F(AV)}	2 A			
V _R	40 V			
V _F at I _F	0.63 V			
I _{RM} max.	26 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single			
E _{AS}	3.0 mJ			

FEATURES







COMPLIANT

High frequency operation

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-20MQ040NTRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	2	Α		
V_{RRM}		40	V		
I _{FSM}	$t_p = 5 \mu s sine$	120	Α		
V _F	2 A _{pk} , T _J = 125 °C	0.63	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-20MQ040NTRPbF	UNITS
Maximum DC reverse voltage	V _R	40	V
Maximum working peak reverse voltage	V_{RWM}	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_C = 110$ °C, On PC board 9 mm ² island (0.013)		2.1	A
See fig. 4	I _{F(AV)}	50 % duty cycle at $T_C = 112$ °C, On PC board 9 mm ² island (0.013		2	A
Maximum peak one cycle non-repetitive surge current	l	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	120	A
See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	30	_ ^
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 6 \text{mH}$		3	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 1.0		А	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		2 A		0.69	V
		1.5 A	T _J = 25 °C	0.62	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A		0.54	
See fig. 1	V _{FM} ('')	2 A		0.63	
		1.5 A	T _J = 125 °C	0.56	
		1 A		0.49	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	0.5	- mA
See fig. 2	IRM (1)	T _J = 125 °C	V _R = Rated V _R	26	
Threshold voltage	V _{F(TO)}	$T_{J} = T_{J} \text{ maximum} $ 0.36 104		0.36	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance	C _T	$V_R = 10 V_{DC}$, $T_J = 25 °C$, test signal = 1 MHz 38		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (DO-214AC) (similar D-64)	2	F

Note

$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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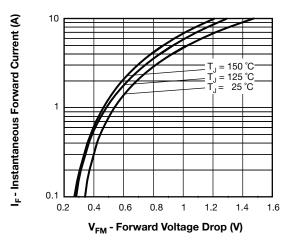


Fig. 1 - Maximum Forward Voltage Drop Characteristics

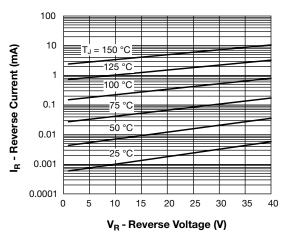


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

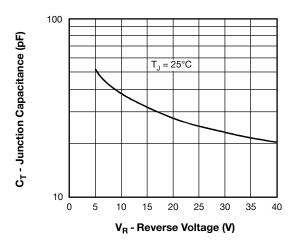


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

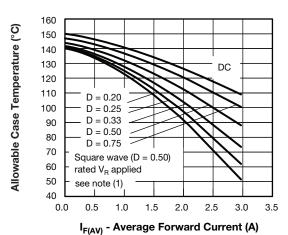
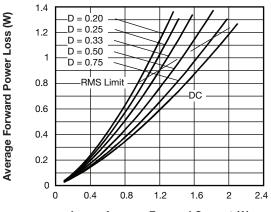
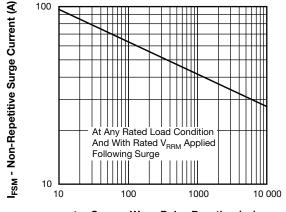


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t_p - Square Wave Pulse Duration (μs)

Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

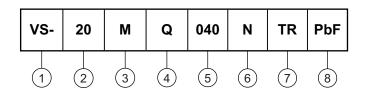
Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

- Current rating

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (040 = 40 V)

6 - N = new SMA

7 - TR = tape and reel

8 - PbF = lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-20MQ040NTRPbF	5AT	7500	13" diameter plastic tape and reel		

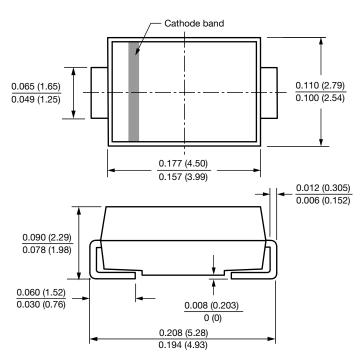
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95400</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?96006			



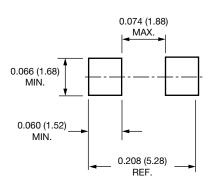
SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout





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Vishay

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