V8PM153

Vishay General Semiconductor

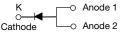
High Current Density Surface-Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.54$ V at $I_F = 4$ A

eSMP[®] Series

www.vishay.com





LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	8.0 A			
V _{RRM}	150 V			
I _{FSM}	140 A			
V_F at I_F = 8.0 A (T_J = 125 °C)	0.62 V			
T _J max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V8PM153	UNIT	
Device marking code		8MP		
Maximum repetitive peak reverse voltage	V _{RRM}	150	V	
Maximum quarage forward restified surrent (fig. 1)	I _F ⁽¹⁾	8.0	— A	
Maximum average forward rectified current (fig. 1)	I _F ⁽²⁾	3.4		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	140	A	
Operating junction temperature range	T _J ⁽³⁾	-40 to +175	°C	
Storage temperature range	T _{STG}	-55 to +175	°C	

Notes

⁽¹⁾ Mounted on 30 mm x 30 mm pad areas aluminum PCB

⁽²⁾ Free air, mounted on recommended copper pad area

 $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J <1/ R_{0JA}

Revision: 19-Nov-2020

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Document Number: 87203

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ROHS COMPLIANT

HALOGEN

FREE

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 4.0 A	T _J = 25 °C	VF ⁽¹⁾	0.68	-	V
	I _F = 8.0 A			0.81	0.87	
	$I_{F} = 4.0 \text{ A}$	- T _J = 125 °C		0.54	-	
	I _F = 8.0 A			0.62	0.67	
Reverse current	V 100 V	T _J = 25 °C T _J = 125 °C	I _R ⁽²⁾	0.0007	-	mA
	V _R = 100 V			1.1	-	
Reverse current	V _R = 150 V	T _J = 25 °C	I _R ⁽²⁾	-	0.1	m (
	$V_{\rm R} = 150 \text{ V}$ $T_{\rm J} = 125 \text{ °C}$	'R (=/	2.4	7	mA	
Typical junction capacitance	4.0 V, 1 MHz		CJ	470	-	pF

Notes

 $^{(1)}$ Pulse test: 300 μs pulse width, 1 % duty cycle $^{(2)}$ Pulse test: pulse width $\leq 5~ms$

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V8PM153	UNIT		
Turping thermal registering	R _{0JA} (1)(2)	75	°C/W		
Typical thermal resistance	R _{0JM} ⁽³⁾	4			

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{\theta JA}$

⁽²⁾ Free air mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ - junction to ambient ⁽³⁾ Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V8PM153-M3/H	0.10	Н	1500	7" diameter plastic tape and reel	
V8PM153-M3/I	0.10	I	6500	13" diameter plastic tape and reel	
V8PM153HM3/H ⁽¹⁾	0.10	Н	1500	7" diameter plastic tape and reel	
V8PM153HM3/I (1)	0.10	l	6500	13" diameter plastic tape and reel	

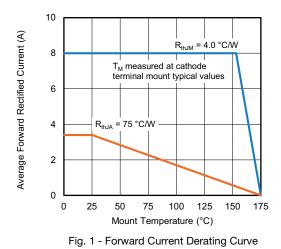
Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)



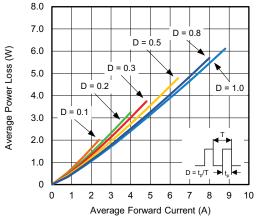
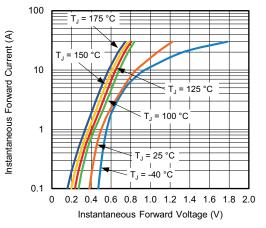
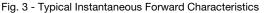


Fig. 2 - Forward Power Loss Characteristics





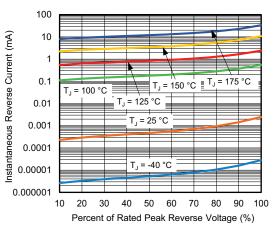


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

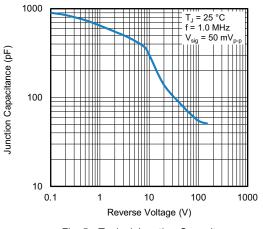


Fig. 5 - Typical Junction Capacitance

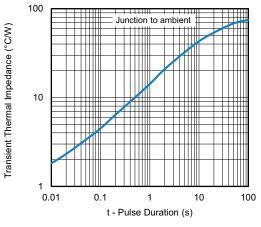


Fig. 6 - Typical Transient Thermal Impedance

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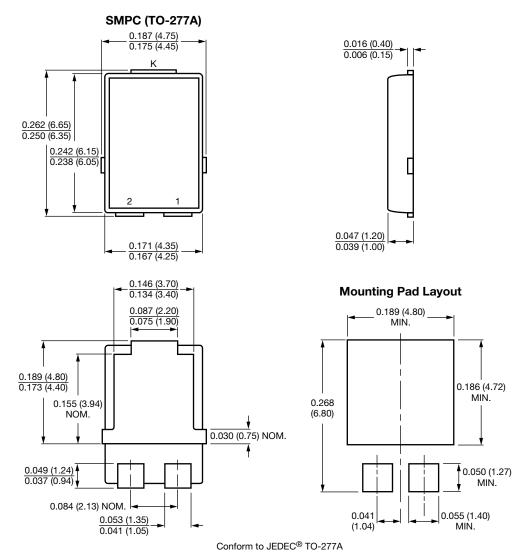
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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