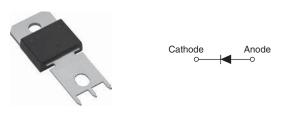


# **High Performance Schottky Rectifier, 100 A**

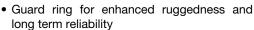


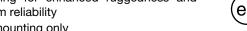
PowerTab®

PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	100 A			
$V_{R}$	15 V			
V <sub>F</sub> at I <sub>F</sub>	0.45 V			
I <sub>RM</sub>	870 mA at 100 °C			
T <sub>J</sub> max.	125 °C			
Diode variation	Single die			
E <sub>AS</sub>	9 mJ			

#### **FEATURES**

- Ultralow forward voltage drop
- · Optimized for OR-ing applications





- Screw mounting only
- AEC-Q101 qualified
- 125 °C max. operating junction temperature  $(V_R < 5 V)$
- High frequency operation
- · Continuous high current operation
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>





#### **DESCRIPTION**

The VS-100BGQ015HF4 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
	Rectangular waveform	100	A		
I <sub>F(AV)</sub>	T <sub>C</sub>	88	°C		
V <sub>RRM</sub>		15	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	5000	А		
V	100 A <sub>pk</sub> (typical)	0.39	V		
V <sub>F</sub>	TJ	125	°C		
T <sub>J</sub>	Range	-55 to +125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VS-100BGQ015HF4	UNITS
Maximum DC reverse voltage V <sub>R</sub>	V-	T <sub>J</sub> = 100 °C	15	V
	T <sub>J</sub> = 125 °C	5	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 88 °C,	rectangular waveform	100	Α
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	5000	A
	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	1000	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 4.5 mH		9	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 3 x V <sub>R</sub> typical		Α	



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
E	(1)	50 A	T <sub>J</sub> = 25 °C	0.36	0.4	- V
		100 A		0.45	0.52	
Forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	50 A	- T <sub>J</sub> = 125 °C	0.27	0.31	
		100 A		0.39	0.45	
	. (1)	T <sub>J</sub> = 100 °C, V <sub>R</sub> = 12 V		480	700	mA
		$T_J = 125 ^{\circ}\text{C},  V_R = 5 ^{\circ}\text{V}$		1	1.2	Α
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	7	18	A
		T <sub>J</sub> = 100 °C		580	870	mA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		38	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		000	V/µs	

### Note

 $^{(1)}\,$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	e T <sub>J</sub>		-55 to +125	°C	
Maximum storage temperature range	T <sub>Stg</sub>		-55 to +150	C	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	0.50	°C/W	
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.30	C/VV	
Approximate weight			5	g	
Approximate weight			0.18	OZ.	
Mounting torque minimum	ı		1.2 (10)	N · m	
Mounting torque maximum	1		2.4 (20)	(lbf $\cdot$ in)	
Marking device		Case style PowerTab®	100BG	Q015H	



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# Vishay Semiconductors

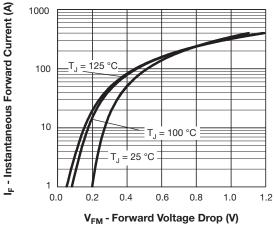


Fig. 1 - Maximum Forward Voltage Drop Characteristics

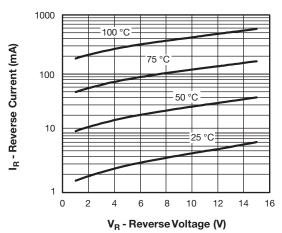


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

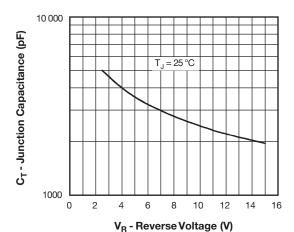


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

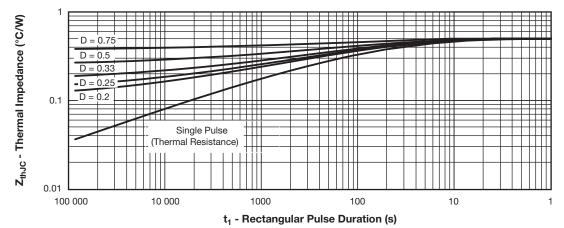


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

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### Vishay Semiconductors

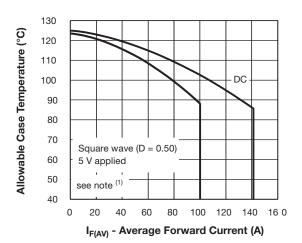
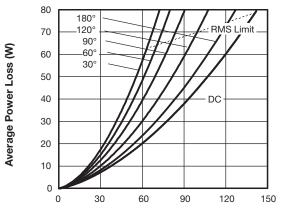


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics

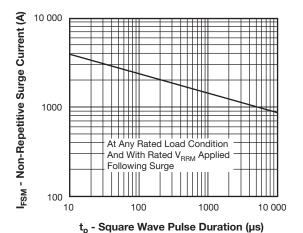


Fig. 7 - Maximum Non-Repetitive Surge Current

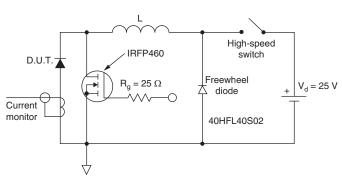


Fig. 8 - Unclamped Inductive Test Circuit

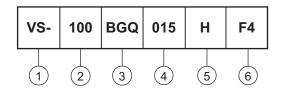
#### Note

 $^{(1)}$  Formula used:  $T_C = T_J$  - (Pd + Pd\_{REV}) x R<sub>thJC</sub>; Pd = Forward power loss =  $I_{F(AV)}$  x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd\_{REV} = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 5 V



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

Current rating (100 = 100 A)

Essential part number

Voltage rating (015 = 15 V)

5 - H = AEC-Q101 qualified

6 - Environmental digit:

- F4 = RoHS compliant and totally lead (Pb)-free

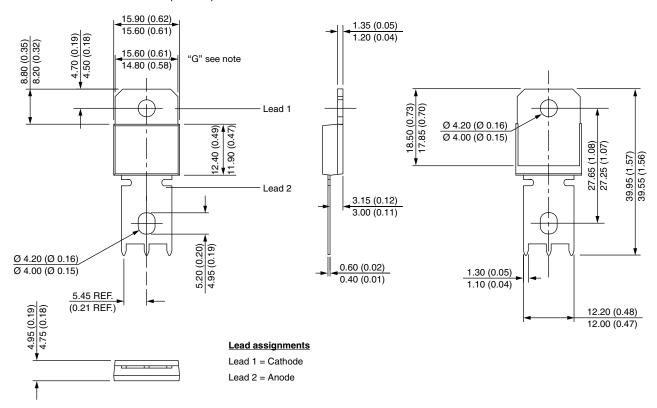
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-100BGQ015HF4	25	375	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95240		
Part marking information	www.vishay.com/doc?95467		
SPICE model	www.vishay.com/doc?95428		
Application note	www.vishay.com/doc?95179		



## PowerTab®

### **DIMENSIONS** in millimeters (inches)



#### Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



## **Legal Disclaimer Notice**

Vishay

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