VBT4060C

Vishay General Semiconductor

Dual Trench MOS Barrier Schottky Rectifier

Ultra Low V_F = 0.32 V at I_F = 5.0 A



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PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _{RRM}	60 V			
I _{FSM}	240 A			
V _F at I _F = 20 A	0.48 V			
T _J max.	150 °C			
Package	D ² PAK (TO-263AB)			
Circuit configuration	Common cathode			

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation



- Meets MSL level 1, per J-STD-020, LF maximum RoHS peak of 245 °C COMPLIANT
- Not recommended for PCB bottom side wave mounting
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: D²PAK (TO-263AB) Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT4060C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	40	^	
	per diode		20	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	240	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5.0 A	T _A = 25 °C	- V _F (1)	0.43	-	V	
	I _F = 10 A			0.48	-		
	I _F = 20 A			0.53	0.62		
	$I_{F} = 5.0 \text{ A}$	T _A = 125 °C		0.32	-		
	I _F = 10 A			0.39	-		
	I _F = 20 A			0.48	0.57		
Reverse current per diode	V _B = 60 V	T _A = 25 °C T _A = 125 °C	I _R ⁽²⁾	-	6.0	mA	
	$v_{\rm R} = 00 v$			34	190		

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VBT4060C	UNIT	
Typical thermal resistance	per diode	$R_{ ext{ heta}JC}$	1.5	°C/W	
	per device		0.8	0/10	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT4060C-E3/4W	1.39	4W	50/tube	Tube	
TO-263AB	VBT4060C-E3/8W	1.39	8W	800/reel	Tape and reel	

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

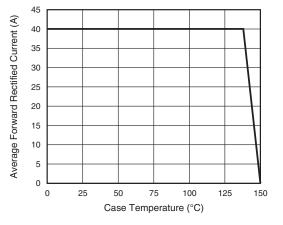


Fig. 1 - Maximum Forward Current Derating Curve

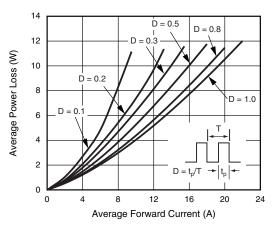
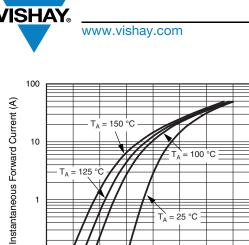


Fig. 2 - Forward Power Dissipation Characteristics Per Diode





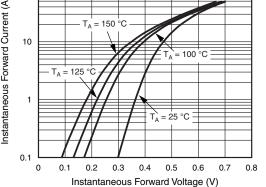


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

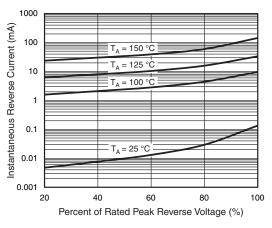


Fig. 4 - Typical Reverse Characteristics Per Diode

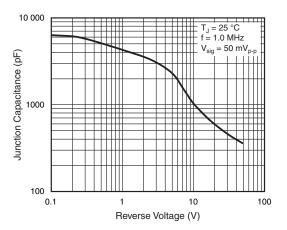


Fig. 5 - Typical Junction Capacitance Per Diode

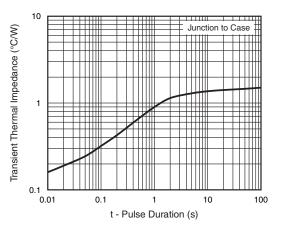
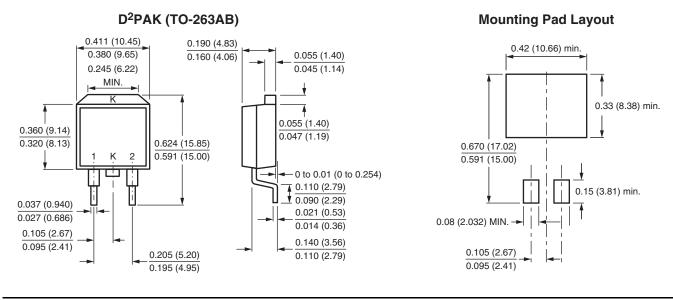


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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