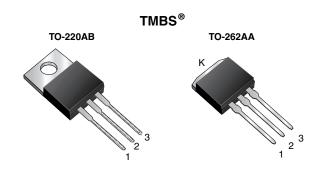
FREE



Vishay General Semiconductor

Dual Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.32 \text{ V}$ at $I_F = 5.0 \text{ A}$



VT30L60C	VIT30L60C		
PIN 1 O PIN 2 O CASE	PIN 1 O PIN 2 PIN 3 O K		
PIN 3 O CASE	PIN 3 OK		

PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _{RRM}	60 V				
I _{FSM}	240 A				
V _F at I _F = 20 A	0.48 V				
T _J max.	150 °C				
Package	TO-220AB, TO-262AA				
Diode variation	Dual common cathode				

FEATURES

Trench MOS Schottky technology

· Low forward voltage drop, low power losses

RoHS · High efficiency operation HALOGEN

Solder dip 275 °C max. 10 s, per JESD 22-B106

AEC-Q101 qualified

Automotive ordering code: base P/NHM3

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VT4060C VIT4060C		UNIT
Maximum repetitive peak reverse voltage	mum repetitive peak reverse voltage V _R		60		V
Maximum average forward rectified current (fig. 1)	per device		40		А
	per diode	I _{F(AV)}	20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	240		А
Voltage rate of change (rated V _R)		dV/dt	10 000		V/µs
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150		°C



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5.0 A	T _A = 25 °C	V _F (1)	0.43	-	V	
	I _F = 10 A			0.48	-		
	I _F = 20 A			0.53	0.62		
	I _F = 5.0 A	T _A = 125 °C		0.32	-		
	I _F = 10 A			0.39	-		
	I _F = 20 A			0.48	0.57		
Reverse current per diode	V _R = 60 V	T _A = 25 °C	I _R ⁽²⁾	-	6.0	· mA	
	$v_R = 00 \text{ V}$ T_A	T _A = 125 °C		34	190		

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VT4060C	VIT4060C	UNIT	
Typical thermal resistance	per diode	$R_{ heta JC}$	1.5		°C/W
	per device		0.8		

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	VT4060C-M3/4W	1.89	4W	50/tube	Tube		
TO-262AA	VIT4060C-M3/4W	1.46	4W	50/tube	Tube		
TO-220AB	VT4060CHM3/4W (1)	1.89	4W	50/tube	Tube		
TO-262AA	VIT4060CHM3/4W (1)	1.46	4W	50/tube	Tube		

Note

(1) AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

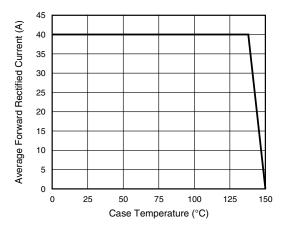


Fig. 1 - Maximum Forward Current Derating Curve

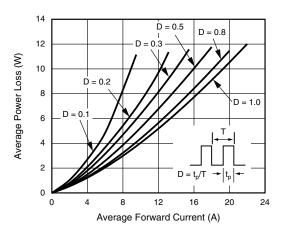


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

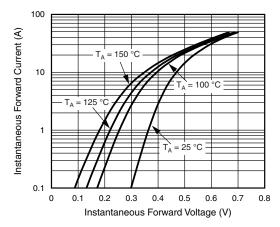


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

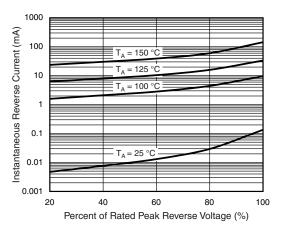


Fig. 4 - Typical Reverse Characteristics Per Diode

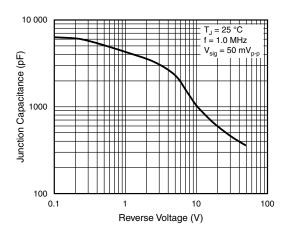


Fig. 5 - Typical Junction Capacitance Per Diode

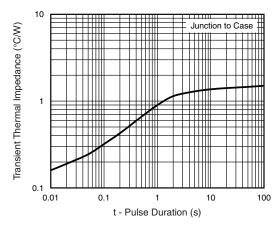
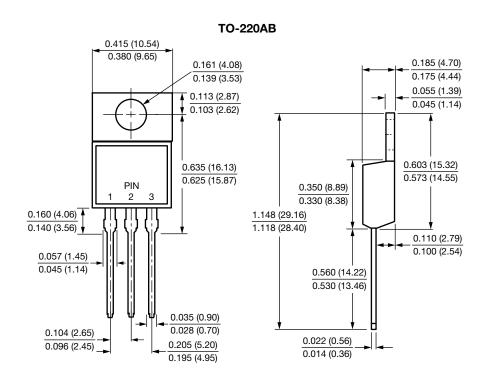


Fig. 6 - Typical Transient Thermal Impedance Per Diode

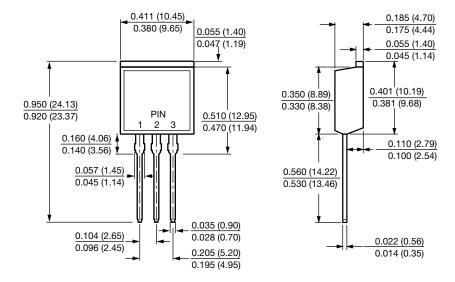


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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



TO-262AA





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