



PJD16P06A

60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

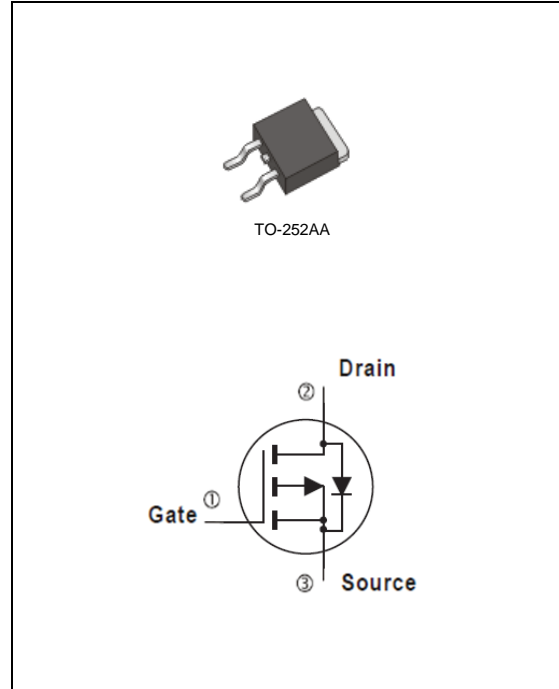
-16 A

Features

- $R_{DS(ON)}, V_{GS}@-10V, I_D@-8A < 48m\Omega$
- $R_{DS(ON)}, V_{GS}@-4.5V, I_D@-4A < 65m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- TO-252AA Approx. Weight : 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (Note 4)	$T_C=25^\circ\text{C}$	I_D	-16	A
	$T_C=100^\circ\text{C}$		-10	
Pulsed Drain Current (Note 1)	$T_C=25^\circ\text{C}$	I_{DM}	-64	
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	25	W
	$T_C=100^\circ\text{C}$		10	
Continuous Drain Current (Note 4)	$T_A=25^\circ\text{C}$	I_D	-5	A
	$T_A=70^\circ\text{C}$		-4	
Power Dissipation	$T_A=25^\circ\text{C}$	P_D	2	W
	$T_A=70^\circ\text{C}$		1.3	
Single Pulse Avalanche Energy (Note 6)		E_{AS}	51	mJ
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	5	$^\circ\text{C/W}$
	Junction to Ambient	$R_{\theta JA}$	62.5	

- Limited only By Maximum Junction Temperature



PJD16P06A

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.7	-2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-8A$	-	40	48	m Ω
		$V_{GS}=-4.5V, I_D=-4A$	-	55	65	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-60V, V_{GS}=0V$	-	-	-1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	$V_{DS}=-30V, I_D=-8A,$ $V_{GS}=-10V$ (Note 2,3)	-	22	-	nC
Gate-Source Charge	Q_{gs}		-	4.1	-	
Gate-Drain Charge	Q_{gd}		-	5.2	-	
Input Capacitance	C_{iss}	$V_{DS}=-30V, V_{GS}=0V,$ $f=1\text{MHZ}$	-	1256	-	pF
Output Capacitance	C_{oss}		-	87	-	
Reverse Transfer Capacitance	C_{rss}		-	59	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=-30V, I_D=-1A,$ $V_{GS}=-10V, R_G=6\Omega$ (Note 2,3)	-	13	-	ns
Turn-On Rise Time	t_r		-	42	-	
Turn-Off Delay Time	$t_{d(off)}$		-	65	-	
Turn-Off Fall Time	t_f		-	16	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	-16	A
Reverse Recovery Time	V_{SD}	$I_S=-1A, V_{GS}=0V$	-	-0.72	-1	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ\text{C}$.
4. The maximum current rating is package limited.
5. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. $L=0.1\text{mH}, I_{AS}=-32A, V_{GS}=-10V, V_{DS}=-25V, R_G=25\text{ ohm}$.
7. Guaranteed by design, not subject to production testing.



PJD16P06A

TYPICAL CHARACTERISTIC CURVES

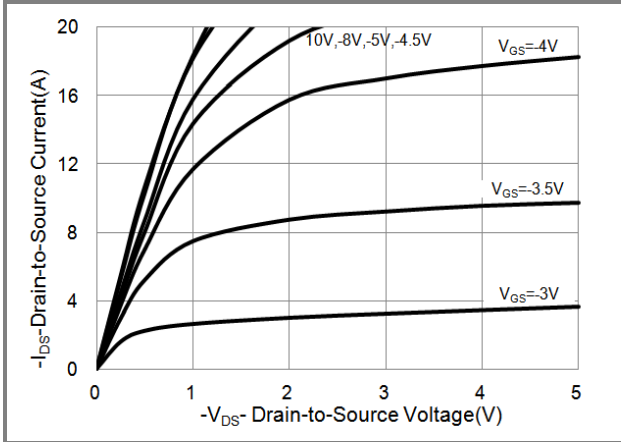


Fig.1 On-Region Characteristics

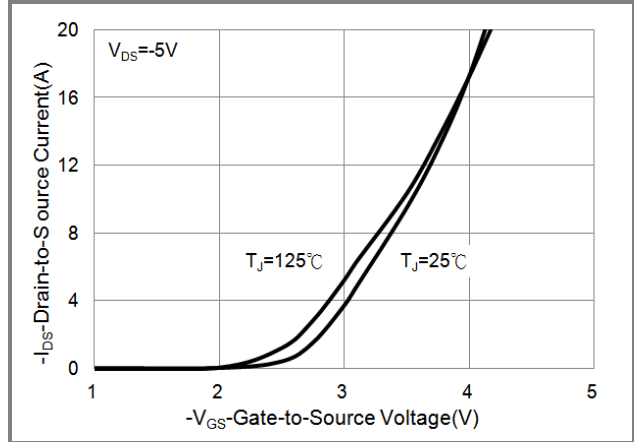


Fig.2 Transfer Characteristics

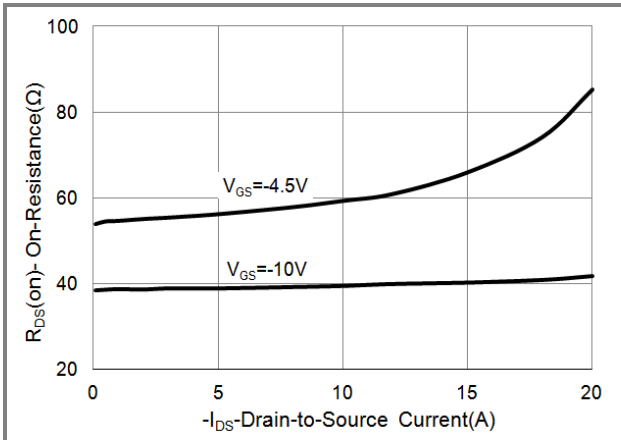


Fig.3 On-Resistance vs. Drain Current

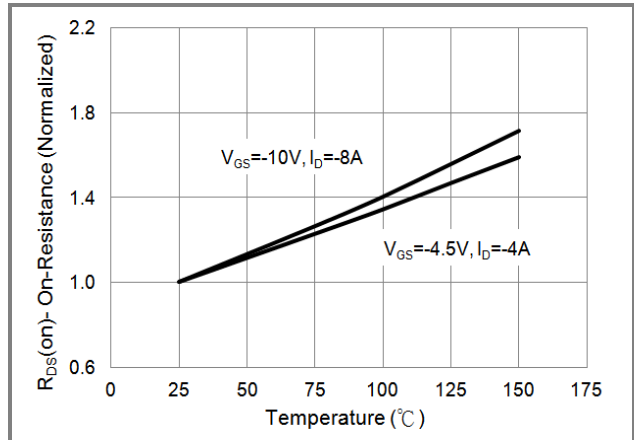


Fig.4 On-Resistance vs. Junction temperature

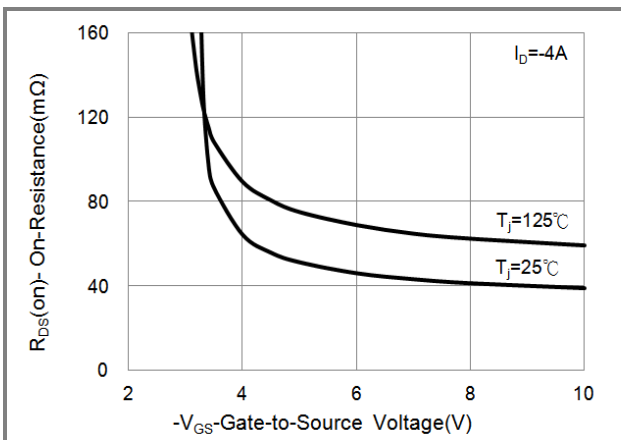


Fig.5 On-Resistance Variation with V_{GS}

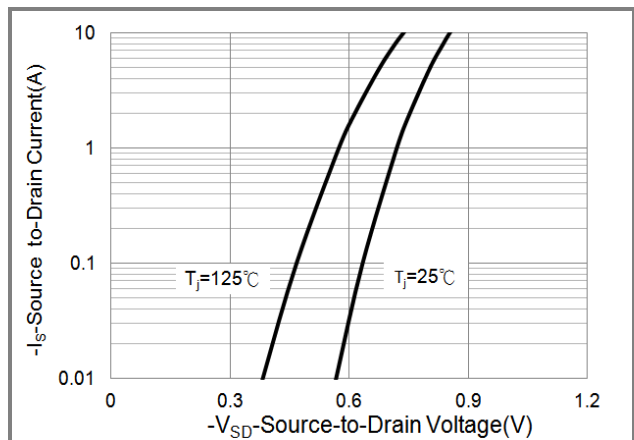


Fig.6 Body Diode Characteristics



PJD16P06A

TYPICAL CHARACTERISTIC CURVES

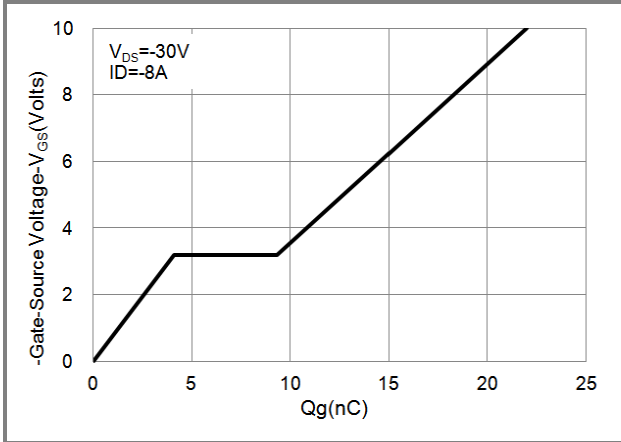


Fig.7 Gate-Charge Characteristics

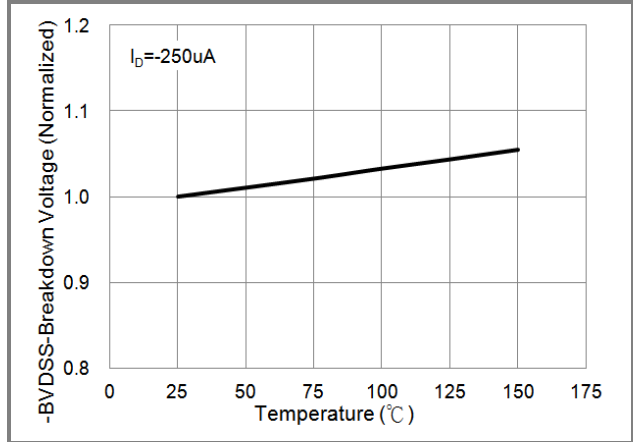


Fig.8 Breakdown Voltage Variation vs. Temperature

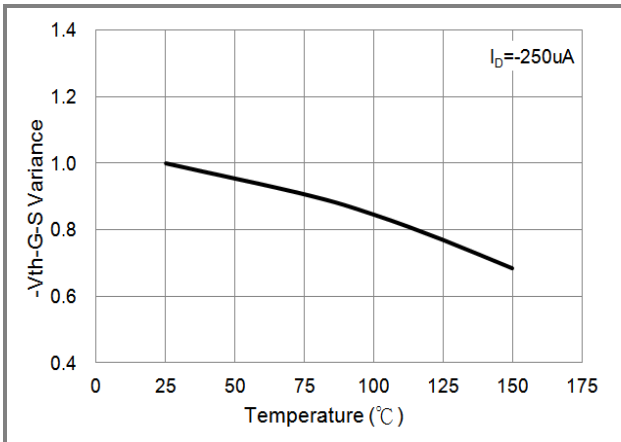


Fig.9 Threshold Voltage Variation with Temperature

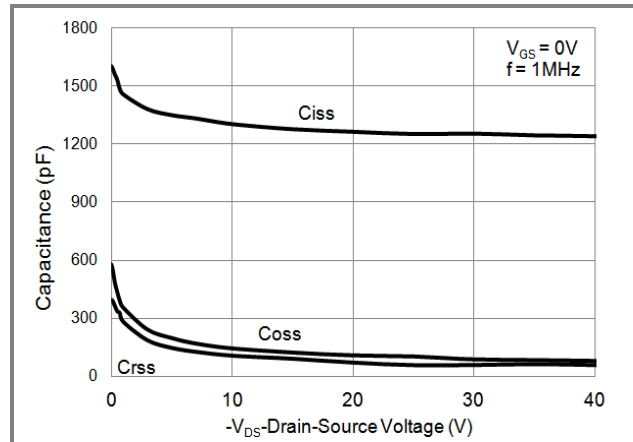


Fig.10 Capacitance vs. Drain-Source Voltage

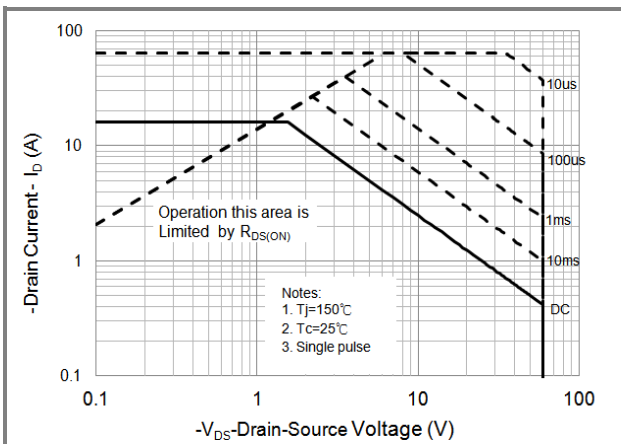


Fig.11 Maximum Safe Operating Area



PJD16P06A

TYPICAL CHARACTERISTIC CURVES

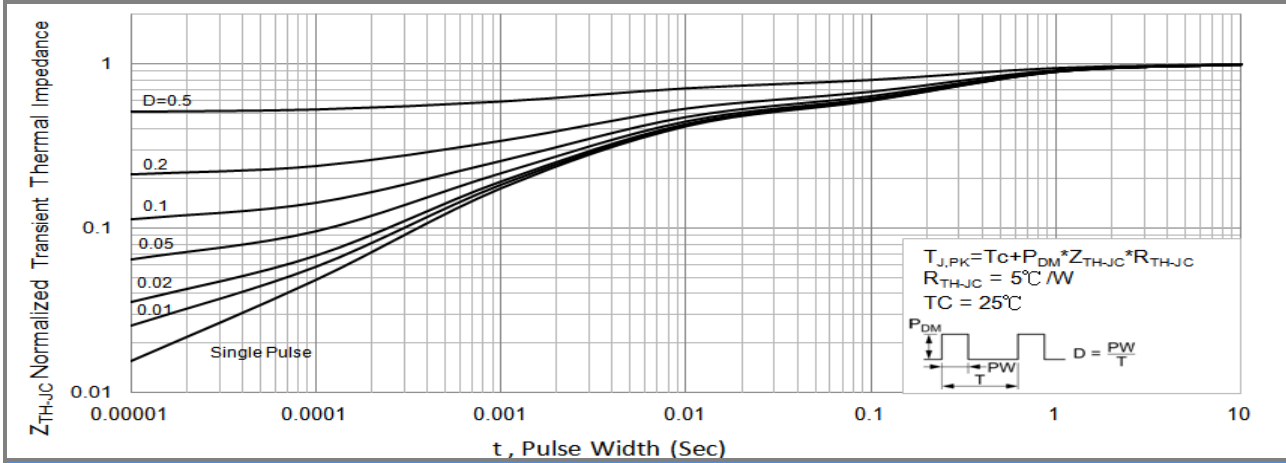


Fig.12 Normalized Thermal Transient Impedance

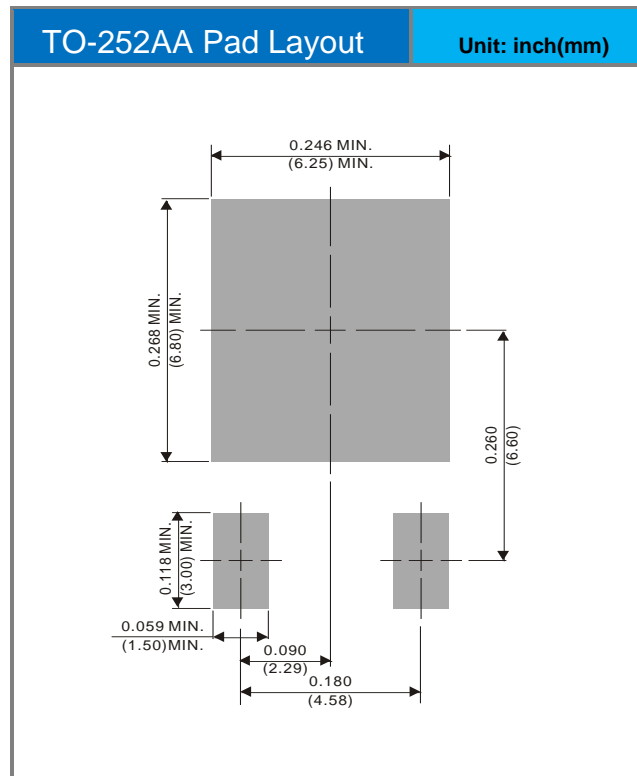
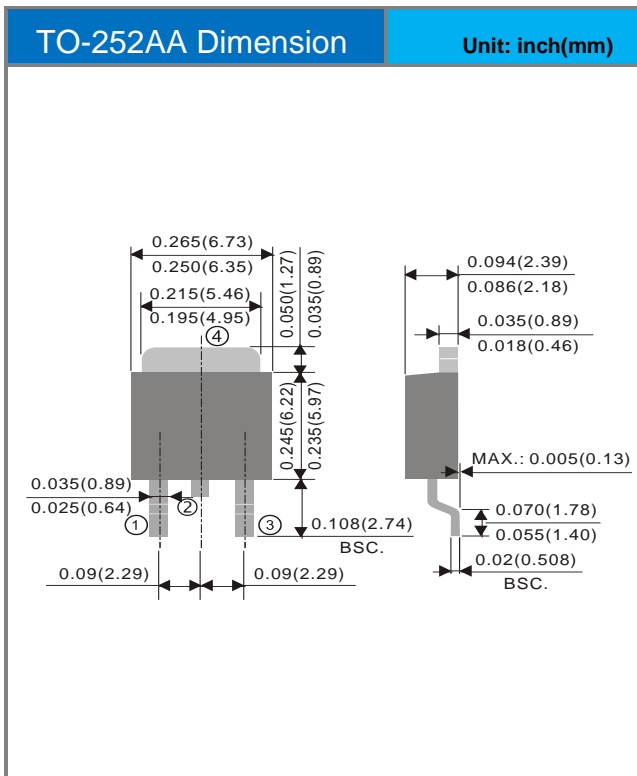


PJD16P06A

Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJD16P06A_L2_00001	TO-252AA	3,000pcs / 13" reel	D16P06A	Halogen free

Packaging Information & Mounting Pad Layout





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