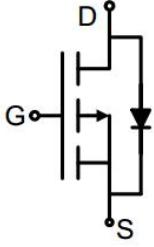
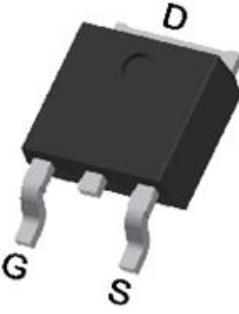


## P-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GT750P08K uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> -80V</li> <li>● <math>I_D</math> (at <math>V_{GS} = -10V</math>) -28A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = -10V</math>) &lt; 70mΩ</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = -4.5V</math>) &lt; 80mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TO-252</p>
---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------	----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------

<b>Ordering Information</b>			
<b>Device</b>	<b>Package</b>	<b>Marking</b>	<b>Packaging</b>
GT750P08K	TO-252	GT750P08	2500pcs/Reel

<b>Absolute Maximum Ratings</b> $T_C = 25^\circ C$ , unless otherwise noted			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Drain-Source Voltage	$V_{DS}$	-80	V
Continuous Drain Current	$I_D$	-28	A
Pulsed Drain Current (note1)	$I_{DM}$	-112	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	80	W
Single pulse avalanche energy (note2)	$E_{AS}$	100	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

<b>Thermal Resistance</b>			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	50	°C/W
Maximum Junction-to-Case	$R_{thJC}$	1.56	°C/W

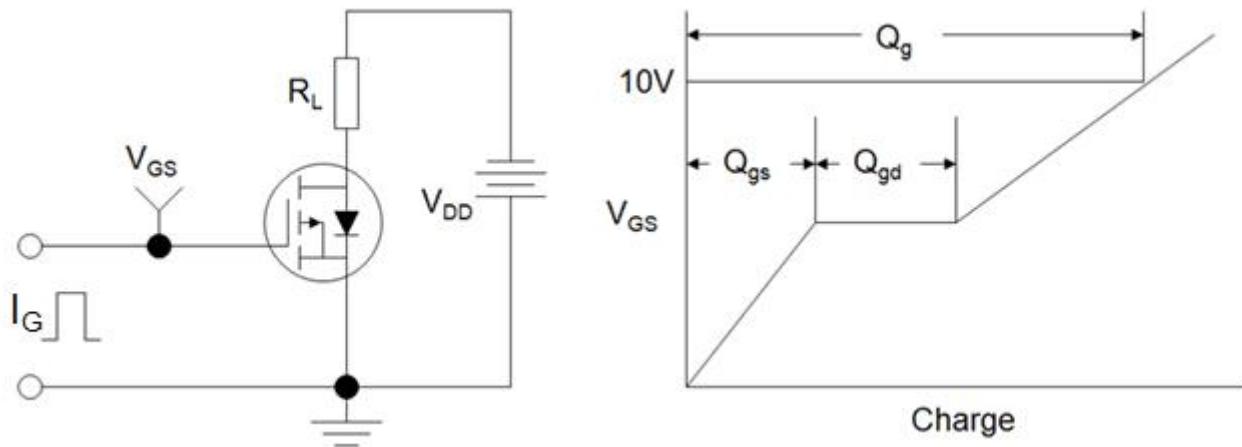
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$	-80	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = -80\text{V}, V_{GS} = 0\text{V}$	--	--	-1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{GS} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.0	-2.0	-3.0	V
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = -10\text{V}, I_D = -5\text{A}$	--	52	70	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}, I_D = -5\text{A}$	--	65	80	
Forward Transconductance	$g_{\text{FS}}$	$V_{DS} = -5\text{V}, I_D = -5\text{A}$	--	12	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{GS} = 0\text{V}, V_{DS} = -40\text{V}, f = 1.0\text{MHz}$	--	1981	--	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		--	169	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	23	--	
Total Gate Charge	$Q_g$	$V_{DD} = -40\text{V}, I_D = -5\text{A}, V_{GS} = -10\text{V}$	--	36	--	$\text{nC}$
Gate-Source Charge	$Q_{gs}$		--	4	--	
Gate-Drain Charge	$Q_{gd}$		--	8	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{DD} = -40\text{V}, I_D = -5\text{A}, R_G = 3\Omega$	--	13	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	17	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	40	--	
Turn-off Fall Time	$t_f$		--	15	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	-28	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = -5\text{A}, V_{GS} = 0\text{V}$	--	--	-1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = -5\text{A}, V_{GS} = 0\text{V}$ $dI/dt = -100\text{A}/\mu\text{s}$	--	45	--	$\text{nC}$
Reverse Recovery Time	$T_{rr}$		--	41	--	ns

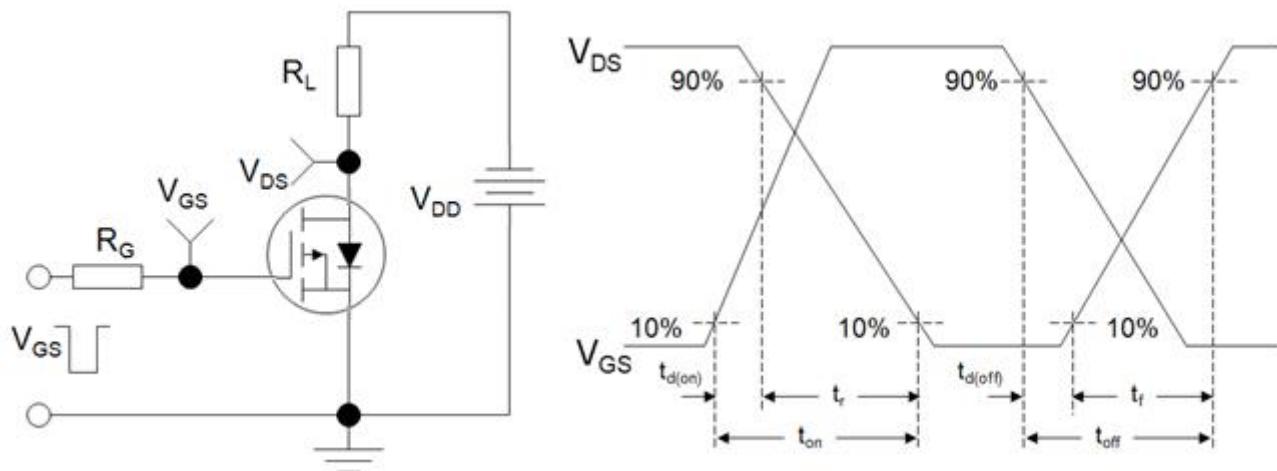
**Notes**

- Repetitive Rating: Pulse width limited by maximum junction temperature
- EAS condition :  $T_J=25^\circ\text{C}$ ,  $VDD=-50\text{V}$ ,  $VGS=-10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$
- Identical low side and high side switch with identical  $R_G$

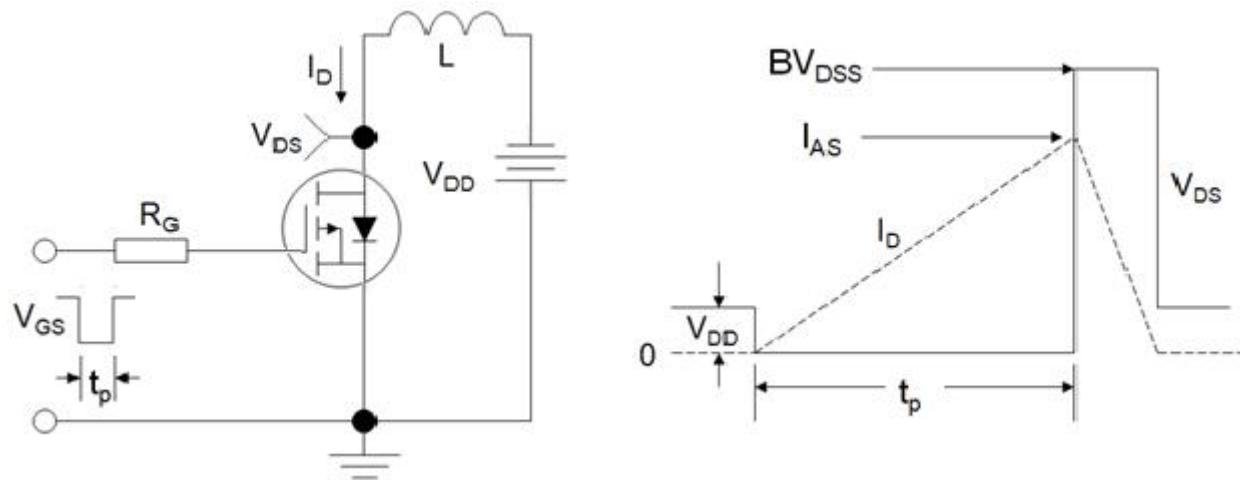
Gate Charge Test Circuit



Switch Time Test Circuit

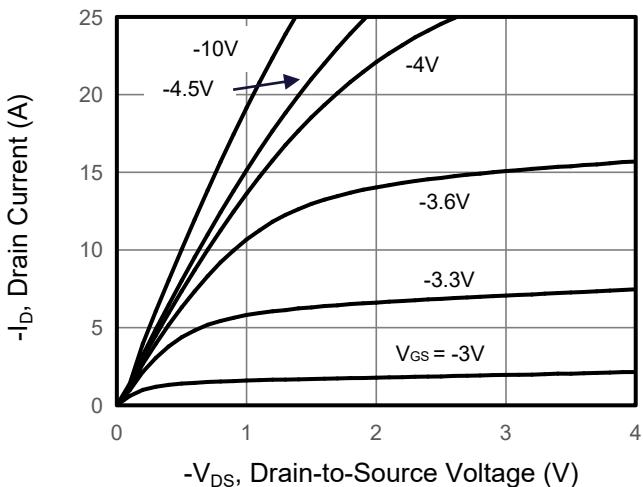


EAS Test Circuit

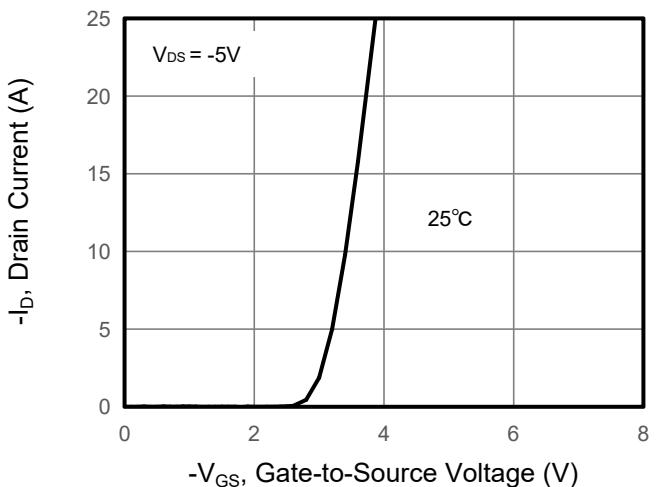


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

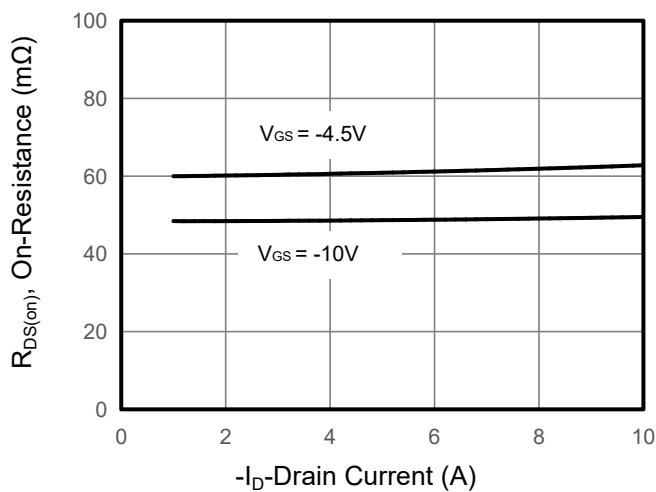
**Figure 1. Output Characteristics**



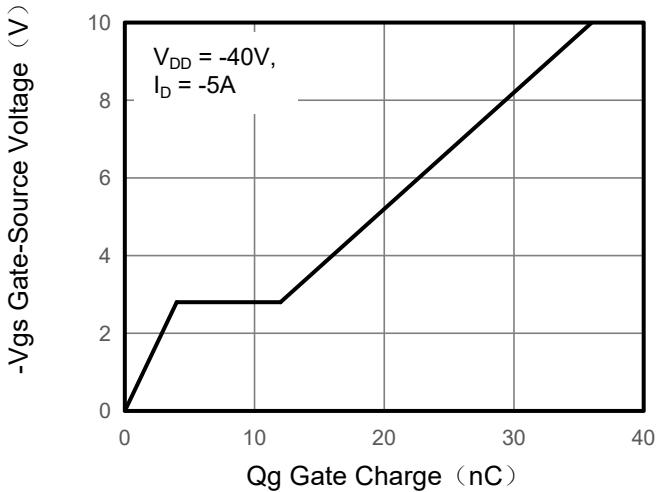
**Figure 2. Transfer Characteristics**



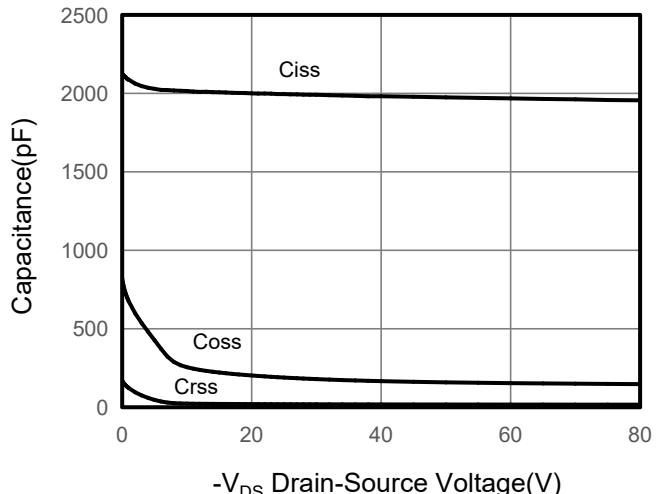
**Figure 3. Drain Source On Resistance**



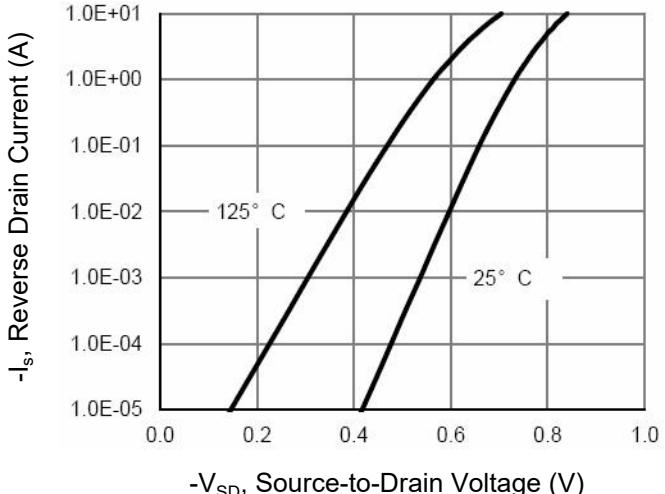
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

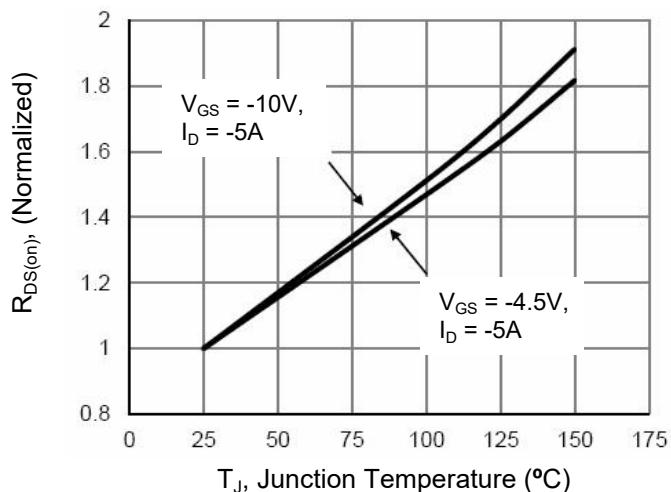


**Figure 6. Source-Drain Diode Forward**

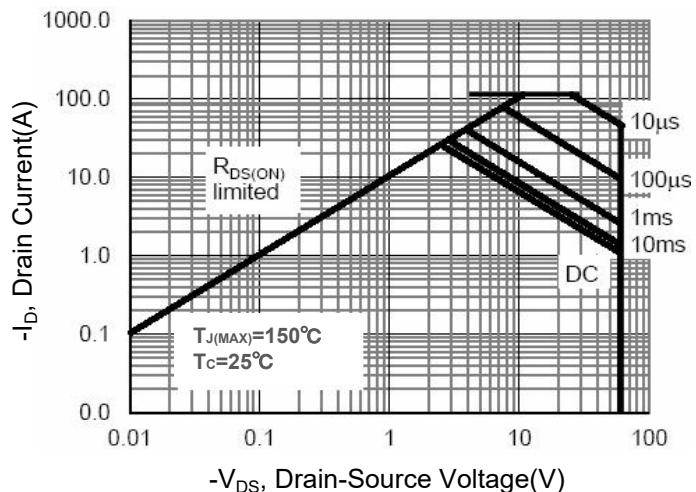


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

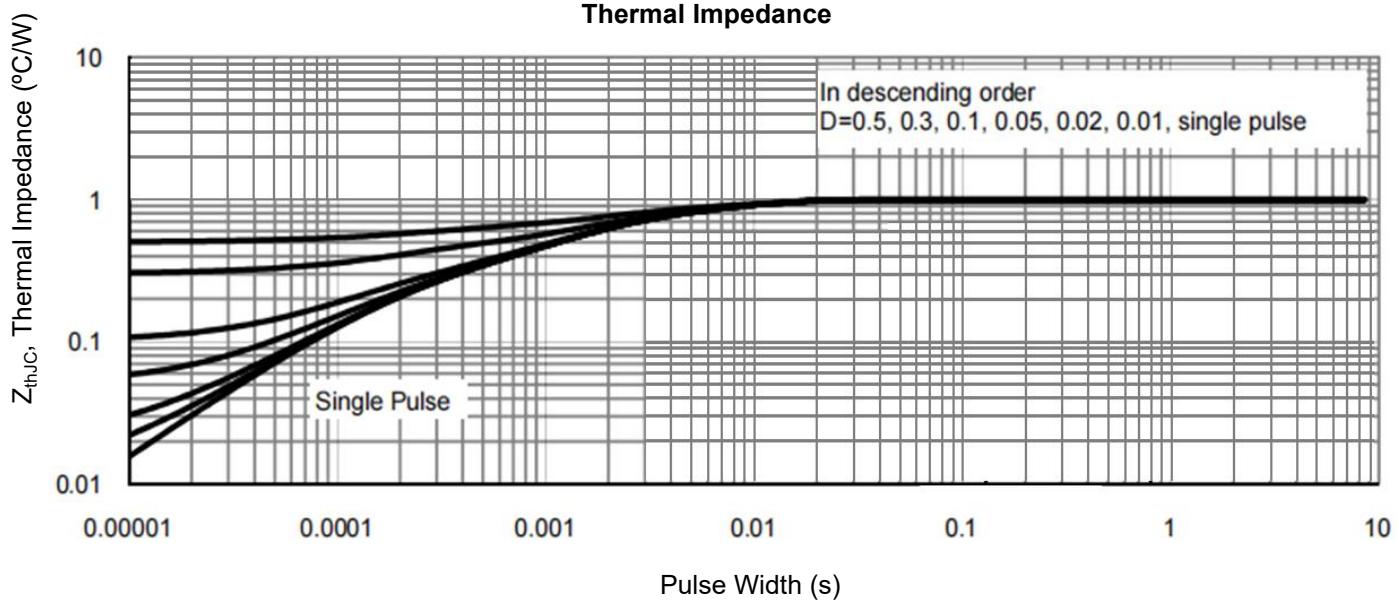
**Figure 7. Drain-Source On-Resistance**



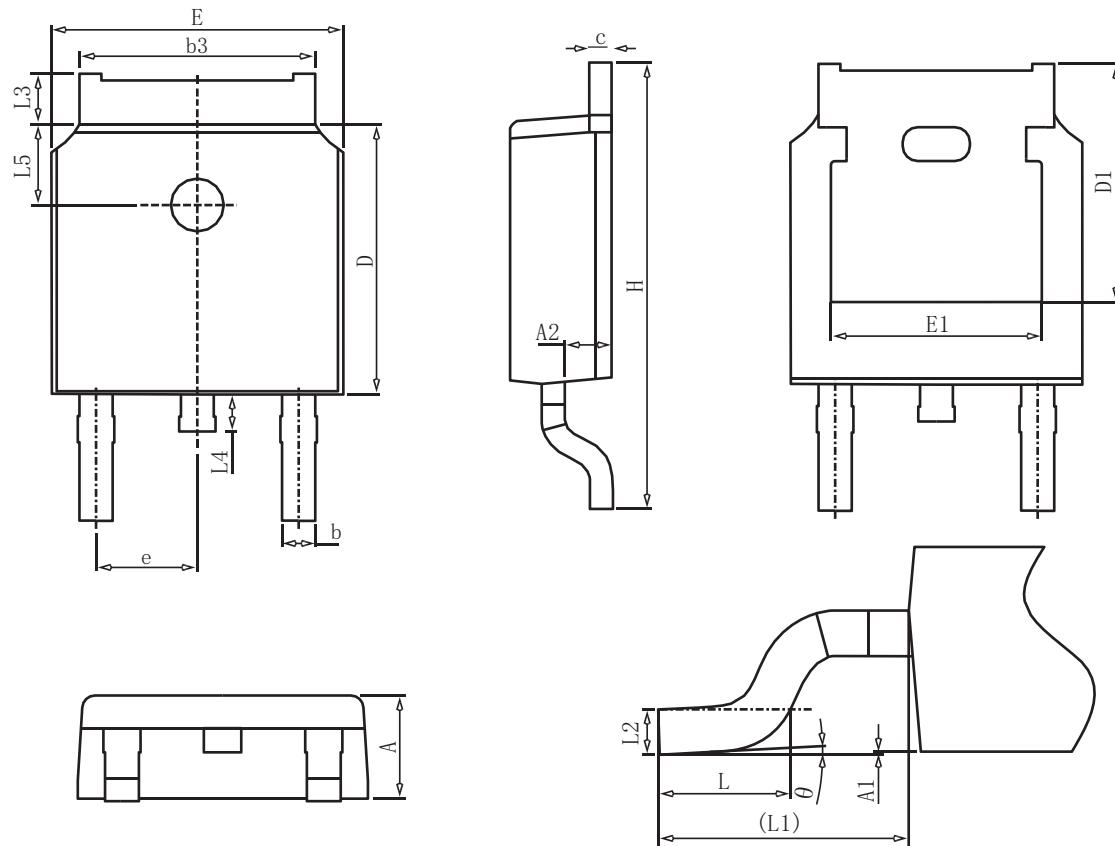
**Figure 10. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-252 Package Information



## COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°