

## N-Channel Enhancement Mode Power MOSFET

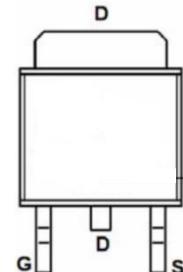
### Description

This Power MOSFET is produced using advanced TRENCH technology.

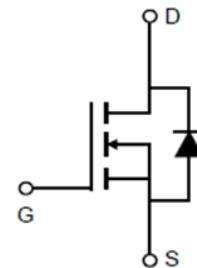
This advanced technology has been especially tailored to minimize conduction loss, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

### Features

- $V_{DS}=30V$ ,  $I_D=120A$
- $R_{DS\text{ (ON)}}\text{ TYP} = 2.6m\Omega$  @  $V_{GS}=10V$
- $R_{DS\text{ (ON)}}\text{ TYP} = 3.6m\Omega$  @  $V_{GS}=4.5V$
- Very Low On-resistance RDS(ON)
- Low Crss
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-252-2L top view



Schematic diagram

### Applications

- PWM Application
- Load Switch
- Power Management

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source Voltage	V <sub>DS</sub>	30	V
Gate-source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	120	A
T <sub>C</sub> =100°C		78	
Pulsed Drain Current(T <sub>C</sub> =25°C, T <sub>p</sub> Limited By T <sub>jmax</sub> ) <sup>(note1)</sup>	I <sub>DM</sub>	480	A
Maximum Power Dissipation(T <sub>C</sub> =25°C)	P <sub>D</sub>	88	W
Avalanche energy , single Pulse(L=0.5mH) <sup>(note2)</sup>	E <sub>AS</sub>	342	mJ
Operating Junction And Storage Temperature	T <sub>j</sub> , T <sub>stg</sub>	-55 To 150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T <sub>L</sub>	300	°C

## Thermal Resistance

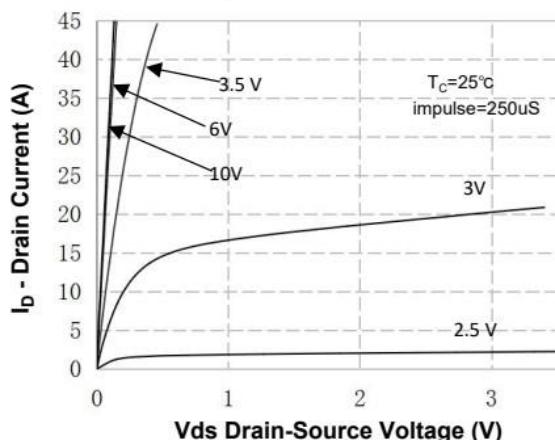
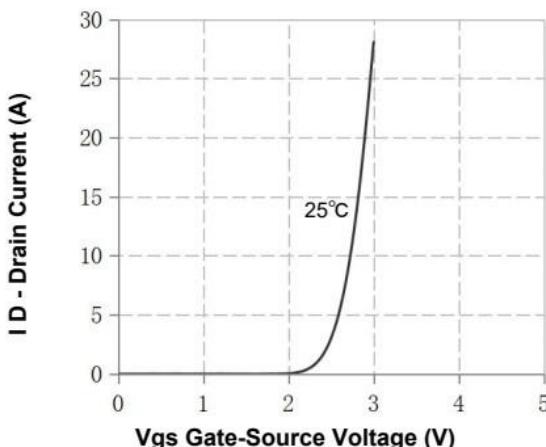
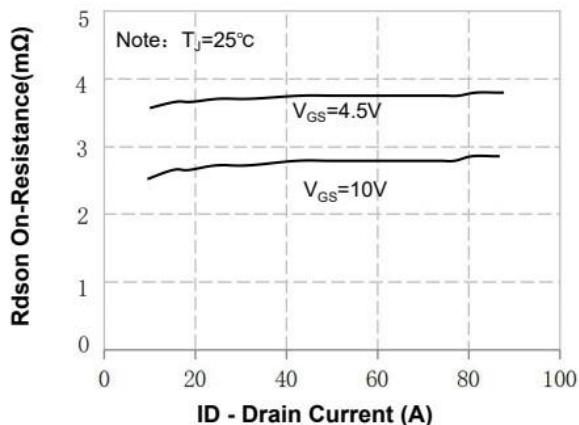
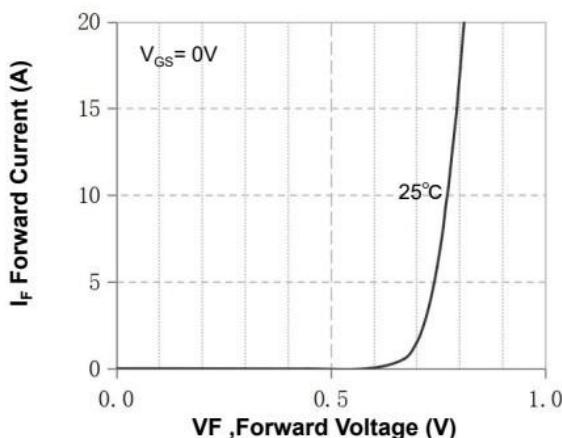
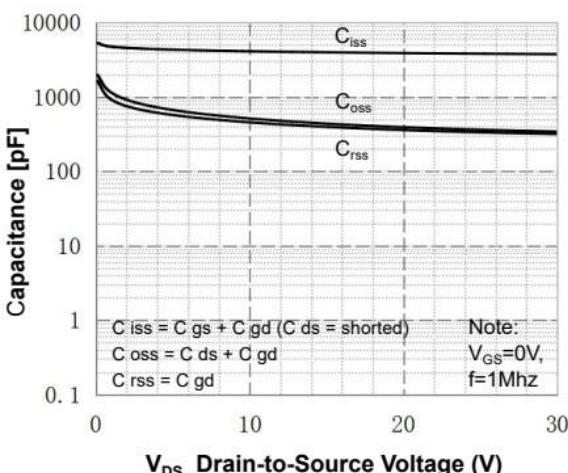
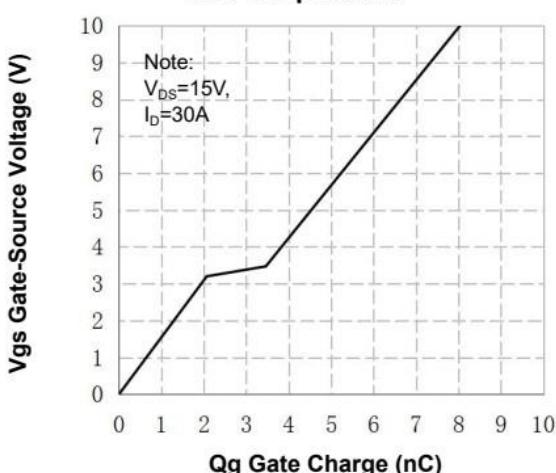
Parameter	Symbol	Max	Unit
Junction-to-Case	R <sub>θJC</sub>	1.4	°C/W

Note:

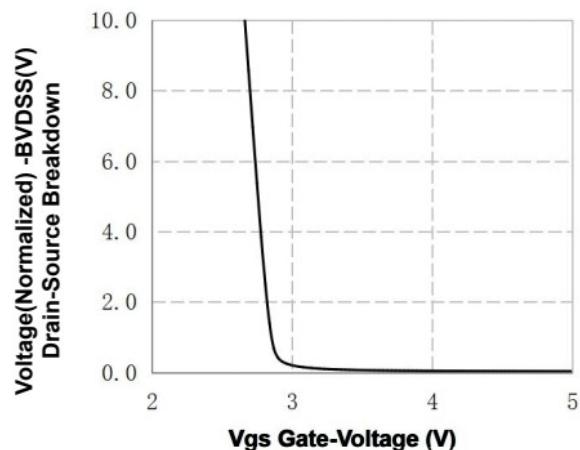
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: T<sub>J</sub> =25°C, V<sub>DD</sub> =30V, V<sub>G</sub> =10V, R<sub>G</sub> =25Ω, L=0.5mH, I<sub>AS</sub>=25A
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%

## Electrical Characteristic (TC=25°C unless otherwise noted)

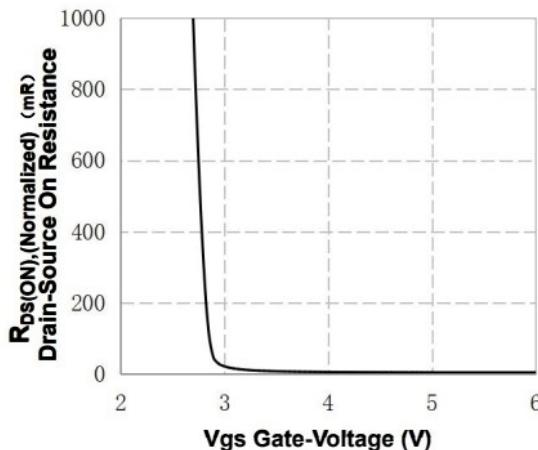
Parameter	Symbol	Value			Unit	Test Condition
		Min.	Typ.	Max.		
<b>Off Characteristic</b>						
Drain-source breakdown voltage	BV <sub>DSS</sub>	30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
Zero gate voltage drain current	I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V
Gate-source leakage current	I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
<b>On Characteristics</b>						
Gate threshold voltage	V <sub>GS(th)</sub>	1.0	1.5	2.2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
Drain-source on-state resistance	R <sub>DS(on)</sub>	-	2.6	3.5	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =20A
		-	3.6	4.8	mΩ	V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A
<b>Dynamic Characteristic</b>						
Input Capacitance	C <sub>iss</sub>	-	4000	-	PF	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1.0MHz
Output Capacitance	C <sub>oss</sub>	-	437	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	396	-		
<b>Switching Characteristics</b>						
Turn-on delay time	t <sub>d(on)</sub>	-	14	-	nS	V <sub>DS</sub> =15V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V R <sub>L</sub> =3Ω, T <sub>j</sub> =25°C
Turn-on Rise time	t <sub>r</sub>	-	18	-		
Turn-off delay time	t <sub>d(off)</sub>	-	40	-		
Turn-off Fall time	t <sub>f</sub>	-	12	-		
Gate Total Charge	Q <sub>G</sub>	-	72	-	nC	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, I <sub>D</sub> =30A
Gate-Source Charge	Q <sub>gs</sub>	-	46	-		
Gate-Drain Charge	Q <sub>gd</sub>	-	13	-		
<b>Drain-Source Diode Characteristics</b>						
Body Diode Forward Voltage	V <sub>SD</sub>	-	-	1.2	V	V <sub>GS</sub> =0V, I <sub>SD</sub> =30A, T <sub>j</sub> =25°C
Body Diode Forward Current	I <sub>s</sub>	-	-	120	A	-
Body Diode Reverse Recovery Time	T <sub>rr</sub>	-	48	-	ns	T <sub>j</sub> =25°C, I <sub>SD</sub> =30A, V <sub>GS</sub> =0V, d <sub>i</sub> /d <sub>t</sub> =100A/μs
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	-	80	-	nC	

**N- Channel Typical Characteristics****Figure 1. On-Region Characteristics****Figure 2. Transfer Characteristics****Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage****Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature****Figure 5. Capacitance Characteristics****Figure 6. Gate Charge Characteristics**

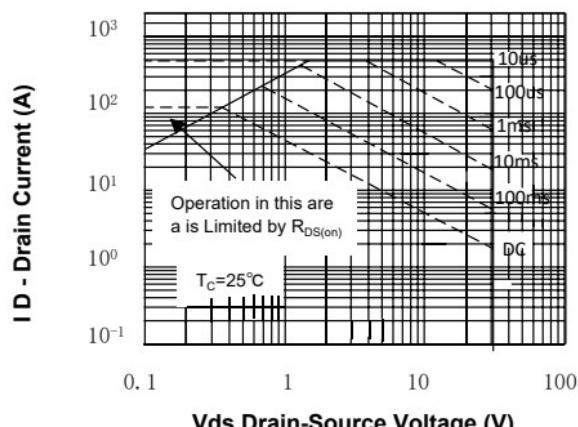
### N- Channel Typical Characteristics (Continued)



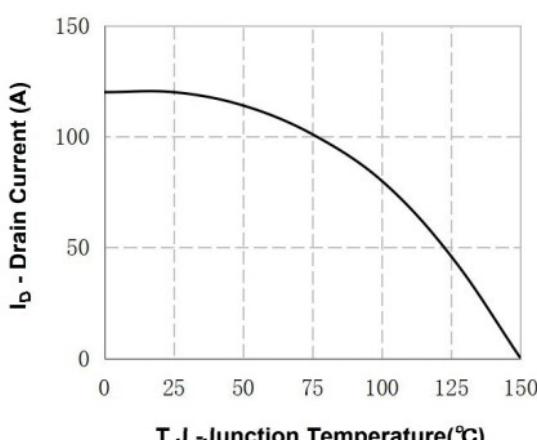
**Figure 7.** Breakdown Voltage Variation vs Gate-Voltage



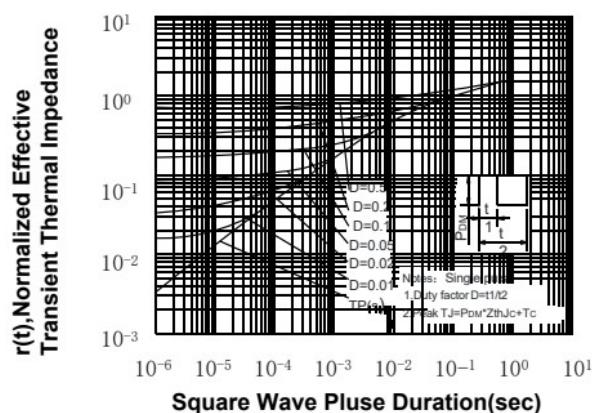
**Figure 8.** On-Resistance Variation vs Gate Voltage



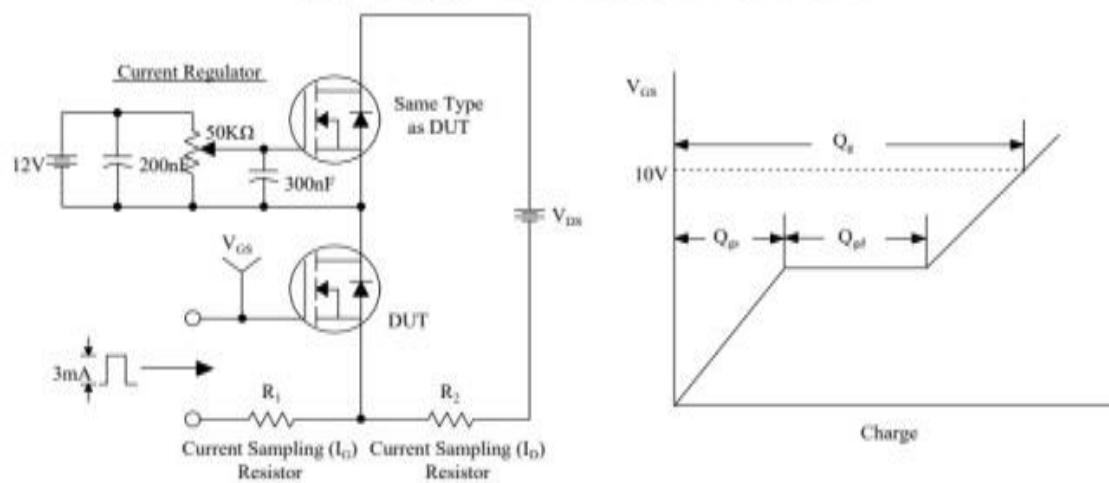
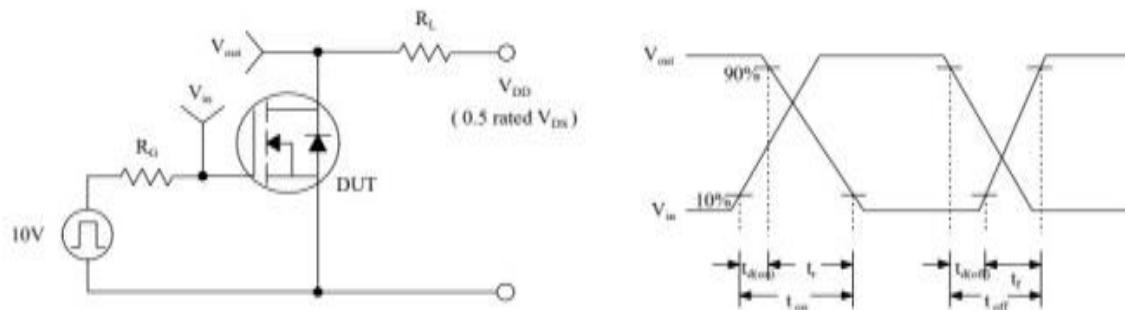
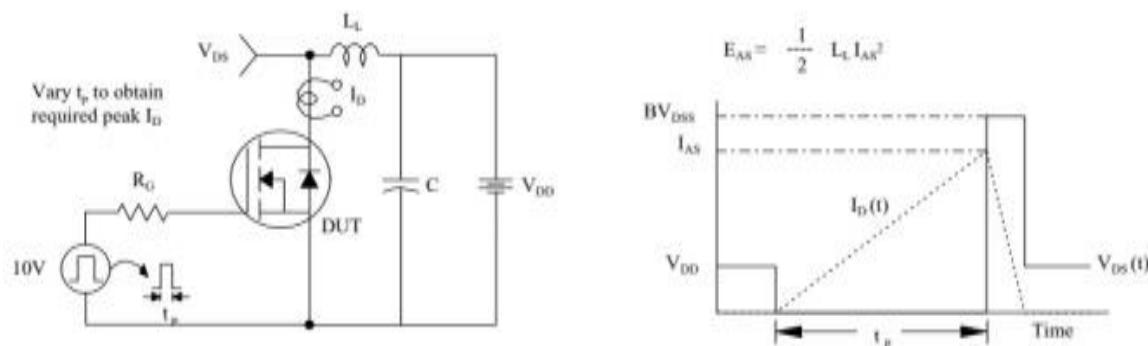
**Figure 9.** Maximum Safe Operating Area

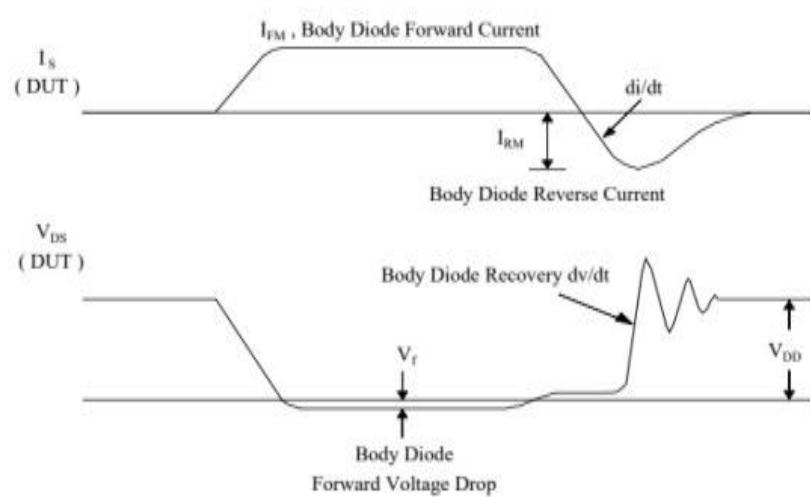
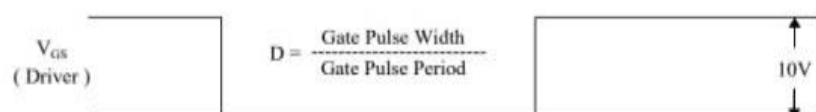
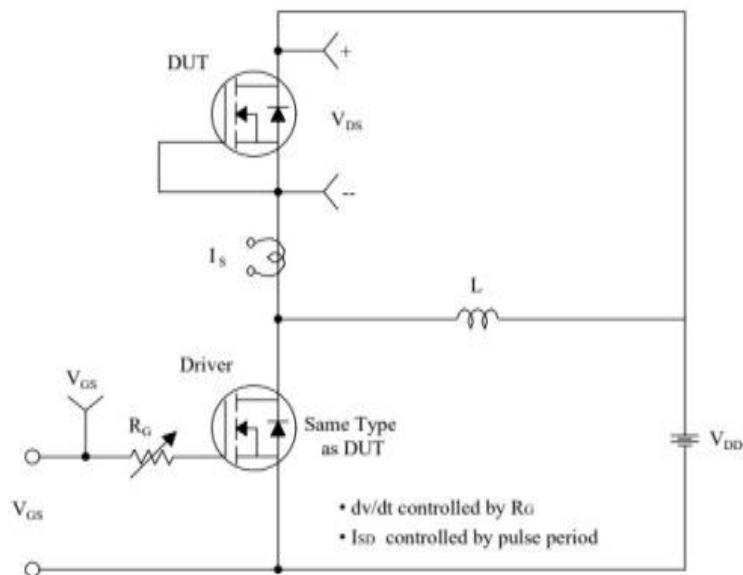


**Figure 10.** Maximum Continuous Drain Current vs Case Temperature



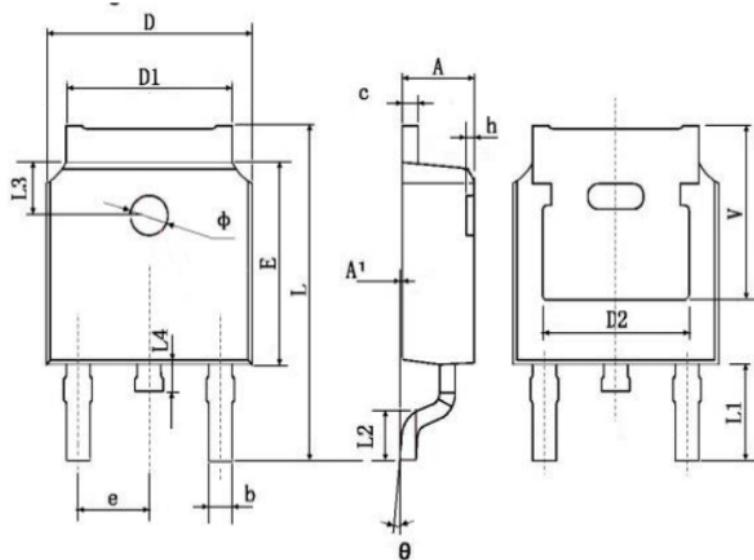
**Figure 11.** Transient Thermal Response Curve

**Gate Charge Test Circuit & Waveform****Resistive Switching Test Circuit & Waveforms****Unclamped Inductive Switching Test Circuit & Waveforms**

**Peak Diode Recovery dv/dt Test Circuit & Waveforms**

## Package Information

TO-252 DPAK



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.250	2.350	0.089	0.093
A1	0.050	0.150	0.002	0.006
b	0.660	0.860	0.026	0.034
c	0.458	0.558	0.018	0.022
D	6.550	6.650	0.259	0.263
D1	5.234	5.434	0.207	0.215
D2	4.826 TYP.		0.191 TYP.	
E	6.050	6.150	0.239	0.243
e	2.236	2.336	0.088	0.092
L	9.820	10.220	0.388	0.404
L1	3.000 TYP.		0.119 TYP.	
L2	1.400	1.600	0.055	0.063
L3	1.800 TYP.		0.071 TYP.	
L4	0.700	0.900	0.028	0.036
Φ	1.150	1.250	0.045	0.049
θ	0°	3°	0°	3°
h	0.000	0.300	0.000	0.012
V	5.399 TYP		0.213 TYP	