



深圳市汉昇实业有限公司

SHENZHEN HANSHENG INDUSTRIAL CO.LTD.,

HS32B01A

规格书

DA TASHHEET

汉昇 HS	制作	审核	批准

版本: VER 1.0	5.4' TFT 240*320(RGB)

深圳市汉昇实业有限公司

地址: 深圳市南山区西丽镇牛成路 208 栋亿莱工业大厦 5 楼

电话: 0755-86114312/86114313

传真: 0755-86114314

网址: www.hsicm.com

Revised History

Part Number	Revision	Revision Content	Revised on
J U54D23C	A	New	2042-28-35

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1. General Description

J U54D23C is a 240RGB*320 dots matrix TFT LCD module. It has a TFT panel composed of 720sources and 320gates. The LCM can be easily accessed by micro-controller.

2. Features

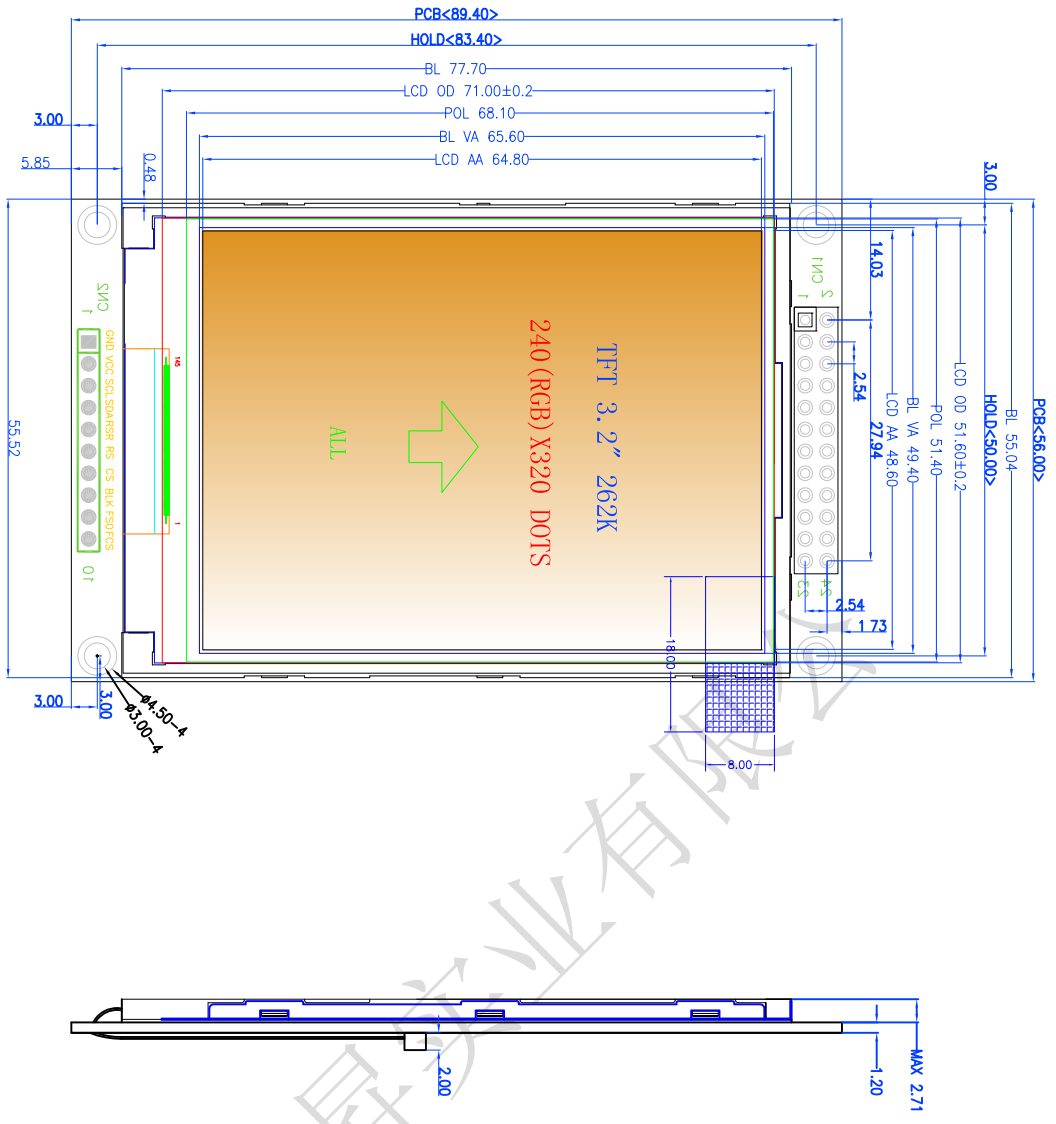
Display Mode	Transmissive
	a-TFT
Display Format	Graphic 240RGB*320 Dot-matrix
Input Data	16bits parallel interface
Viewing Direction	12 o'clock
Drive	ST7789

3. Mechanical Specification

Item	Specifications	Unit
Dimensional outline	58.00(W)*8;.60(H)*7.: 5MAX	mm
Resolution	240RGB*320	dots
LCD Active area	4;.60(W)*870(H)	mm
Pixel size	0.40(W)*0.40(H+)	mm

4. Product picture

版次	描述	日期
#10	初版	2019/08/13
#11		
#12		



注意：默认不带字库，SPI需要字库的请联系公司业务。SPI不带字库的时候FSO、FCS悬空即可。

- NOTE: 1. DISPLAY TYPE: 3.2" TFT
2. INTERFACE: 4SPI/MCU-8BIT/MCU-16BIT
3. DRIVE METHOD: 240(H)RGB*320(V)
4. OPERATING VOLTAGE: VOP=3.3V
5. POLARIZER MODE: TRANSMISSIVE/NORMALLY BLACK

6. OPERATING TEMP: -20°C ~ +70°C.
7. STORAGE TEMP: -30°C ~ +80°C.
8. CONNECTOR: ST7789
9. ALL WITHOUT TOLERANCES. X±0.2

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项目名称	LCM图
型号	HS32B01A
页序：	版本：#A1
制图：1/1	审核：
单位：mm	比例：1:1
日期：2019/08/13	

CN1
M0=L M1=L M2=L
Parallel 16 BIT

1	CS
2	RS
3	WR
4	AD
5	RST
6	DO
7	D1
8	D2
9	D3
10	D4
11	D5
12	D6
13	D7
14	D8
15	D9
16	D10
17	D11
18	D12
19	D13
20	D14
21	D15
22	GND
23	BLK
24	VCC

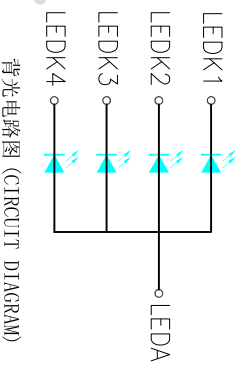
CN1
M0=H M1=L M2=L
Parallel 8 BIT

1	CS
2	RS
3	WR
4	AD
5	RST
6	
7	
8	
9	
10	
11	
12	
13	
14	D8
15	D9
16	D10
17	D11
18	D12
19	D13
20	D14
21	D15
22	GND
23	BLK
24	VCC

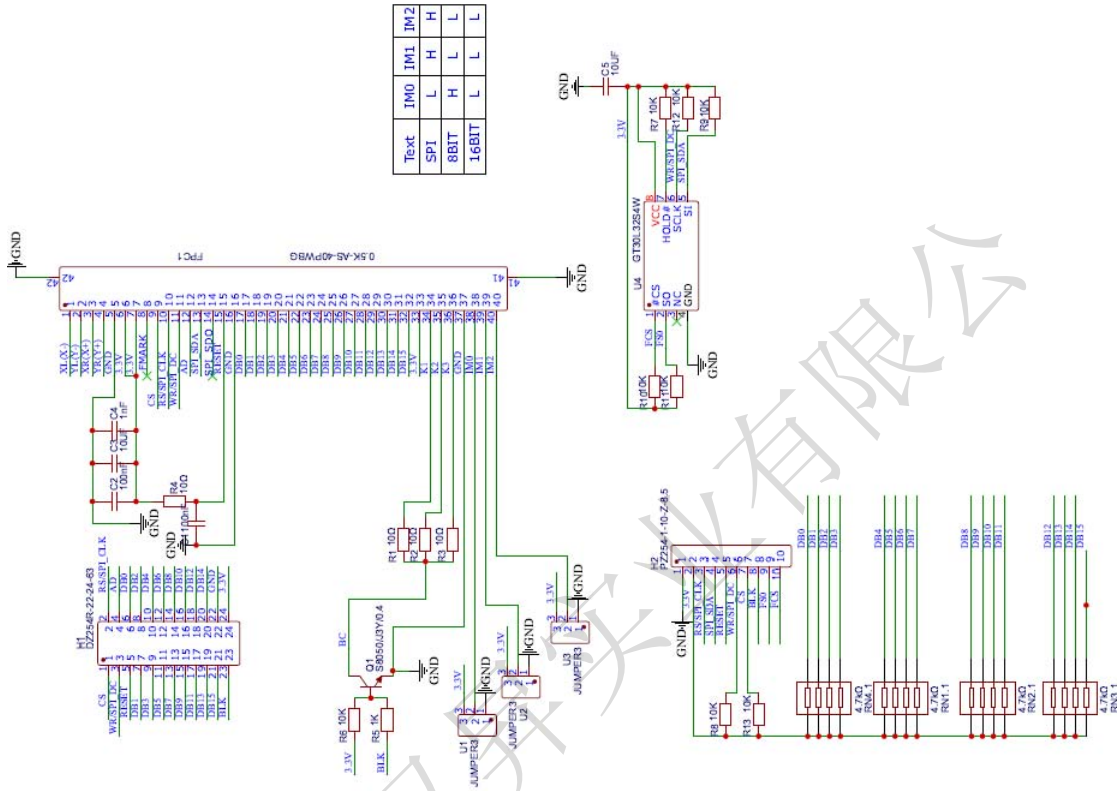
CN2
M0=L M1=H M2=H
serial 4spi

1	GND
2	VCC
3	SCL
4	SDA
5	RSR
6	RS
7	CS
8	BLA
9	FSO
10	FCS

	M0	M1	M2	
SPI	L	H	H	CN2
8BIT	H	L	L	CN1
16BIT	L	L	L	CN1



6. Schematic diagram



7. Maximum Ratings

Item	Symbol	Min	Max	Unit	Note
Supply voltage	VCC	3.0	3.3	V	
Operating temperature	T _{OPR}	-20	70	°C	
Storage temperature	T _{STR}	-30	80	°C	

8. Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	V_{CC}		2.7	2.8	3.3	V
Input Voltage	H level	T_{IH}	$0.8 \cdot IOVCC$		$IOVCC$	V
	L level	T_{IL}	-0.3		$0.2 \cdot IOVCC$	
Storage temperature	I_{DD}	With internal voltage generation $V_{CC}=2.8V$; $T_{emp}=25^{\circ}C$			TBD	mA

9. Backlight Characteristic

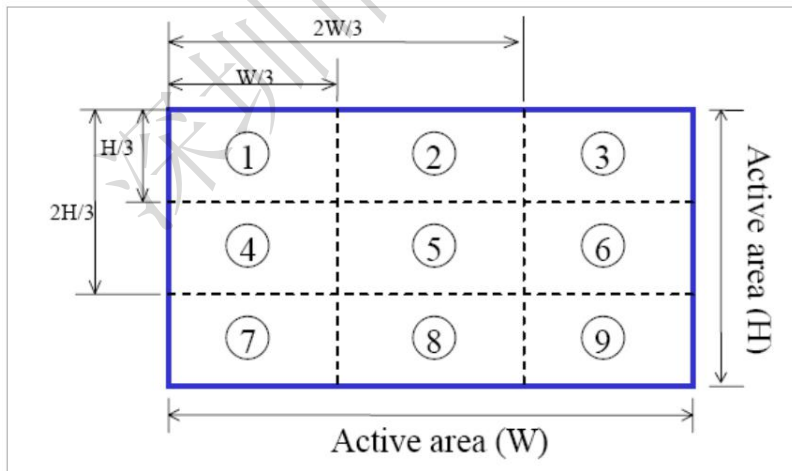
Item	Symbol	Min	Typical	Max	Unit	Notes
LED module Forward voltage	V_{LED}	3.0	3.2	3.3	V	
LED module current	I_{LED}			80	mA	
L/G Surface Luminance ★1	L_s	300			Cd/m^3	
LCM Surface brightness uniform ★2	L_D	80				%

★ 1Test condition is:

- (a) Center point on active area.
- (b) Best Contrast.

★2Uniform measure condition:

- (1) Measure 9 point. Measure location show below;
- (2) Uniform=(Min. brightness /Max. brightness)*100%
- (3) Best Contrast.



10. Module Function Description

10.1 Pin Descriptions

PIN No.	Symbol	Description
1	CS	LCD Chip selection pin; Low enable, high disable.
2	RS	Data/Command Control This pin is Data/Command control pin. When the pin is pulled high, the input at SDA is treated as display data. When the pin is pulled low, the input at SDA will be transferred to the command register.
3	WR	Serves as a write signal and MCU write data at the rising edge.
4	RD	Serves as a read signal and MCU read data at the rising edge.
5	RES	Power Reset for Controller and Driver This pin is reset signal input. When the pin is low, initialization of the chip is executed. Keep this pin pull high during normal operation
6-21	DB0-DB15	Data bus.
22	GND	Ground of Logic Circuit This is a ground pin. It acts as a reference for the logic pins. It must be connected to external ground
23	BLK	Backlight control pin When the pin is pulled high turn on backlight, When the pin is pulled low turn off backlight
24	VCC	Power Supply for Logic This is a voltage supply pin. It must be connected to external source

CN2 SPI

Symbol	Symbol	Description
1	GND	Power Ground.
2	VCC	Power Supply for Analog 3.3V
3	SCL	This pin is used to be serial interface clock.
4	SDA	SPI interface input/output pin.
5	RES	This signal will reset the device, Signal is active low.
6	DC	Display data/command selection pin in 4-line serial interface.
7	CS	LCD Chip selection pin, Low enable, High disable
8	BLK	Backlight control switch, backlight on by default, low level off backlight
9	FS0	Font database data output
10	FCS	FONT IC Chip selection pin, Low enable, High disable

10.2 Timing characteristics.

7.4.1 8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus

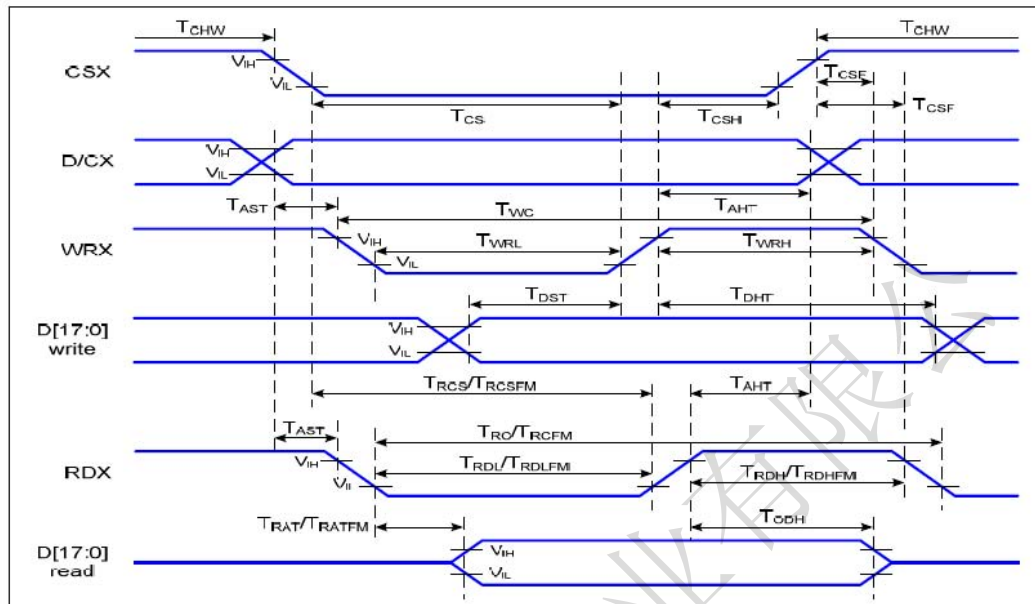


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)

VDD=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25°C

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
WRX	T _{WC}	Write cycle	66		ns	
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{DST}	Data setup time	10		ns	For CL=30pF

10.3 Commands

Refer to the Technical Manual for the ILI9341

11. Electro-optical Characteristics

Item	Symbol	Conditions	Temp	Min.	Typ.	Max.	Unit	Note
Response Time	T_R	$\theta = \phi = 0$	25°C		TBD	TBD	msec	NOTE2
	T_F				TBD	TBD		
Viewing Angle Range	$\phi = 0^\circ (6'')$	$\phi = 90^\circ (3'')$		$\phi = 180^\circ (12'')$		$\phi = 270^\circ (9'')$		NOTE3
$\theta (25^\circ\text{C}) \text{ CR} \geq 10$	TBD	TBD		TBD		TBD		NOTE3

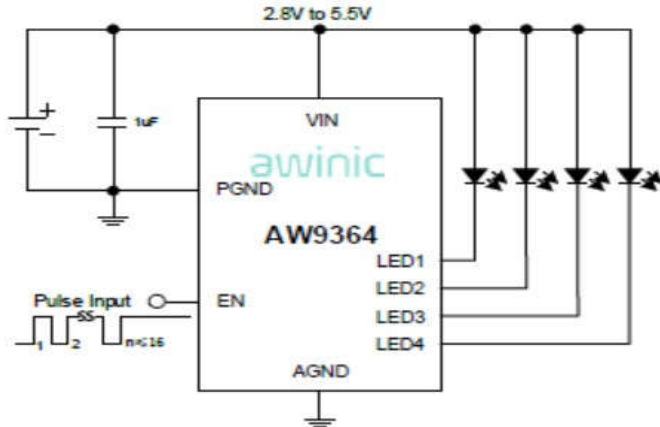
The above "viewing angle" is the measuring position with the largest contrast ratio. Not for good image quality. Viewing direction for good image quality is 12 O'clock.

● For panel only

● Electro-Optical Characteristics Test Method

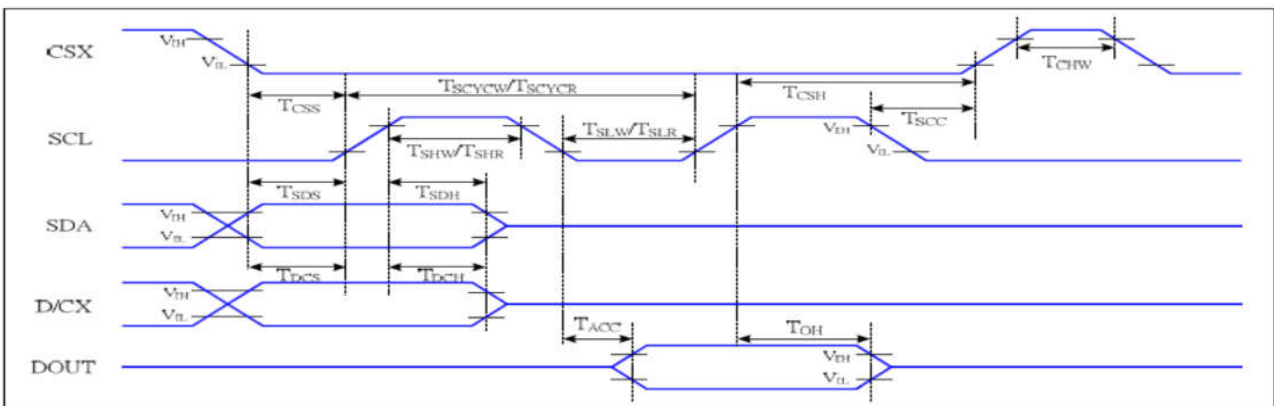
4.4 Backlight Recommended Circuit

Motherboard driver backlight is need constant current circuit , if threated voltage screen after light brightness difference . Current and power consumption of the machine are inconsistent , so recommend a backlight driving circuit is best rated current . It is recommended to use IC (AW9364) . The reference circuit is as follows:

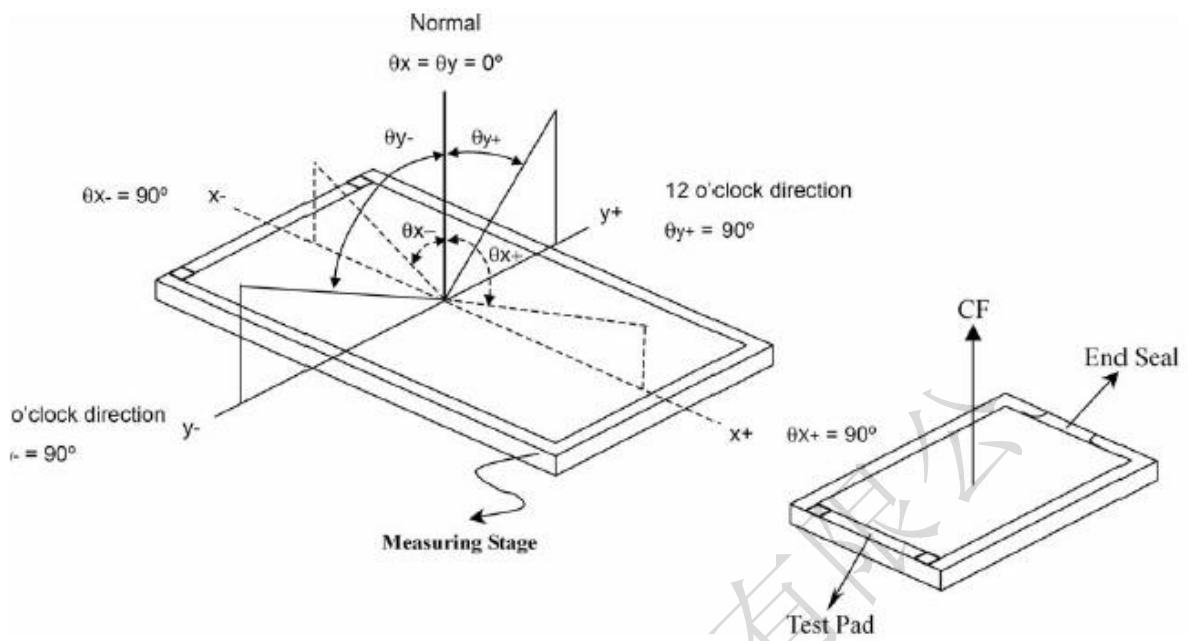


4.5 AC Timing Characteristic of The LCD

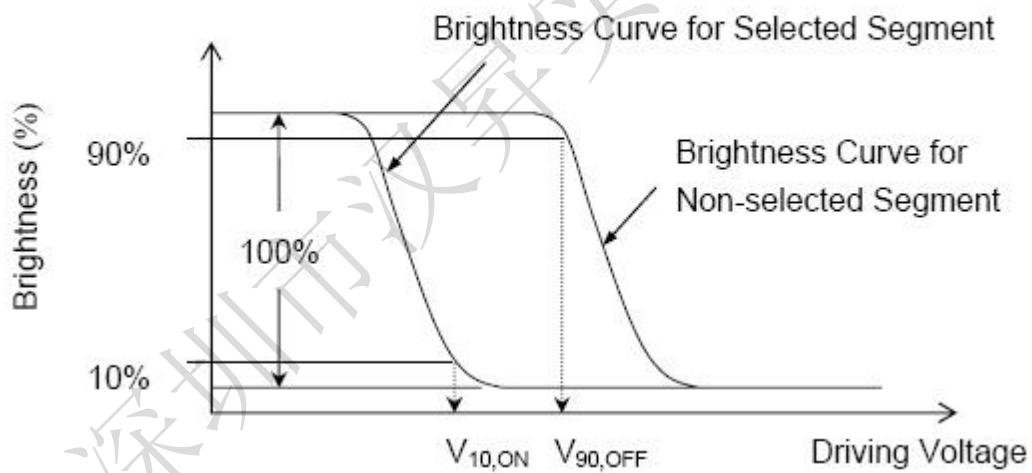
Serial interface Characteristics(4-line serial):



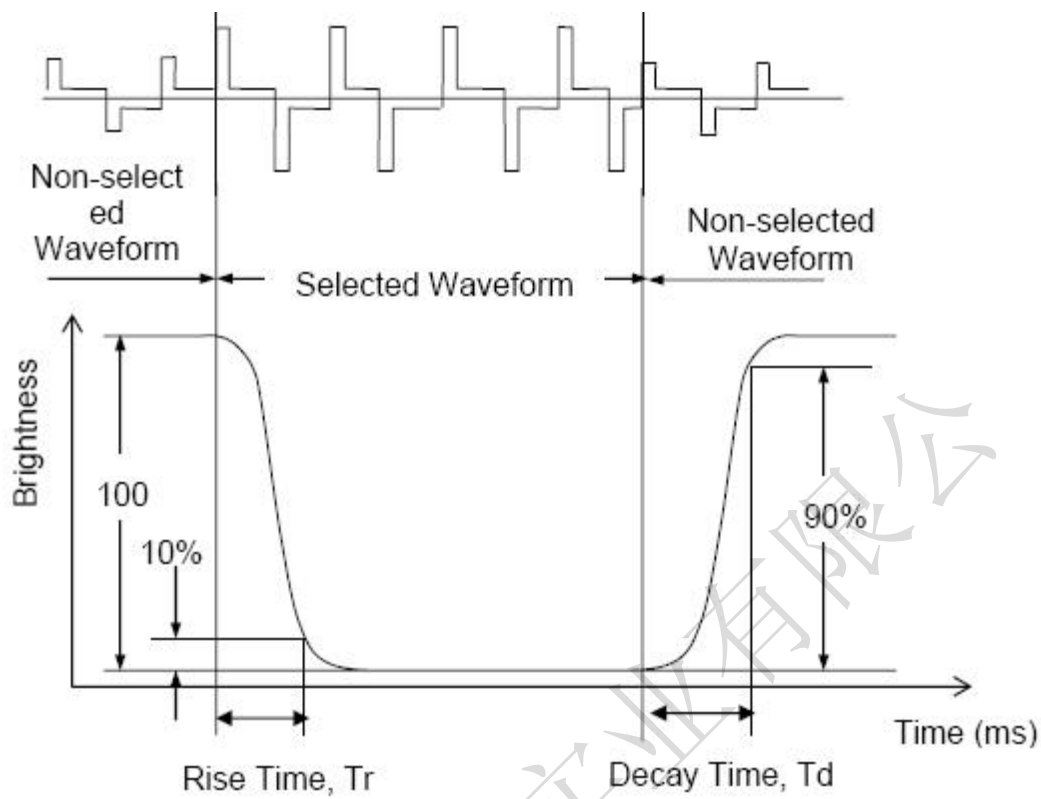
Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T _{css}	Chip select setup time (write)	15		ns	
	T _{csh}	Chip select hold time (write)	15		ns	
	T _{css}	Chip select setup time (read)	60		ns	
	T _{scc}	Chip select hold time (read)	65		ns	
	T _{chw}	Chip select "H" pulse width	40		ns	
SCL	T _{scywr}	Serial clock cycle (Write)	16		ns	-write command & data ram
	T _{shw}	SCL "H" pulse width (Write)	7		ns	
	T _{slw}	SCL "L" pulse width (Write)	7		ns	
	T _{scywr}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T _{shr}	SCL "H" pulse width (Read)	60		ns	
	T _{slr}	SCL "L" pulse width (Read)	60		ns	
D/CX	T _{dcS}	D/CX setup time	10		ns	
	T _{dch}	D/CX hold time	10		ns	
SDA (DIN)	T _{sdS}	Data setup time	7		ns	
	T _{sdh}	Data hold time	7		ns	
DOUT	T _{acc}	Access time	10	50	ns	For maximum CL=30pF
	T _{oh}	Output disable time	15	50	ns	For minimum CL=8pF



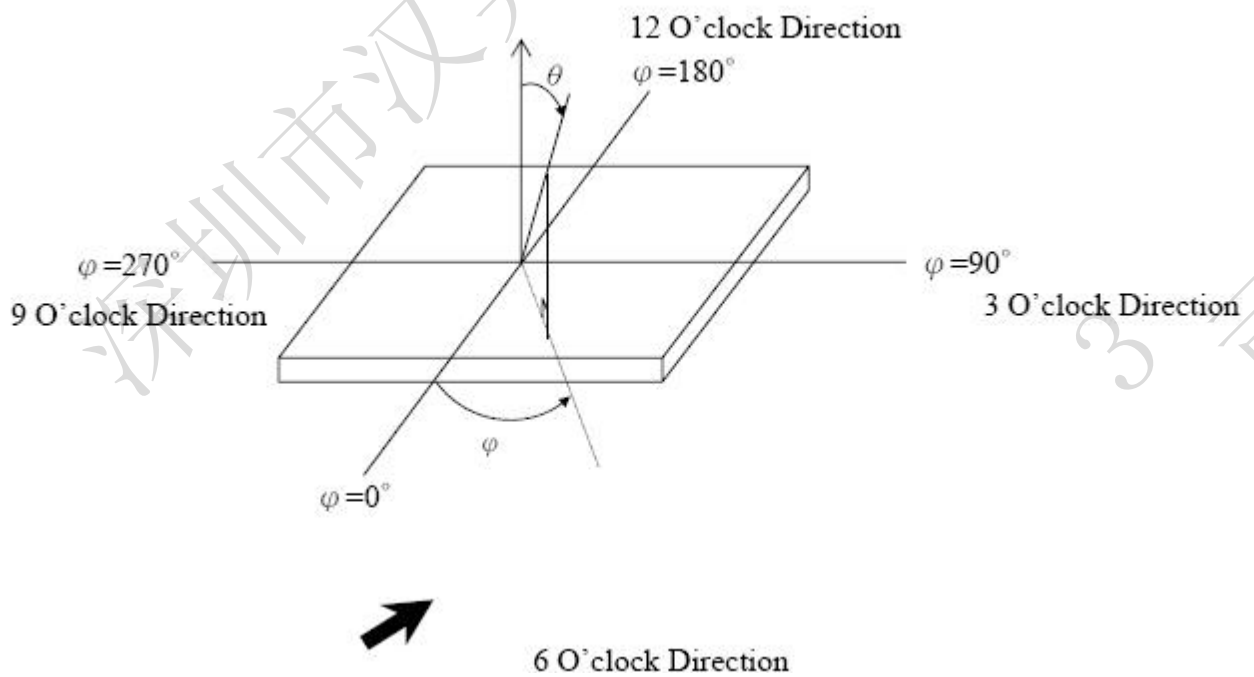
$$V_{op} = (V_{10, ON} + V_{90, OFF})/2$$



.Note2.Definition of Optical Response Time:

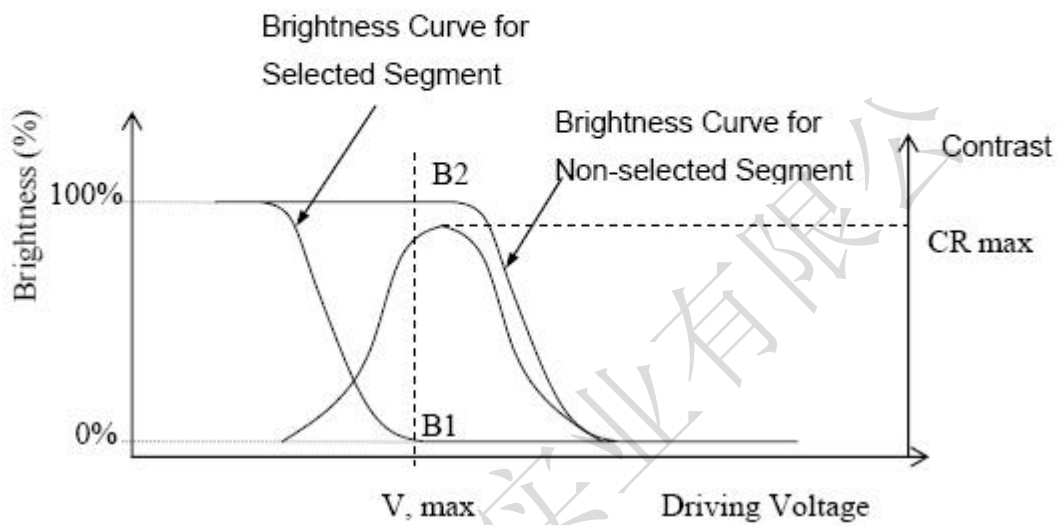


.Note3.Definition of Viewing Angle θ and ϕ :



Note4.Definition of Contrast ratio (CR):

$$CR = \frac{\text{Brightness of Non-selected Segment (B2)}}{\text{Brightness of Selected Segment (B1)}}$$



12. Reliability

12.1 Mtbf

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal

12.2 Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C*240Hrs	<ul style="list-style-type: none"> ◦ No Defect Of Operational Function In Room Temperature Are Allowable ◦ IDD of LCM in Pre-and Post-Test Should Follow Specification
2	Low Temperature Non-Operating Test	-30°C*240Hrs	
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*240Hrs	
4	High Temperature Operating Test	70°C*240Hrs	
5	Low Temperature Operating Test	-20°C*240Hrs	
6	Thermal Shock Test	-20°C (30Min) ↔ 70°C (30Min) *10CYCLES	

Notes:

1. Judgments should be made after exposure in room temperature for two hours.
2. The distill water is used for the high temperature/humidity test.
3. The sample above is individually for every reliability tests condition.

13. Inspection standards

1. AQL (Acceptable Quality Level)

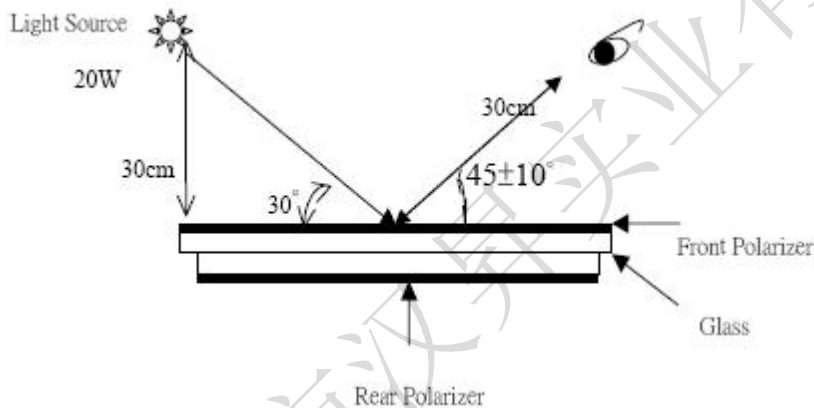
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	0.65	1.5

2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is 1000 ± 200 . (Darkroom's lux: 100 ± 50), About an angle of incidence 30° , a distance of 30 cm with an angle of 45° to check the products without uncovering the film!

(As shown below)

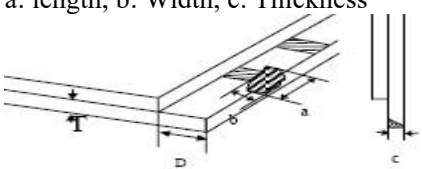
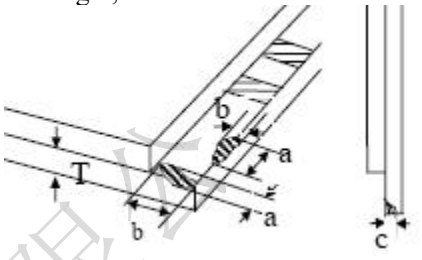
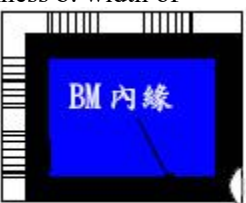



3. Inspection item and criteria

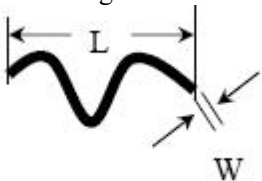
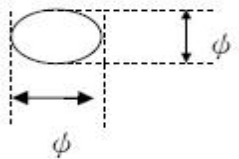
3.1 Visual inspection criterion in immobility

3.1.1 Glass defect

NO	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	
2	Cracks (Major defect)	1. Linear cracks panel 2. Nonlinear crack contrast by limited sample	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage. 1) $b \leq 1/3$ Pin width (non bonding area) 2) bonding area ≤ 0.5 mm	A: Length, b: Width

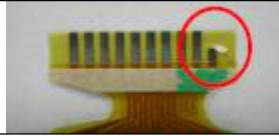
		【Accept】	
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) $b \leq 1/3$ of effective length for bonding electrode 【Accept】	a: length, b: Width, c: Thickness 
5	Pin-side, non-conductive area damaged (minor defect)	1) Damage area don't touch the ITO (Including contraposition mark, except scribing mark) 【Accept】 2) $C < T$ $b \leq BM/3$ of width 【Accept】 3) $c = T$ b not touch the seal glue 【Accept】 4) a disregards	a: Length, b: Width c: Thickness 
6	Non-pin-side damage (minor defect)	$c < T$ 1) b exceeds $1/3 BM$ 【Reject】 $c = T$ b not touch the seal glue 【Reject】	c: Thickness b: width of  damage 

3.1.2 LCD appearance defect (View area)

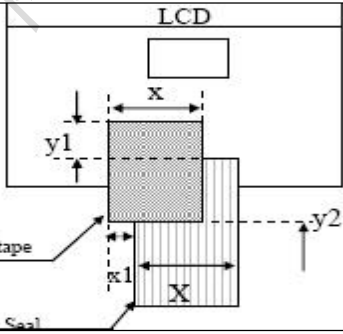
NO	Defect item	Criteria		Remark
		Specification	Allowable	
1	Fiber, glass cratch, polarizer scratch/folded (minor defect)	$W \leq 0.03\text{mm}$	disregard	note1: L: Length, W: Width note2: disregard if out of AA 
		$0.03\text{mm} < W \leq 0.05\text{mm};$ $L \leq 3.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm};$ $L \leq 3.0\text{mm}$	1	
		$W > 0.1\text{mm}; L > 3.0\text{mm}$	0	
2	Polarizer bubble, concave and convex (minor defect)	$\phi \leq 0.2\text{mm}$	disregard	note1: $\phi = (L+W)/2$, L: Length, W: Width note2: disregard if out of AA
		$0.2\text{mm} < \phi \leq 0.3\text{mm}$	2	
		$0.3\text{mm} < \phi \leq 0.5\text{mm}$	1	
		$0.5\text{mm} < \phi$	0	
3	Black dots, dirty dots, impurities, eye winker (minor defect)	$\phi \leq 0.15\text{mm}$	disregard	note2: disregard if out of AA 
		$0.15\text{mm} < \phi \leq 0.25\text{mm}$	2	
		$0.25\text{mm} < \phi \leq 0.3\text{mm}$	1	
		$0.3\text{mm} < \phi$	0	
4	Polarizer prick	$\phi \leq 0.1\text{mm}$	disregard	note1: $\phi = (L+W)/2$, L=Length,

	(minor defect)	$0.1\text{mm} < \phi \leq 0.25\text{mm}$	3	W=Width note2:the distance between two dots>5mm
		$\phi > 0.25\text{mm}$	0	

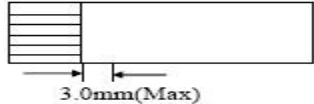
3.1.3FPC

NO	Defect item	Criteria		Remark
1	Copper screen peel (minor defect)	Copper screen peel 【Reject】		
2	No release tape or peel	No release tape or peel 【Reject】		
3	Dirty dot and impurity of FPC for customer using side (minor defect)	Specification	Allowable	Note1: Cannot have stride ITO impurities
		$\phi \leq 0.25\text{mm}$	2	
		$\phi > 0.25$	0	

3.1.4Black tape & Mara tape

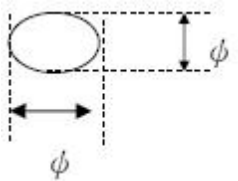
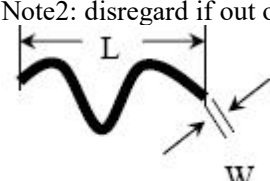
NO	Defect item	Criteria	Remark
1	FPC or H/S black tape (minor defect)	1. shift spec: 1) glue to the polarize 【Reject】 2) IC bare 【Reject】 2. left-and-right spec: 1) exceed of FPC edge or H-S edge 【Reject】 2) IC bare 【Reject】	
2	No black tape (major defect)	No black tape 【Reject】	
3	Tape position mistake (minor defect)	Not by engineering drawing	
4	Mara tape defect (minor defect)	Peel before pulling the protecting film 【Reject】	

3.1.5Silicon and Taffy glue

NO	Defect item	Criteria	Remark
1	Quantity of silicon (major defect)	Uncover the ITO and circuit area 【Reject】	note: compared by engineering
2	Taffy glue (major defect)	1.Uncover the reveal copper area【Reject】 2.Cover layer 0.3mm(Min)~3.0mm(Max) 【Reject】	note: if customer has special requirement, refer to the technical document 
3	Depth of glue covering (major defect)	Depth of glue covering otop front Polarizer 【Reject】	Except of the special requirement

3.2Electrical criteria

NO	Defect item	Criteria	Remark
----	-------------	----------	--------

1	No display (major defect)	No display 【Rej ect】		
2	Missing line (major defect)	Missing line 【Rej ect】		
3	Seg-com light and dark (major defect)	Seg-com light and dark 【Rej ect】	ND filter 2% test	
4	No display in immobility (major defect)	No display in immobility 【Rej ect】		
5	Flicker of Pattern (major defect)	Flicker of Pattern 【Rej ect】		
6	Mura (major defect)	ND filter 2%test		
7	Over current (major defect)	Over current 【Rej ect】		
8	Voltage out of specification (major defect)	Voltage out of specification 【Rej ect】		
9	Pattern blur, error code (major defect)	Pattern blur, error code 【Rej ect】		
10	Dark light, Flicker (major defect)	Dark light, Flicker 【Rej ect】		
11	Black/white dots、 Dirty dots、 eye winker (major defect)	Specification	Allowable	Note 1:disregard if out of AA 
		$\phi \leq 0.15\text{mm}$	disregard	
		$0.15\text{mm} < \phi \leq 0.25\text{mm}$	2	
		$0.25\text{mm} < \phi \leq 0.3\text{mm}$	1	
	$0.3\text{mm} < \phi$	0		
12	Fiber、glass crutch、Polarizer scratch/folded (major defect)	$W \leq 0.03\text{mm}$	disregard	Note 1:L: Length, W: Width Note2: disregard if out of AA 
		$0.03\text{mm} < W \leq 0.05\text{mm}$ $L \leq 3.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm}$ $L \leq 3.0\text{mm}$	1	
		$W > 0.1\text{mm}; L > 3.0\text{mm}$	0	

14.Precautions for using LCD modules.

14.1 Safety

- (1)Do not swallow any liquid crystal ,even if there is no proof that liquid crystal is poisonous.
- (2)If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3)If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

14.2 Srorang Conditions

- (4) Store the panel or module in a dark place where the temperature is $23 \pm 5^{\circ}\text{C}$ and the humidity is below $45 \pm 20\% \text{RH}$.
- (5) Store in anti-static electricity container.
- (6) Store in clean environment, free from dust, active gas, and solvent.
- (7) Do not place the module near organics solvents or corrosive gases.
- (8) Do not crush, shake, or jolt the module.

14.3 Handling Precautions

- (9) Avoid static electricity, which can damage the CMOS LSI.
- (10) The polarizing plate of the display is very fragile, please handle it very carefully.
- (11) Do not give external shock.
- (12) Do not apply excessive force on the surface.
- (13) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14) Do not use ketonic solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15) Do not operate it above the absolute maximum rating.
- (16) Do not remove the panel or frame from the module.

14.4 Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.

15. Revision history

16. The appendix

```
void LCD_Init(void)
{
    LCD_RES_Clr(); //复位
    delay_ms(100);
    LCD_RES_Set();
    delay_ms(100);

    LCD_BLK_Set(); //打开背光
    delay_ms(100);
    //***** Start Initial Sequence *****//
    LCD_WR_REG(0x11); //Sleep out
    delay_ms(120); //Delay 120ms
    //***** Start Initial Sequence *****//
    LCD_WR_REG(0xCF);
    LCD_WR_DATA8(0x00);
    LCD_WR_DATA8(0xD9);
    LCD_WR_DATA8(0X30);

    LCD_WR_REG(0xED);
    LCD_WR_DATA8(0x64);
    LCD_WR_DATA8(0x03);
    LCD_WR_DATA8(0X12);
    LCD_WR_DATA8(0X81);

    LCD_WR_REG(0xE8);
    LCD_WR_DATA8(0x85);
    LCD_WR_DATA8(0x10);
    LCD_WR_DATA8(0x78);

    LCD_WR_REG(0xCB);
    LCD_WR_DATA8(0x39);
    LCD_WR_DATA8(0x2C);
    LCD_WR_DATA8(0x00);
    LCD_WR_DATA8(0x34);
    LCD_WR_DATA8(0x02);

    LCD_WR_REG(0xF7);
    LCD_WR_DATA8(0x20);

    LCD_WR_REG(0xEA);
    LCD_WR_DATA8(0x00);
```

```
LCD_WR_DATA8(0x00);

LCD_WR_REG(0xC0); //Power control
LCD_WR_DATA8(0x21); //VRH[5:0]

LCD_WR_REG(0xC1); //Power control
LCD_WR_DATA8(0x12); //SAP[2:0];BT[3:0]

LCD_WR_REG(0xC5); //VCM control
LCD_WR_DATA8(0x32);
LCD_WR_DATA8(0x3C);

LCD_WR_REG(0xC7); //VCM control2
LCD_WR_DATA8(0xC1);

LCD_WR_REG(0x36); // Memory Access Control
LCD_WR_DATA8(0x08);

LCD_WR_REG(0x3A);
LCD_WR_DATA8(0x55);

LCD_WR_REG(0xB1);
LCD_WR_DATA8(0x00);
LCD_WR_DATA8(0x18);

LCD_WR_REG(0xB6); // Display Function Control
LCD_WR_DATA8(0x0A);
LCD_WR_DATA8(0xA2);

LCD_WR_REG(0xF2); // 3Gamma Function Disable
LCD_WR_DATA8(0x00);

LCD_WR_REG(0x26); //Gamma curve selected
LCD_WR_DATA8(0x01);

LCD_WR_REG(0xE0); //Set Gamma
LCD_WR_DATA8(0x0F);
LCD_WR_DATA8(0x20);
LCD_WR_DATA8(0x1E);
LCD_WR_DATA8(0x09);
LCD_WR_DATA8(0x12);
LCD_WR_DATA8(0x0B);
LCD_WR_DATA8(0x50);
```

```
LCD_WR_DATA8(0XB8);
LCD_WR_DATA8(0x44);
LCD_WR_DATA8(0x09);
LCD_WR_DATA8(0x14);
LCD_WR_DATA8(0x05);
LCD_WR_DATA8(0x23);
LCD_WR_DATA8(0x21);
LCD_WR_DATA8(0x00);

LCD_WR_REG(0XE1); //Set Gamma
LCD_WR_DATA8(0x00);
LCD_WR_DATA8(0x19);
LCD_WR_DATA8(0x19);
LCD_WR_DATA8(0x00);
LCD_WR_DATA8(0x12);
LCD_WR_DATA8(0x07);
LCD_WR_DATA8(0x2D);
LCD_WR_DATA8(0x28);
LCD_WR_DATA8(0x3F);
LCD_WR_DATA8(0x02);
LCD_WR_DATA8(0x0A);
LCD_WR_DATA8(0x08);
LCD_WR_DATA8(0x25);
LCD_WR_DATA8(0x2D);
LCD_WR_DATA8(0x0F);
LCD_WR_REG(0x29); //Display on
}
```

深圳市汉昇实业有限公司

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