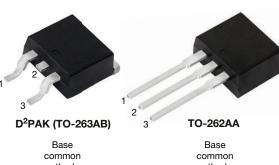
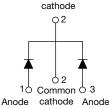


VS-30CTQ0.0S-M3, VS-30CTQ0.0-1-M3 Series

Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 15 A





TO-262AA Base common cathode 0 2 0 2 10 Common 0 3 Anode cathode Anode

VS-30CTQ...S-M3

VS-30CTQ...-1-M3

PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 15 A							
V _R	50 V, 60 V							
V _F at I _F	0.56 V							
I _{RM} typ.	45 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	13 mJ							
Package	D ² PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

FEATURES

- 150 °C T_J operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	30	А						
V _{RRM}		50/60	V						
I _{FSM}	t _p = 5 μs sine	1000	A						
VF	15 A_{pk} , T_J = 125 °C (per leg)	0.56	V						
TJ	Range	-55 to +150	°C						

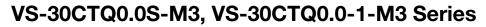
VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-30CTQ050S-M3 VS-30CTQ050-1-M3	VS-30CTQ060S-M3 VS-30CTQ060-1-M3	UNITS				
Maximum DC reverse voltage	V _R	50	60	V				
Maximum working peak reverse voltage	V _{RWM}	50	00	v				

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ROHS COMPLIANT HALOGEN

FREE



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SHAY

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ABSOLUTE MAXIMUM RATINGS										
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS					
Maximum average per device					30					
forward current See fig. 5	per leg	I _{F(AV)}	50 % duty cycle at $T_C = 105 \text{ °C}$	15	Α					
Maximum peak one cycle non-repetitive surge current per leg See fig. 7			5 μ s sine or 3 μ s rect. pulse	Following any rated load	1000	~				
		I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	260					
Non-repetitive avalanche ener	gy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, I_{AS} = 1.50 \ A, L = 11.$	13	mJ					
Repetitive avalanche current p	ber leg	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu		1.50	А				

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS				
		15 A	T _{.1} = 25 °C	0.62	v				
Maximum forward voltage drop per leg See fig. 1	V _{EM} ⁽¹⁾	30 A	1j=25 C	0.82					
	VFM (1)	15 A	T.I = 125 °C	0.56					
		30 A	1j = 125 C	0.71					
Maximum reverse leakage current per leg	I (1)	T _J = 25 °C	V - Potod V	0.80	mA				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	$V_R = Rated V_R$	160					
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	45	mA				
Threshold voltage	V _{F(TO)}	T T maximum		0.39	V				
Forward slope resistance	r _t	$I_{J} = I_{J}$ maximum	$T_J = T_J$ maximum		mΩ				
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range	720	pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 r	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R 10 000							

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C				
Maximum thermal resistance, junction to case per leg		Б	DC aparation	3.25	°C/W				
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.63	C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50					
Approximate weight				2	g				
Approximate weight				0.07	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque maximum				12 (10)	(lbf · in)				
Marking daviag			Case style D ² PAK (TO-263AB)	30CT0 30CT0	2050S 2060S				
Marking device			Case style TO-262AA	30CTC 30CTC					

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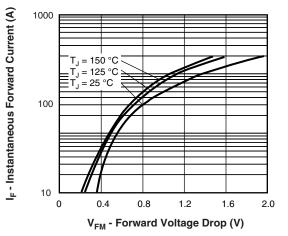
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VS-30CTQ0.0S-M3, VS-30CTQ0.0-1-M3 Series

Vishay Semiconductors



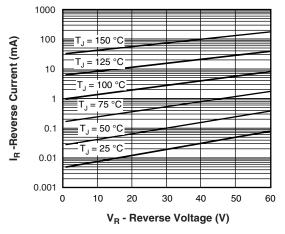


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

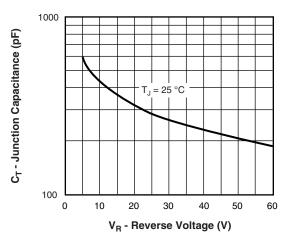


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

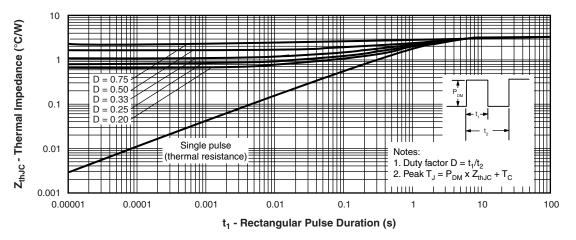


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

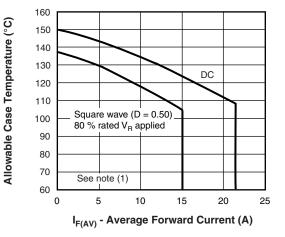
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 Document Number: 94934

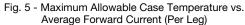
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Vishay Semiconductors





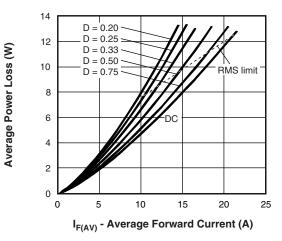


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

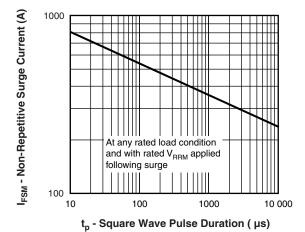


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

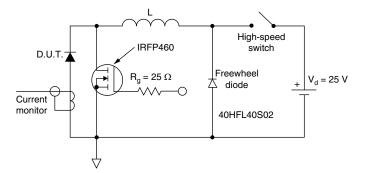


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{BEV} = inverse power loss = $V_{B1} \times I_B (1 - D)$; I_B at $V_{B1} = 10 V$

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Vishay Semiconductors

ORDERING INFORMATION TABLE

www.vishay.com

VISHAY

Device code	VS-	30	с	т	Q	060	S	TRL	-M3
		2	3	4	5	6	7	8	9
	민 ·	- Vis	hay Sen	licondu	ctors pr	oduct			
	2 -	- Cui	rrent rati	ng (30 A	A)				
	3 -	- Cire	cuit con [.]	figuratio	n: C = c	commor	n catho	de	
	4 -	- Т=	TO-220)					
	5 -	- Scł	nottky "(Q" series	sг	050 5	0.14		
	6 -	- Vol	tage rati	ngs —		050 = 5 060 = 6			
	7 -	• S	= D ² PA	K (TO-2					
		• -1	l = TO-2	62AA					
	8 -	• N	one = tı	ıbe					
		• T	RL = tap	e and re	eel (left	oriented	d - for D	² PAK (1	TO-263/
		• T	RR = tap	be and r	eel (righ	nt orient	ed - for	D ² PAK	(TO-26
	9 -	M3	3 = halo	gen-free	e, RoHS	-compl	iant, an	d termiı	nation le

ORDERING INFORMATION										
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION								
VS-30CTQ050S-M3	50	Antistatic plastic tubes								
VS-30CTQ050STRR-M3	800	13" diameter plastic tape and reel								
VS-30CTQ050STRL-M3	800	13" diameter plastic tape and reel								
VS-30CTQ050-1-M3	50	Antistatic plastic tubes								

LINKS TO RELATED DOCUMENTS								
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164						
Dimensions	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

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Outline Dimensions



Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

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