$R_1$  and  $C_1$  recommended to reduce supply ripple for  $V_S < 2.8$  V

decoding.

The TSOP373.. series devices are optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. AGC3 may also suppress some data signals if continuously transmitted.

New designs should prefer the TSOP373.. series containing the newer AGC3. The TSOP375.. series contain a very robust AGC5. This series should only be used for critically noisy environments.

These components have not been qualified according to

## **FEATURES**

- Very low supply current
- · Photo detectors and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- · Insensitive to supply voltage ripple and noise
- Material categorization:

for definitions of compliance please see www.vishay.com/doc?99912

## **MECHANICAL DATA**

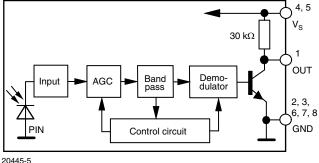
Pinnina: 6, 8 = GND, 5 = V<sub>S</sub>, 1 = OUT

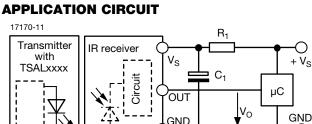
## **ORDERING CODE**

Taping:

TSOP37...TT1 - top view taped, 1800 pcs/reel

## **BLOCK DIAGRAM**





**Vishay Semiconductors** 

e4

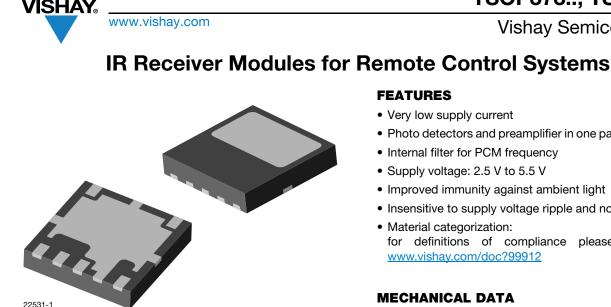
RoHS

COMPLIANT

HALOGEN FREE

GREEN

(5-2008)



## LINKS TO ADDITIONAL RESOURCES



## DESCRIPTION

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The TSOP373.., TSOP375.. series are miniaturized receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a PCB, the epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for

automotive specifications.



PARTS TABLE

AGC		NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
	33 kHz	TSOP37333	TSOP37533	
Carrier frequency	36 kHz	TSOP37336 <sup>(1)</sup>	TSOP37536	
	38 kHz	TSOP37338 <sup>(2)(3)(4)(5)</sup>	TSOP37538	
	40 kHz	TSOP37340	TSOP37540	
	56 kHz	TSOP37356	TSOP37556	
Package		Belobog		
Pinning		1 = OUT, 2, 3, 6, 7, 8 = GND, 4, 5 = V <sub>S</sub>		
Dimensions (mm)		3.95 W x 3.95 H x 0.8 D		
Mounting		SMD		
Application		Remote control		
Best choice for		<sup>(1)</sup> MCIR <sup>(2)</sup> Mitsubishi <sup>(3)</sup> RECS-80 Code <sup>(4)</sup> r-map <sup>(5)</sup> XMP-1, XMP-2		
Special options		Extended temperature range: <u>www.vishay.com/doc?82738</u>		

ABSOLUTE MAXIMUM RA	XIMUM RATINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		Vs	-0.3 to +6	V
Supply current		I <sub>S</sub>	3	mA
Output voltage		Vo	-0.3 to (V <sub>S</sub> + 0.3)	V
Output current		Ι <sub>Ο</sub>	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW

Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

ELECTRICAL AND OPTI	CAL CHARACTERISTICS	(T <sub>amb</sub> = 25 °C	C, unless o <sup>.</sup>	therwise sp	ecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		V <sub>S</sub>	2.5	-	5.5	V
Supply current	$V_{S} = 3.3 V, E_{v} = 0$	I <sub>SD</sub>	0.27	0.35	0.45	mA
Supply current	E <sub>v</sub> = 40 klx, sunlight	I <sub>SH</sub>	-	0.45	-	mA
Transmission distance	$E_v = 0$ , IR diode TSAL6200, $I_F = 50$ mA, test signal see Fig. 1	d	-	24	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V <sub>OSL</sub>	-	-	100	mV
Minimum irradiance	$\begin{array}{l} \mbox{Pulse width tolerance:} \\ t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_{o,} \\ \mbox{test signal see Fig. 1} \end{array}$	E <sub>e min.</sub>	-	0.12	0.25	mW/m <sup>2</sup>
Maximum irradiance	t <sub>pi</sub> - 5/f <sub>o</sub> < t <sub>po</sub> < t <sub>pi</sub> + 6/f <sub>o</sub> , test signal see Fig. 1	E <sub>e max.</sub>	30	-	-	W/m <sup>2</sup>
Directivity	Angle of half transmission distance	Φ1/2	-	± 75	-	0

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# TSOP373.., TSOP375..





## **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)

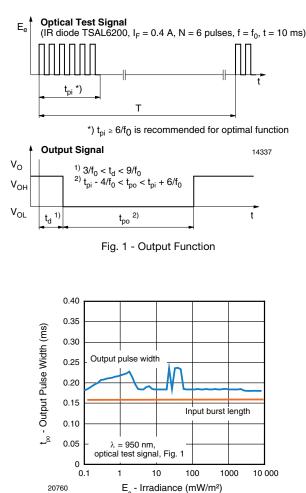
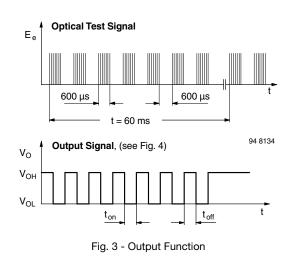


Fig. 2 - Output Pulse Width vs. Irradiance



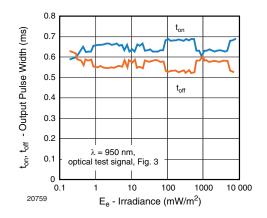


Fig. 4 - Output Pulse Diagram

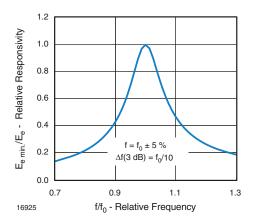


Fig. 5 - Frequency Dependance of Responsivity

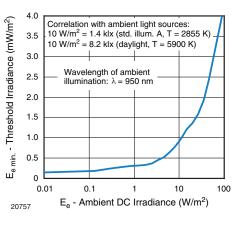


Fig. 6 - Sensitivity in Bright Ambient

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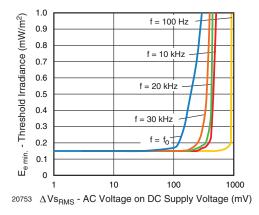


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

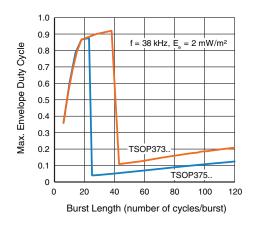


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

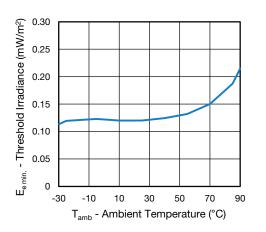


Fig. 9 - Sensitivity vs. Ambient Temperature

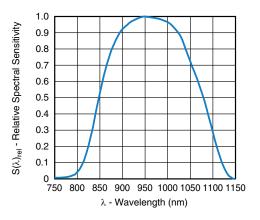
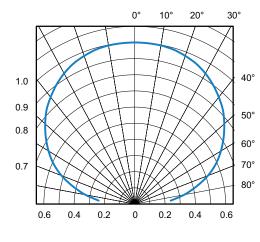


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength





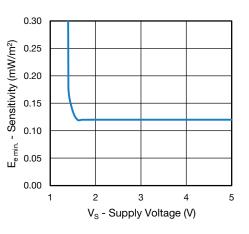


Fig. 12 - Sensitivity vs. Supply Voltage

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## SUITABLE DATA FORMAT

The TSOP373.., TSOP375.. series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP373.., TSOP375.. in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

## **Vishay Semiconductors**

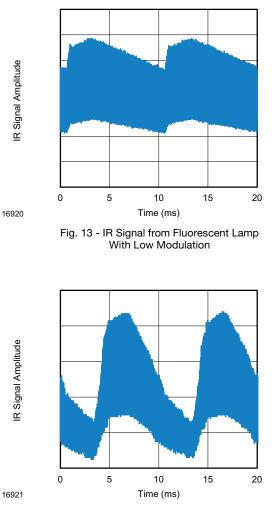


Fig. 14 - IR Signal from Fluorescent Lamp With High Modulation

	TSOP373	TSOP375
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles
For bursts greater than	35 cycles	24 cycles
a minimum gap time in the data stream is needed of	> 6 x burst length	> 25 ms
Maximum number of continuous short bursts/second	2000	2000
MCIR code	Preferred	Yes
XMP-1, XMP-2 code	Preferred	Yes
Suppression of interference from fluorescent lamps	Mild and complex disturbance patterns are suppressed (example: signal pattern of Fig. 13 and 14)	Critical disturbance patterns are suppressed, e.g. highly dimmed LCDs

#### Note

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP372.., TSOP374..



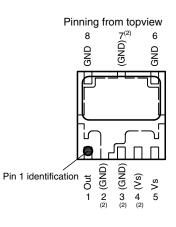
## **PACKAGE DIMENSIONS** in millimeters

# TSOP373.., TSOP375..

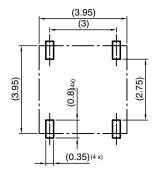
# **Vishay Semiconductors**

Not indicated tolerances ± 0.1

technical drawings according to DIN specifications



Proposed pad layout from component side (dim. for reference only)

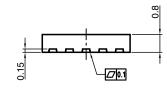


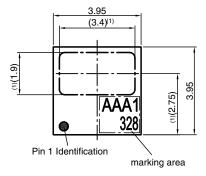
 $\begin{array}{c} \hline 0 \\ \hline 0 \hline$ 

 $4 \times 0.75 = 3$ 

(0.3)

0.475

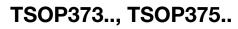




Drawing-No.: 6.550-5315.01-4 Issue: 2; 12.02.14

#### Notes

- <sup>(1)</sup> Optically effective area
- <sup>(2)</sup> Pins connected internally. It is not necessary to connect externally





## **ASSEMBLY INSTRUCTIONS**

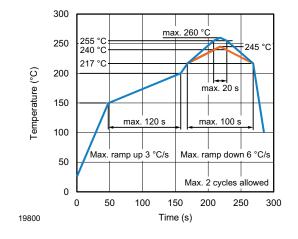
#### **Reflow Soldering**

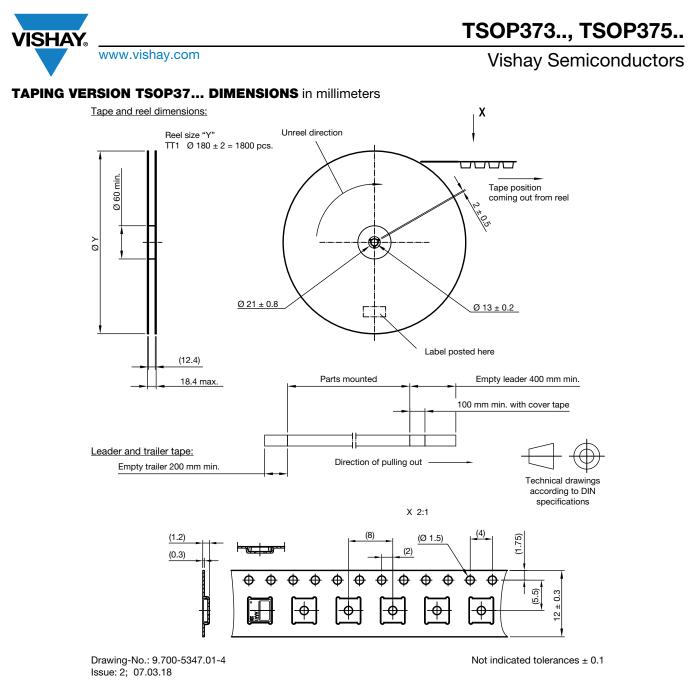
- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

## VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE

#### Manual Soldering

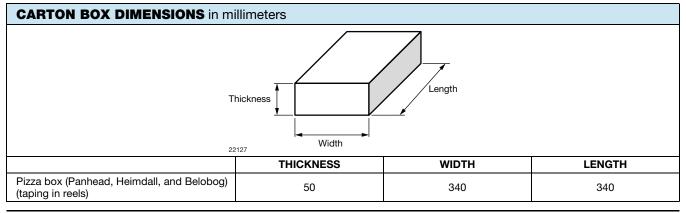
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off





## **OUTER PACKAGING**

The sealed reel is packed into a pizza box.



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TSOP373.., TSOP375..





## LABEL

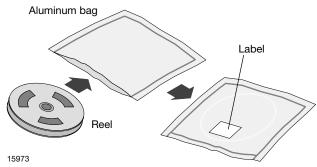
#### Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL			
	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	РСК	-	
Mixed code indicator	MIXED CODE	-	
Origin	XXXXXX+	Company logo	
Long bar code top	Туре	Length	
Item-number	Ν	8	
Plant-code	Ν	2	
Sequence-number	Х	3	
Quantity	Ν	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	Х	3	
Data-code	Ν	3	
Batch-number	Х	10	
Filter	-	1	
Total length	-	17	

## **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



## FINAL PACKING

The sealed reel is packed into a cardboard box.

## **RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity  $\leq$  60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

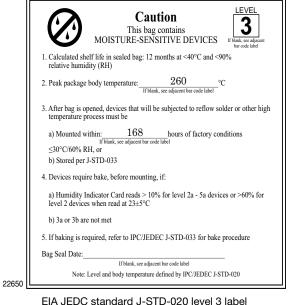
96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC<sup>®</sup> standard J-STD-020 level 3 label is included on all dry bags.

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A JEDC standard J-STD-020 level 3 lab is included on all dry bags

## ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

## VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

## BAR CODE PRODUCT LABEL (example)



22178



Vishay

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