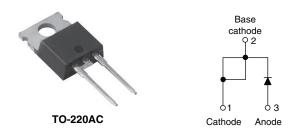
# High Performance Schottky Rectifier, 18 A



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PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	18 A							
V <sub>R</sub>	35 V, 40 V, 45 V							
V <sub>F</sub> at I <sub>F</sub>	0.53 V							
I <sub>RM</sub> max.	25 mA at 125 °C							
T <sub>J</sub> max.	175 °C							
E <sub>AS</sub>	24 mJ							
Package	TO-220AC							
Circuit configuration	Single							

#### FEATURES

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- Guard ring for enhanced ruggedness and long term reliability
- AEC-Q101 qualified meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

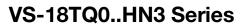
The VS-18TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I <sub>F(AV)</sub>	Rectangular waveform	18	A							
V <sub>RRM</sub>	Range	35 to 45	V							
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1800	A							
V <sub>F</sub>	18 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.53	V							
TJ	Range	-55 to +175	°C							

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-18TQ035HN3	VS-18TQ040HN3	VS-18TQ045HN3	UNITS				
Maximum DC reverse voltage	V <sub>R</sub>								
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	40	45	V				

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS						
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_{C}$ = 149 °C	18							
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load	1800	А					
non-repetitive surge current See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	390						
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 3.6 \ A, \ L = 3.7$	24	mJ						
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by T <sub>J</sub> maxim	3.6	А						

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### **Vishay Semiconductors**

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
		18 A	T <sub>.1</sub> = 25 °C	0.60					
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	36 A	$1_{\rm J} = 25$ C	0.72	V				
	¥FM <sup>(1)</sup>	18 A	T <sub>.1</sub> = 125 °C	0.53					
		36 A	$-1_{\rm J} = 125$ C	0.67					
Maximum reverse leakage current	I (1)	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated V <sub>B</sub>	2.5	<b>س</b> ۸				
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	v <sub>R</sub> = naleu v <sub>R</sub>	25	mA				
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal ran	ge 100 kHz to 1 MHz) 25 °C	1400	pF				
Typical series inductance	Ls	Measured lead to lead 5 m	8	nH					
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs					

Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECI	THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	VALUES	UNITS						
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C					
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	1.50	°C/W					
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	C/ W					
Approximate weight				2	g					
Approximate weight				0.07	oz.					
Mounting torque	minimum			6 (5)	kgf ⋅ cm					
Mounting torque maximum				12 (10)	(lbf · in)					
Marking device				18TQ035H						
			Case style TO-220AC	18TQ	040H					
				18TQ045H						



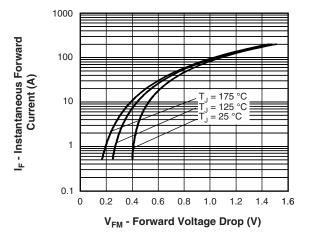


Fig. 1 - Maximum Forward Voltage Drop Characteristics

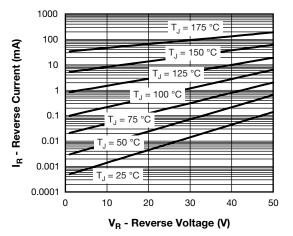


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

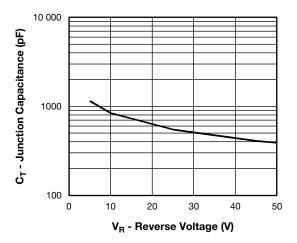


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

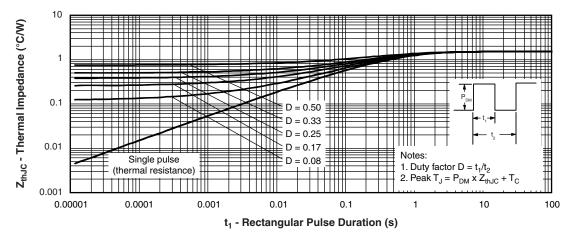
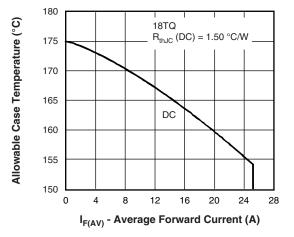
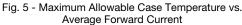


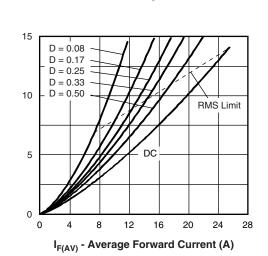
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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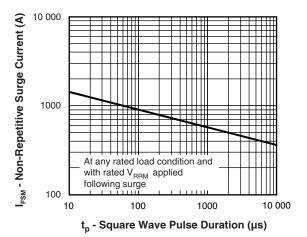












Average Power Loss (W)

Fig. 7 - Maximum Non-Repetitive Surge Current

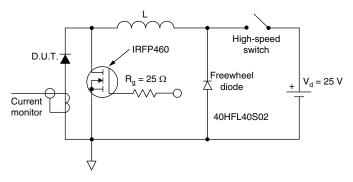


Fig. 8 - Unclamped Inductive Test Circuit

VS-18TQ0..HN3 Series

Vishay Semiconductors



#### **ORDERING INFORMATION TABLE**

Device code	VS-	18	т	Q	045	н	N3	
		2	3	4	5	6	7	
	1 - 2 - 3 -	Cur Pac	,	ng (18 =	ctors pro : 18 A)	duct		
	4 -   5 -   6 -   7 -	Volt H = Env	age rati AEC-Q rironmer	101 qua ntal digit	lified	- comp	035 = 3 040 = 2 045 = 2	40 V

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-18TQ035HN3	50	1000	Antistatic plastic tube						
VS-18TQ040HN3	50	1000	Antistatic plastic tube						
VS-18TQ045HN3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95221					
Part marking information	TO-220AC-N3	www.vishay.com/doc?95068					
SPICE model		www.vishay.com/doc?96209					





**TO-220AC** 

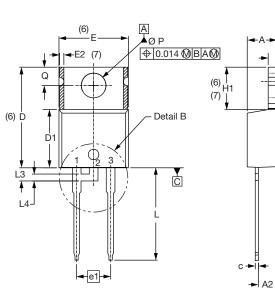
B Seating

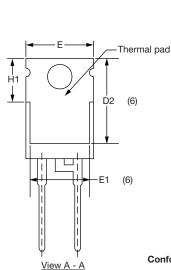
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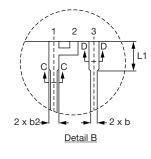
-A1

plane

### **DIMENSIONS** in millimeters and inches









Conforms to JEDEC<sup>®</sup> outline TO-220AC

⊕ 0.015 BA

SYMBOL -	MILLIM	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			e1	4.88	5.28	0.192	0.208	
b	0.69	1.01	0.027	0.040			H1	5.84	6.86	0.230	0.270	6, 7
b1	0.38	0.97	0.015	0.038	4		L	13.52	14.02	0.532	0.552	
b2	1.20	1.73	0.047	0.068			L1	3.32	3.82	0.131	0.150	2
b3	1.14	1.73	0.045	0.068	4		L3	1.78	2.13	0.070	0.084	
с	0.36	0.61	0.014	0.024			L4	0.76	1.27	0.030	0.050	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.73	0.139	0.147	
D	14.85	15.25	0.585	0.600	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355								
D2	11.68	12.88	0.460	0.507	6							
E	10.11	10.51	0.398	0.414	3, 6							

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

- (4) Dimension b1, b3 and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimension: inches
- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2 and E1

<sup>(7)</sup> Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed

<sup>(8)</sup> Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

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