Low forward voltage drop, low power losses

- 10 s per JESD 22-B106
- AEC-Q101 gualified available: - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

### **MECHANICAL DATA**

#### Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Mounting torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)									
PARAMETER		SYMBOL	VX60M60C	UNIT					
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	60	V					
Maximum average forward rectified current	per device	1	60	٨					
(fig. 1)	per diode	IF(AV)	30	A					
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I <sub>FSM</sub>	300	А					
Operating junction temperature range		T <sub>J</sub> <sup>(1)</sup>	-40 to +175	°C					
Storage temperature range		T <sub>STG</sub>	-40 to +175	C					

Note

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{0JA}$ 

## Dual High Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low V<sub>F</sub> = 0.33 V at  $I_F$  = 5.0 A

### **FEATURES**

- Trench MOS Schottky technology
- · High efficiency operation
- Solder bath temperature 275 °C maximum,

# VX60M60C

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**TO-220AB** 



PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 30 A							
V <sub>RRM</sub>	60 V							
I <sub>FSM</sub>	300 A							
V <sub>F</sub> at I <sub>F</sub> = 30 A (T <sub>J</sub> = 125 °C)	0.55 V							
T <sub>J</sub> max.	175 °C							
Package	TO-220AB							
Circuit configuration	Common cathode							







RoHS

COMPLIANT

HALOGEN

FREE





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise noted)										
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT				
	I <sub>F</sub> = 5 A		V <sub>F</sub> <sup>(1)</sup>	0.45	-					
	I <sub>F</sub> = 15 A	T <sub>J</sub> = 25 °C		0.52	-	V				
Instantaneous forward voltage per diode	I <sub>F</sub> = 30 A			0.60	0.66					
	I <sub>F</sub> = 5 A			0.33	-					
	I <sub>F</sub> = 15 A	T <sub>J</sub> = 125 °C		0.44	-					
	I <sub>F</sub> = 30 A			0.55	0.60					
Reverse current at rated V <sub>R</sub> per diode	V - 60 V	T <sub>J</sub> = 25 °C	I <sub>B</sub> <sup>(2)</sup>	-	0.6	mA				
neverse current at rated VR per diode	V <sub>R</sub> = 60 V	T <sub>J</sub> = 125 °C	<sup>I</sup> R <sup>(-)</sup>	9	40					
Typical junction capacitance	4.0 V, 1 MHz		CJ	3800	-	pF				

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER SYMBOL VX60M60C							
R <sub>θJC</sub> <sup>(1)</sup>	1	°C/W					
	SYMBOL	SYMBOL VX60M60C					

#### Note

<sup>(1)</sup> Thermal resistance junction-to-case to follow JEDEC<sup>®</sup> 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)									
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE					
VX60M60C-M3/P	2.04	Р	50/tube	Tube					
VX60M60CHM3/P (1)	2.04	Р	50/tube	Tube					

Note

(1) AEC-Q101 qualified



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

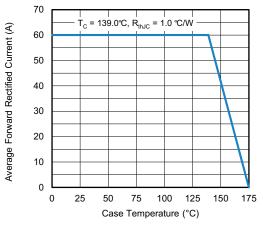


Fig. 1 - Maximum Forward Current Derating Curve

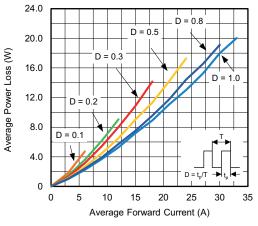


Fig. 2 - Average Power Loss Characteristics

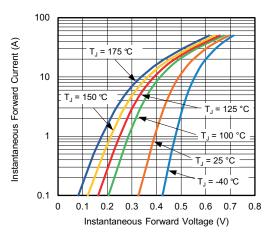


Fig. 3 - Typical Instantaneous Forward Characteristics

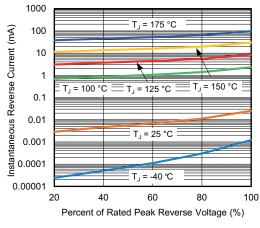


Fig. 4 - Typical Reverse Leakage Characteristics

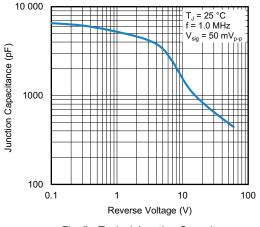


Fig. 5 - Typical Junction Capacitance

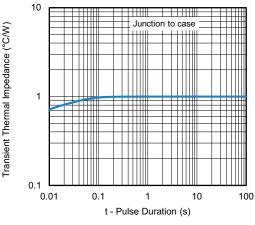


Fig. 6 - Typical Transient Thermal Impedance

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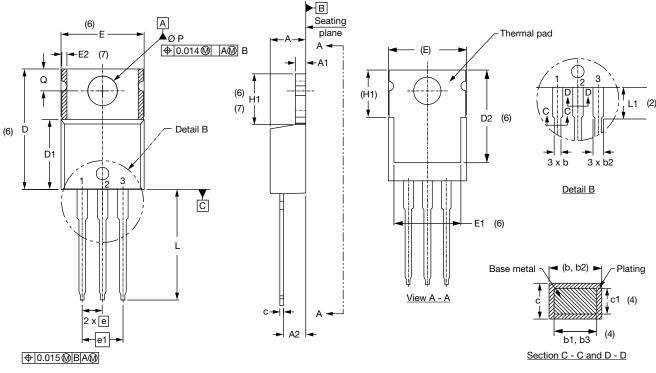
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## VX60M60C

Vishay General Semiconductor



#### DIMENSIONS in millimeters (inches) TO-220AB



Lead tip

Conforms to JEDEC® outline TO-220AB

SAMBOI	MILLIM	IETERS	INC	HES	NOTES		CYMPOL	MILLIMETERS		INCHES		NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	JIES	SYMBOL	MIN.	MAX.	MIN.	MIN. MAX.	NOTES
А	4.25	4.65	0.167	0.183			D2	11.68	12.88	0.460	0.507	6
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.56	2.92	0.101	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			E2	-	0.76	-	0.030	7
b1	0.38	0.97	0.015	0.038	4		е	2.41	2.67	0.095	0.105	
b2	1.20	1.73	0.047	0.068			e1	4.88	5.28	0.192	0.208	
b3	1.14	1.73	0.045	0.068	4		H1	5.84	6.86	0.230	0.270	6, 7
С	0.36	0.61	0.014	0.024			L	13.52	14.02	0.532	0.552	
c1	0.36	0.56	0.014	0.022	4	]	L1	3.32	3.82	0.131	0.150	2
D	14.85	15.25	0.585	0.600	3	]	ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355		]	Q	2.60	3.00	0.102	0.118	

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Dimension b1, b3 and c1 apply to base metal only

<sup>(5)</sup> Controlling dimensions: inches

<sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2 and E1

- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC<sup>®</sup> TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

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