VCAN36C2-03G

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Vishay Semiconductors

Bidirectional Symmetrical (BiSy) Low Capacitance, Dual-Line ESD Protection Diode in SOT-323

FEATURES

Small SOT-323 package
2-line ESD protection
Working range ± 36 V

For CAN FD and FLEX-bus applications

• Low load capacitance $C_D < 4.6 \text{ pF}$ at $V_R = 5 \text{ V}$

please see <u>www.vishay.com/doc?99912</u>

• ESD capability according to AEC-Q101: human body

• Material categorization: for definitions of compliance

Low leakage current I_R < 0.05 μA

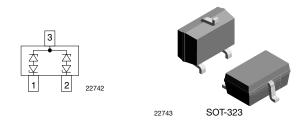
• ESD immunity acc. IEC 61000-4-2

± 27 kV contact discharge ± 27 kV air discharge

model: class H3B: > 8 kV

• e3 - pins plated with tin (Sn)

• AEC-Q101 qualified available



MARKING (example only)

SHA



ABC = type code (see table below) WW = date code working week VY = date code year

LINKS TO ADDITIONAL RESOURCES



ORDERING INFORMATION								
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE				PACKAG	ING CODE		
	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE) 15K/BOX = MOQ	10K PER 13" REEL (8 mm TAPE) 10K/BOX = MOQ	ORDERING CODE (EXAMPLE)	
		STANDARD	GREEN		ISK/BOX = MOQ			
VCAN36C2-03G	-	E		3	-08		VCAN36C2-03G-E3-08	
VCAN36C2-03G	Н	E		3	-08		VCAN36C2-03GHE3-08	
VCAN36C2-03G	-	E		3		-18	VCAN36C2-03G-E3-18	
VCAN36C2-03G	Н	E		3		-18	VCAN36C2-03GHE3-18	

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VCAN36C2-03G	SOT-323	36C	5.65 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS SY		VALUE	UNIT			
Peak pulse current	T_A = 25 °C, acc. IEC 61000-4-5; t_p = 8/20 µs; single shot	I _{PPM}	1.8	А			
Peak pulse power	T_A = 25 °C; pin 1 or 2 to pin 3; acc. IEC 61000-4-5; t_p = 8/20 μs ; single shot	P _{PP}	120	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 \text{ °C}$	M	± 27	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 ^\circ\text{C}$	V _{ESD}	± 27	kV			
Operating temperature	Junction temperature	TJ	-55 to +175	°C			
Storage temperature		T _{STG}	-55 to +175	°C			

Pb-free



COMPLIANT

Rev. 1.0, 27-Oct-2021

For technical questions, contact: ESDprotection@vishay.com

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ELECTRICAL CHARACTERISTICS (pin 1 to 3, 3 to 1, 2 to 3, or 3 to 2) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	V _{RWM}	-	-	36	V		
Reverse voltage	At I _R = 0.05 μA	V _R	36	-	-	V		
Reverse current	At V _{RWM} = 36 V	I _R	-	-	0.05	μA		
Reverse breakdown voltage	At I _R = 1 mA	V _{BR}	39	42	45	V		
Devene elemenia e velha e e	At I _{PP} 1 A; t _p = 8/20 μs	V _C	-	48	55	V		
Reverse clamping voltage	At I _{PP} = I _{PPM} = 1.8 A; t _p = 8/20 μs	V _C	-	- 48 55 - 54 66	V			
	At $V_R = 0 V$, f = 1 MHz	CD	-		pF			
Capacitance	At $V_R = 5 V$, f = 1 MHz	CD	-	3.8	4.6	pF		
	Diode capacitance matching at $V_R = 5 V$, $C_{D13} vs. C_{D23}$	dC _D	-	-	2	%		

TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)

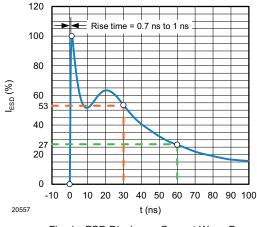
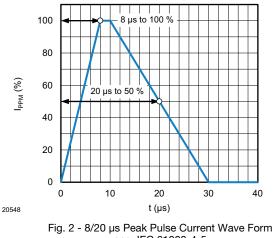


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)



acc. IEC 61000-4-5

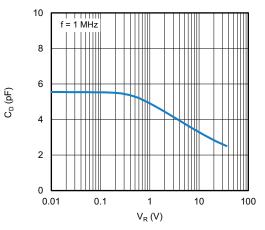
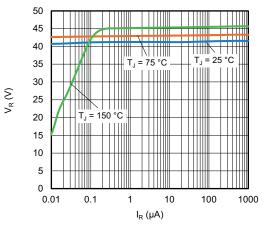
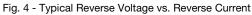


Fig. 3 - Typical Capacitance vs. Reverse Voltage





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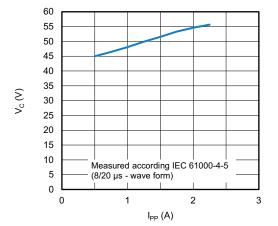


Fig. 5 - Typical Peak Clamping Voltage vs. Peak Pulse Current

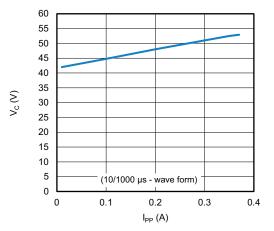


Fig. 6 - Typical Peak Clamping Voltage vs. Peak Pulse Current

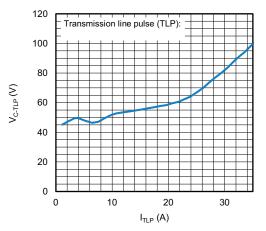


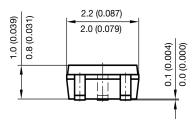
Fig. 7 - Typical Clamping Voltage vs. Peak Pulse Current

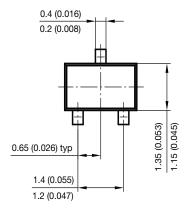
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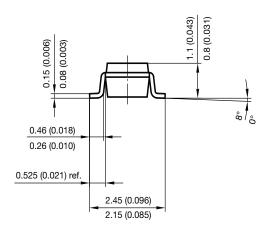
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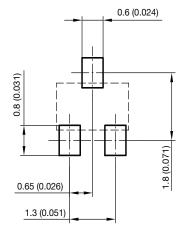
PACKAGE DIMENSIONS in millimeters (inches) SOT-323





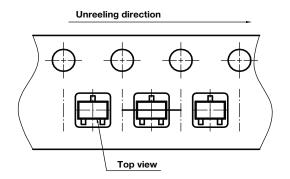


foot print recommendation:



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ORIENTATION IN CARRIER TAPE SOT-323



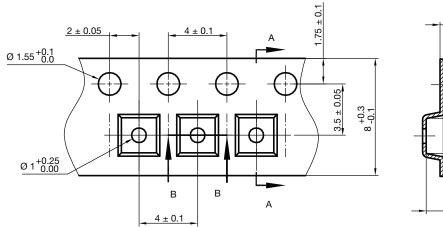
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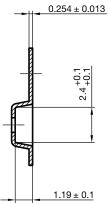
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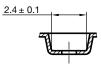
CARRIER TAPE SOT-323





A-A Section

B-B Section



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