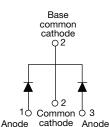


Vishay Semiconductors

# High Performance Schottky Rectifier, 2 x 20 A



D<sup>2</sup>PAK (TO-263AB)



TO-262AA Base common cathode 0 2 10 Common 0 3 Anode cathode Anode

VS-MBRB4045CTHM3

VS-MBR4045CT-1HM3

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 20 A							
V <sub>R</sub>	45 V							
V <sub>F</sub> at I <sub>F</sub>	0.58 V							
I <sub>RM</sub> max.	95 mA at 125 °C							
T <sub>J</sub> max.	150 °C							
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							
E <sub>AS</sub>	20 mJ							

### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform (per device)	40	٨						
I <sub>FRM</sub>	T <sub>C</sub> = 117 °C (per leg)	40	A						
V <sub>RRM</sub>		45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	900	А						
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.58	V						
TJ	Range	-65 to +150	°C						

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-MBRB4045CTHM3, VS-MBR4045CT-1HM3 UN							
Maximum DC reverse voltage	V <sub>R</sub>	45	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	45	v				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS			
Maximum average per leg	l=	$T_{\rm C} = 118 ^{\circ}\text{C}$ , rated $V_{\rm B}$						
forward current per device	I <sub>F(AV)</sub>	$T_{\rm C} = 118$ C, rated $v_{\rm R}$		40				
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 117 °C		^			
Maximum peak one cycle non-repetitive	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	900	A			
peak surge current per leg		10 ms sine or 6 ms rect. pulse	rated $V_{\text{RRM}}$ applied	210				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 4.4 mH		20	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu s$ Frequency limited by $T_J$ maximum $V_A$ = 1.5 x $V_R$ typical		3	А			

Revision: 01-Aug-2023

Document Number: 94721

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

1



www.vishay.com

## Vishay Semiconductors

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop		20 A	T, = 25 °C	0.60				
	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 0	0.80	v			
	V FM \''	20 A	T, = 125 °C	0.58				
		40 A	1j=125 C	0.80				
•• • • • •	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1				
Maximum instantaneous reverse current		T <sub>J</sub> = 100 °C	Rated DC voltage	50	mA			
		T <sub>J</sub> = 125 °C		95				
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		900	pF			
Typical series inductance	L <sub>S</sub>	Measured from top of terr	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

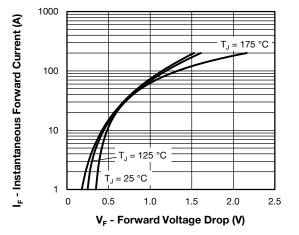
#### Note

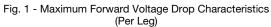
<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range	TJ		-65 to +150	°C				
Maximum storage temperature range	T <sub>Stg</sub>		-65 to +175	-0				
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	1.5					
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (only for TO-220)	0.50	°C/W				
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation (for D <sup>2</sup> PAK (TO-263AB) and TO-262AA)	50					
Approvimento unight			2	g				
Approximate weight			0.07	oz.				
Mounting torgue		Non-lubricated threads	6 (5)	kgf · cm (lbf · in)				
Mounting torque maximum		Non-Iubricated threads	12 (10)					
Marking davias		Case style D <sup>2</sup> PAK (TO-263AB)	MBRB4045CTH					
Marking device		Case style TO-262AA	MBR4045CT-1H					



**Vishay Semiconductors** 





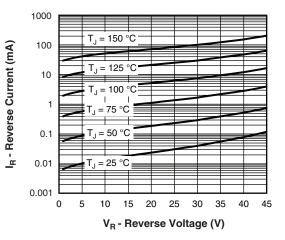


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

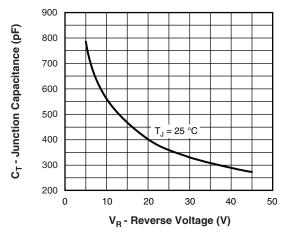
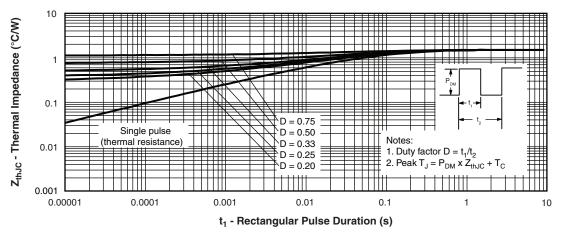


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





 Revision: 01-Aug-2023
 Document Number: 94721

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 DiodesAsia@vishay.com, DiodesEurope@vishay.com

 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



**Vishay Semiconductors** 

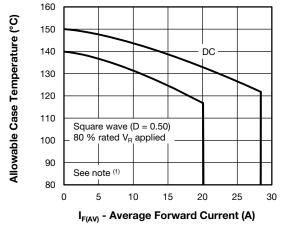


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

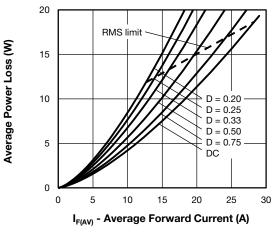


Fig. 6 - Forward Power Loss Characteristics

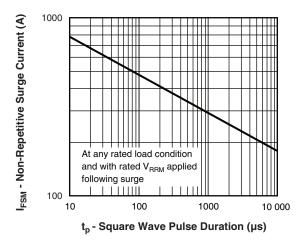


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

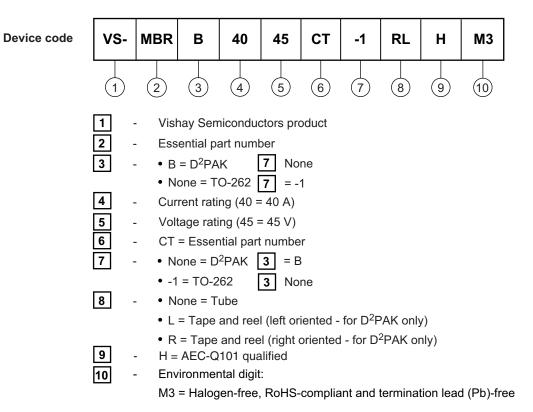
#### Note

 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd\_{REV} = inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = rated  $V_R$ 



**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-MBRB4045CTHM3	50	1000	Antistatic plastic tube						
VS-MBR4045CT-1HM3	50	1000	Antistatic plastic tube						
VS-MBRB4045CTLHM3	800	800	13" diameter reel						
VS-MBRB4045CTRHM3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS							
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95046					
Dimensions	TO-262AA	www.vishay.com/doc?95419					
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444					
	TO-262AA	www.vishay.com/doc?95443					
Packaging information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95032					

# **Outline Dimensions**



D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

Revision: 08-Jul-15

1

## **Outline Dimensions**



**Vishay Semiconductors** 

**TO-262** 

### **DIMENSIONS** in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

3. - Anode

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.