Trench MOS Schottky technology

- Low forward voltage drop, low power losses
- 10 s per JESD 22-B106
- AEC-Q101 gualified available Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3_X - halogen-free, RoHS-compliant, and AEC-Q101 gualified

("_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Mounting torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)									
PARAMETER		SYMBOL	VX30202C	UNIT					
Maximum repetitive peak reverse voltage		V _{RRM}	V _{RRM} 200						
Maximum average forward rectified current	per device		30	А					
(fig. 1)	per diode	I _{F(AV)}	15						
Peak forward surge current 8.3 ms single half si superimposed on rated load	ne-wave	I _{FSM}	260	A					
Operating junction temperature range		T _J ⁽¹⁾	-40 to +175	°C					
Storage temperature range		T _{STG}	-40 to +175						

Note

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta,JA}$

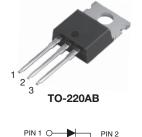
Dual High Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low V_F = 0.54 V at I_F = 5.0 A

FEATURES

- · High efficiency operation
- Solder bath temperature 275 °C maximum,

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PIN 3 O

CASE

PRIMARY CHARACTERISTICS									
I _{F(AV)}	2 x 15 A								
V _{RRM}	200 V								
I _{FSM}	260 A								
V_{F} at I_{F} = 15 A (T_{J} = 125 °C)	0.65 V								
T _J max.	175 °C								
Package	TO-220AB								
Circuit configuration	Common cathode								





HALOGEN

FREE

VX30202C





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ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)									
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT			
	$I_F = 5 A$		V _F ⁽¹⁾	0.69	-				
	I _F = 10 A	T _J = 25 °C		0.75	-				
Instantaneous forward valtage per diade	I _F = 15 A			0.78	0.84	v			
Instantaneous forward voltage per diode	$I_F = 5 A$			0.54	-	v			
	I _F = 10 A	T _J = 125 °C		0.60	-				
	I _F = 15 A			0.65	0.70				
	V _R = 160 V	T _J = 25 °C		0.0012	-				
Reverse current at rated V_R per diode	v _R = 100 v	T _J = 125 °C		1.7	-	mA			
	V _R = 200 V	T _J = 25 °C	IR ()	-	0.1	IIIA			
	$v_{\rm R} = 200 v$	T _J = 125 °C		4	17				
Typical junction capacitance	4.0 V, 1 MHz		CJ	1200	-	pF			

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	VX30202C	UNIT				
Typical thermal resistance per device	R _{θJC} ⁽¹⁾	1	°C/W				

Note

⁽¹⁾ Thermal resistance junction-to-case to follow JEDEC[®] 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)									
PREFERRED P/N	RRED P/N UNIT WEIGHT (g) PACKAGE CODE BASE QUANTITY DELIVER								
VX30202C-M3/P	2.03	Р	50/tube	Tube					
VX30202CHM3_A/P (1)	2.03	Р	50/tube	Tube					

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

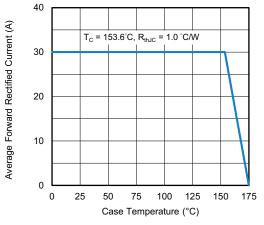


Fig. 1 - Maximum Forward Current Derating Curve

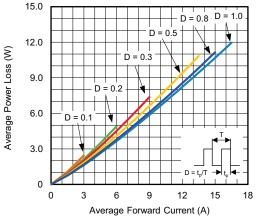


Fig. 2 - Average Power Loss Characteristics

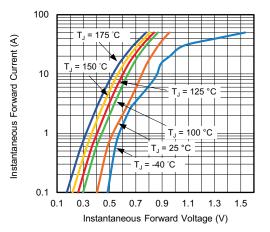


Fig. 3 - Typical Instantaneous Forward Characteristics

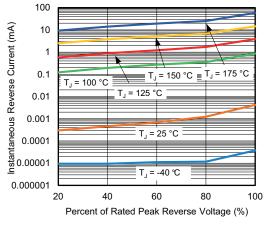


Fig. 4 - Typical Reverse Leakage Characteristics

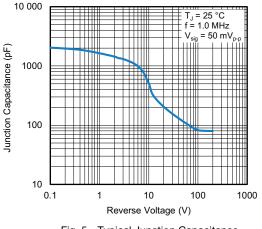


Fig. 5 - Typical Junction Capacitance

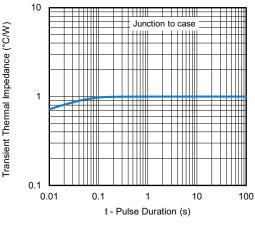


Fig. 6 - Typical Transient Thermal Impedance

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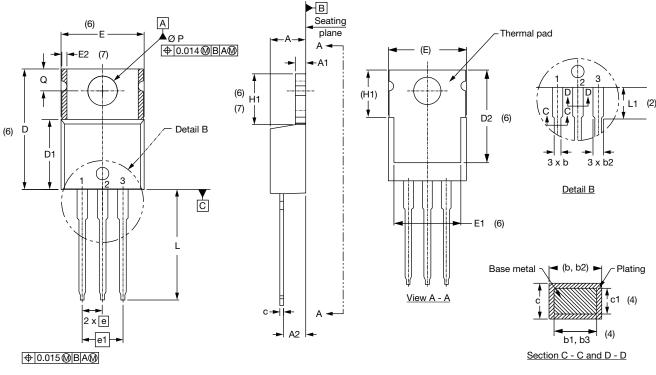
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VX30202C

Vishay General Semiconductor



DIMENSIONS in millimeters (inches) TO-220AB



Lead tip

Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIN	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INCHES		NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	.5	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183			D2	11.68	12.88	0.460	0.507	6
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.56	2.92	0.101	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			E2	-	0.76	-	0.030	7
b1	0.38	0.97	0.015	0.038	4		е	2.41	2.67	0.095	0.105	
b2	1.20	1.73	0.047	0.068			e1	4.88	5.28	0.192	0.208	
b3	1.14	1.73	0.045	0.068	4		H1	5.84	6.86	0.230	0.270	6, 7
С	0.36	0.61	0.014	0.024			L	13.52	14.02	0.532	0.552	
c1	0.36	0.56	0.014	0.022	4		L1	3.32	3.82	0.131	0.150	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Dimension b1, b3 and c1 apply to base metal only

⁽⁵⁾ Controlling dimensions: inches

⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1

 $^{(7)}$ Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed

(8) Outline conforms to JEDEC[®] TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

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