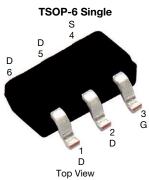
Vishay Siliconix

P-Channel 12 V (D-S) MOSFET



Marking code: AR

PRODUCT SUMMARY						
V _{DS} (V)	-12					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5 \text{ V}$	0.022					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -2.5 \text{ V}$	0.028					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -1.8 \text{ V}$	0.036					
Q _g typ. (nC)	26					
I _D (A) ^a	-8					
Configuration	Single					

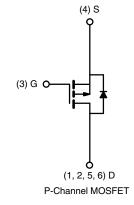
FEATURES

- TrenchFET® power MOSFET
- PWM optimized
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



APPLICATIONS

- · Load switch
- PA switch



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free	Si3473CDV-T1-E3
Lead (Pb)-free and halogen-free	Si3473CDV-T1-GE3

ABSOLUTE MAXIMUM RATING	iS (T _A = 25 °C, u	nless otherv	wise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-12	V	
Gate-source voltage		V_{GS}	± 8	v	
	T _C = 25 °C		-8 a		
Continuous drain surrent /T 150 °C)	T _C = 70 °C	1 .	-8 ^a		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	I _D	-8 a, b, c		
	T _A = 70 °C		-6.5 ^{b, c}	Α	
Pulsed drain current		I _{DM}	-20		
Continuous source-drain diode current	T _C = 25 °C		-3.5		
Continuous source-drain diode current	T _A = 25 °C	l _S	-1.67 ^{b, c}		
	T _C = 25 °C		4.2		
Marrian and a super displace	T _C = 70 °C		2.7	w	
Maximum power dissipation	T _A = 25 °C	P _D	2 b, c	VV	
	T _A = 70 °C	1	1.3 ^{b, c}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 5 s	R _{thJA}	55	62.5	°C/W	
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	25	30	C/VV	

Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 110 $^{\circ}\text{C/W}$

Vishay Siliconix

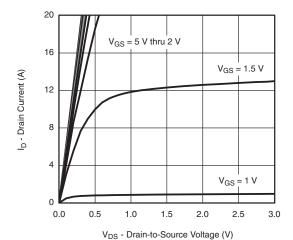
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	1 01111202		1		1111111	
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = -250 μA	-12	-	_	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	•	-	-13	-	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = -250 \mu A$	-	3	-	mV/°C
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	-0.4	-	-1	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 100	nA
		V _{DS} = -12 V, V _{GS} = 0 V	-	-	-1	μΑ
Zero gate voltage drain current	I _{DSS}	V _{DS} = -12 V, V _{GS} = 0 V, T _J = 85 °C	-	-	-10	
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-20	-	-	Α
	, ,	$V_{GS} = -4.5 \text{ V}, I_D = -8.1 \text{ A}$	-	0.016	0.022	
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -7.1 \text{ A}$	-	0.021	0.028	Ω
		$V_{GS} = -1.8 \text{ V}, I_D = -2.8 \text{ A}$	-	0.026	0.036	
Forward transconductance a	9 _{fs}	$V_{DS} = -6 \text{ V}, I_D = -8.1 \text{ A}$	-	30	-	S
Dynamic ^b						
Input capacitance	C _{iss}		-	2010	-	
Output capacitance	C _{oss}	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	580	-	pF
Reverse transfer capacitance	C _{rss}		-	520	-	1
		$V_{DS} = -6 \text{ V}, V_{GS} = -8 \text{ V}, I_{D} = -8.1 \text{ A}$	-	43	65	nC
Total gate charge	Qg		-	26	40	
Gate-source charge	Q _{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -8.1 \text{ A}$	-	3.3	-	
Gate-drain charge	Q _{qd}		-	7.5	-	
Gate resistance	R_g	f = 1 MHz	-	4.8	-	Ω
Turn-on delay time	t _{d(on)}		-	20	30	
Rise time	t _r	V_{DD} = -6 V, R_L = 0.92 Ω	-	55	85	
Turn-off delay time	t _{d(off)}	$I_D\cong$ -6.5 A, $V_{GEN}=$ -4.5 V, $R_g=$ 1 Ω	-	60	90	
Fall time	t _f		-	40	60	1
Turn-on delay time	t _{d(on)}		-	10	15	ns -
Rise time	t _r	V_{DD} = -6 V, R_L = 0.92 Ω	-	15	25	
Turn-off delay time	t _{d(off)}	$I_D \cong$ -6.5 A, $V_{GEN} =$ -8 V, $R_g =$ 1 Ω	-	62	95	
Fall time	t _f		-	35	55	1
Drain-Source Body Diode Characteristic	s					
Continuous source-drain diode current	Is	T _C = 25 °C	-	-	-3.5	
Pulse diode forward current ^a	I _{SM}		-	-	-20	A
Body diode voltage	V _{SD}	I _S = -5.9 A	-	-0.8	-1.2	V
Body diode reverse recovery time	t _{rr}	-	-	50	75	ns
Body diode reverse recovery charge	Q _{rr}	$I_F = -6.5 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	30	45	nC
Reverse recovery fall time	t _a	$T_J = 25 ^{\circ}\text{C}$	-	18	-	
Reverse recovery rise time	t _b	-ta		32		ns

Notes

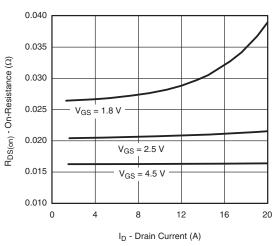
- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

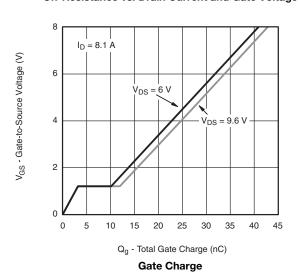


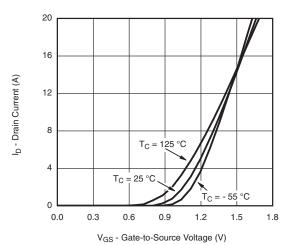


Output Characteristics

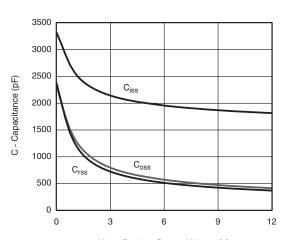


On-Resistance vs. Drain Current and Gate Voltage



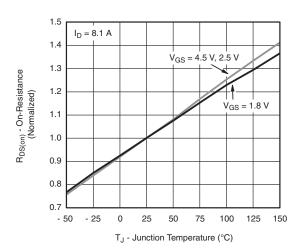


Transfer Characteristics



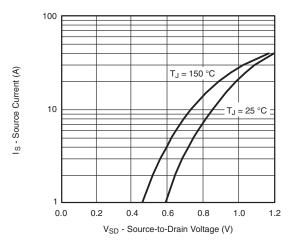
V_{DS} - Drain-to-Source Voltage (V)

Capacitance

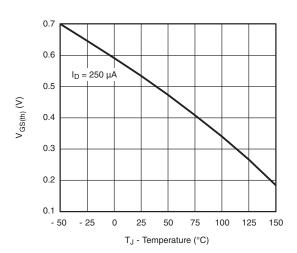


On-Resistance vs. Junction Temperature

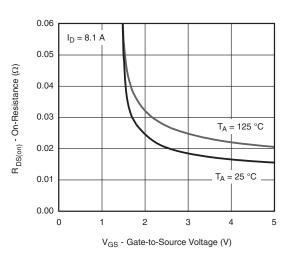




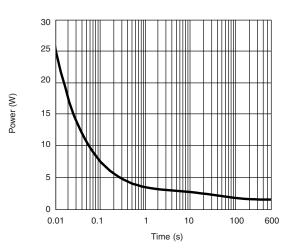
Source-Drain Diode Forward Voltage



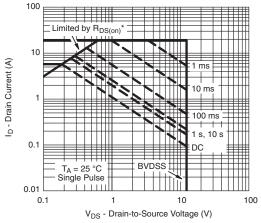
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



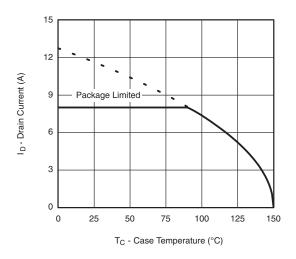
Single Pulse Power

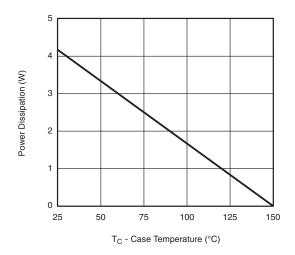


 * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area







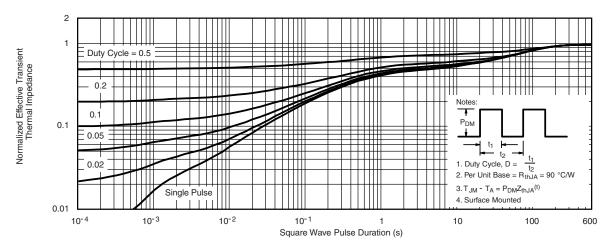
Current Derating a

Power, Junction-to-Foot

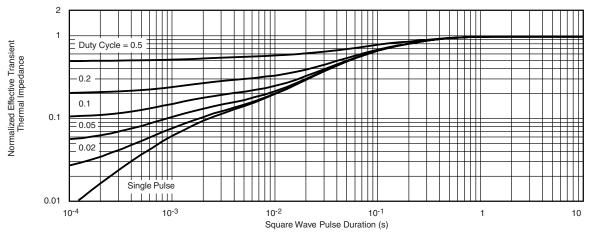
Note

a. The power dissipation P_D is based on T_J max = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

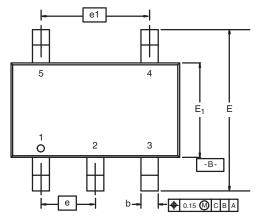
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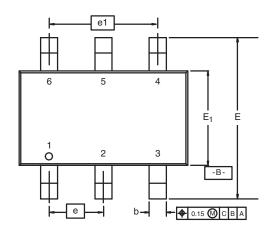




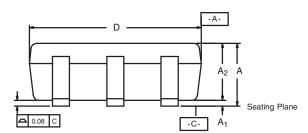
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C

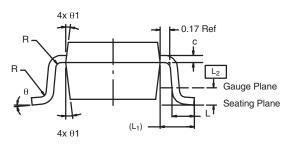




5-LEAD TSOP





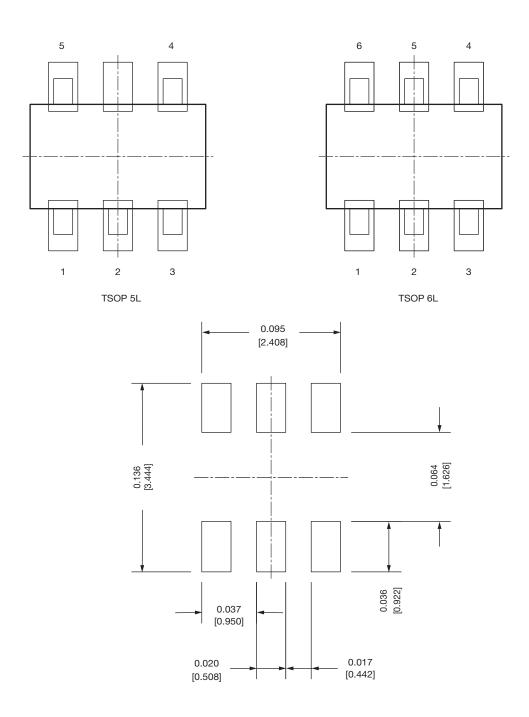


	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200 18-Dec-06



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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