

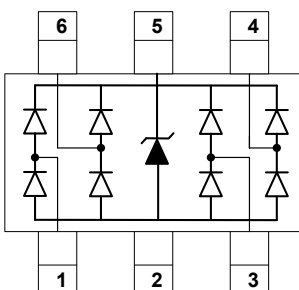
Features

- ◆ Ultra low capacitance: 0.6pF typical
- ◆ Ultra low leakage: nA level
- ◆ Low operating voltage: 5.0V
- ◆ Up to 4 data lines and one power line protects
- ◆ Low clamping voltage
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
Air : ±20kV; discharge: ±15kV
 - IEC61000-4-5 (Lightning) 4A (8/20μs)
- ◆ SOT-363Package
- ◆ RoHS Compliant

Description

The ESDA5U0R1T6 is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The ESDA5U0R1T6 has an ultra-low capacitance with a typical value at 0.6 pF, and complies with the IEC 61000-4-2 (ESD) standard with ±20kV air and ±15kV contact discharge. It is assembled into a 6-pin lead-free SOT-363 package. The combination of small size, ultra low capacitance, and high ESD surge capability make it ideal for use in applications such as multimedia, and other high speed ports.

Circuit Diagram



Applications

- ◆ Monitors and flat panel displays
- ◆ Set-top box and Digital TV
- ◆ Video graphics cards
- ◆ Digital Video Interface (DVI)
- ◆ Notebook Computers
- ◆ PCI Express and Serial SATA Ports

Absolute Maximum Ratings : ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

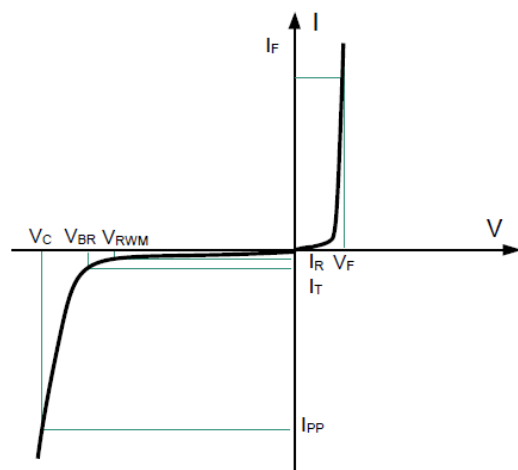
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs , I/O-GND)	Ppk	60	W
Peak Pulse Power (8/20 μs , Vcc-GND)	Ppk	270	W
Peak Pulse Current (8/20 μs , I/O-GND)	IPP	4.0	A
Peak Pulse Current (8/20 μs , Vcc-GND)	IPP	15.0	A
ESD per IEC 61000-4-2 (Air)	$V_{\text{ESD}, \text{VDD}}$	± 20	kV
ESD per IEC 61000-4-2 (Contact)	$V_{\text{ESD}, \text{I/O}}$	± 15	kV
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics : ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

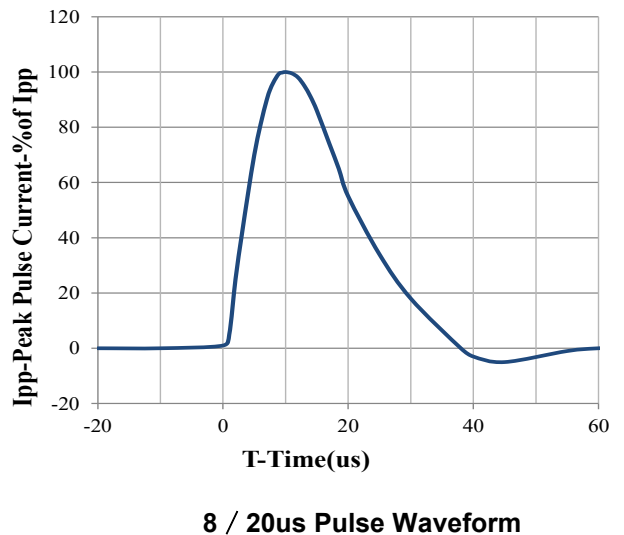
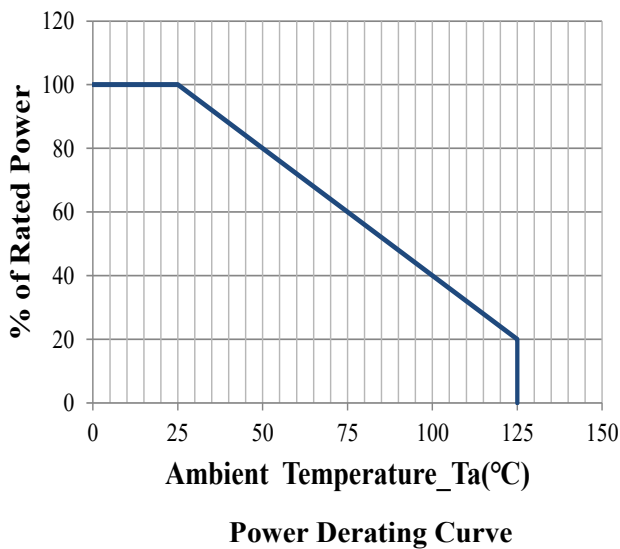
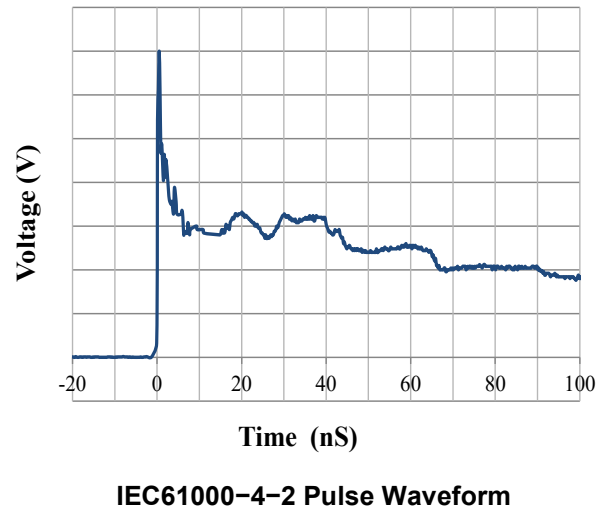
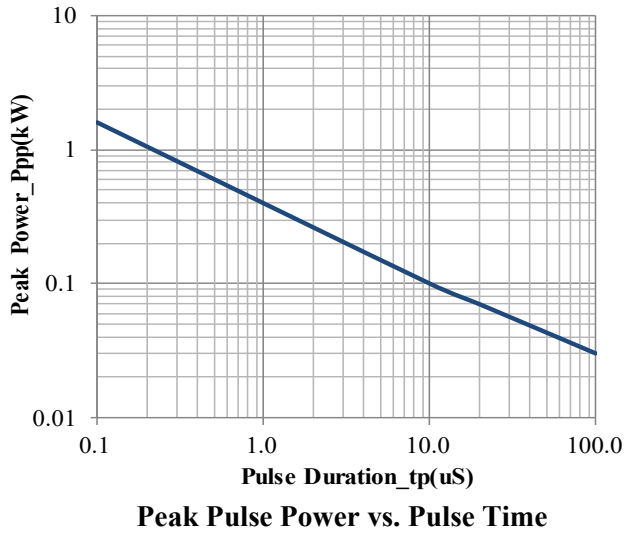
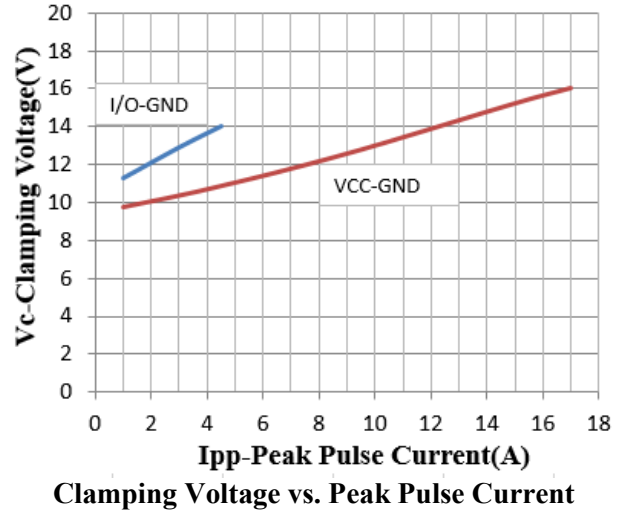
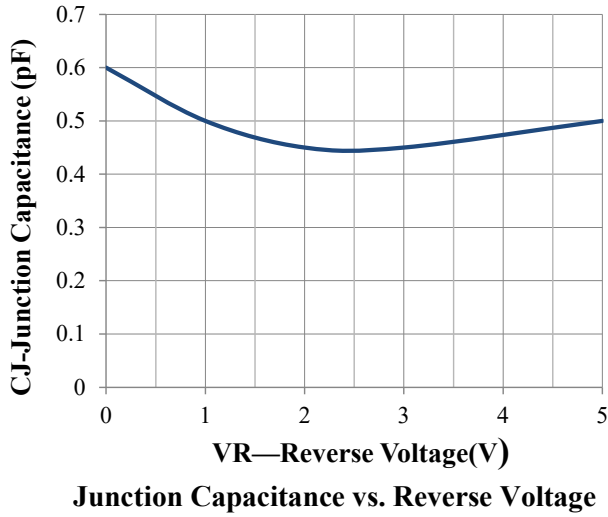
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}	Pin 5 to GND, I/O-GND			5.0	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$, I/O-GND	6.5	7.4	8.0	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$, I/O-I/O	7.0	8.3	9.0	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$, VCC-GND	6.0	7.0	8.0	V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 5\text{V}$			0.1	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 / 20 μs pulse), I/O-GND		8.0	12.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 4\text{A}$ (8 / 20 μs pulse), I/O-GND		11.0	15.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 / 20 μs pulse), I/O-I/O		10.0	12.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 4\text{A}$ (8 / 20 μs pulse), I/O-I/O		19.0	21.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 / 20 μs pulse), VCC-GND		7.5	9.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 15\text{A}$ (8 / 20 μs pulse), VCC-GND			18.0	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$, I/O-GND		0.6	0.8	pF
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$, I/O-I/O		0.3	0.4	pF

Portion Electronics Parameter

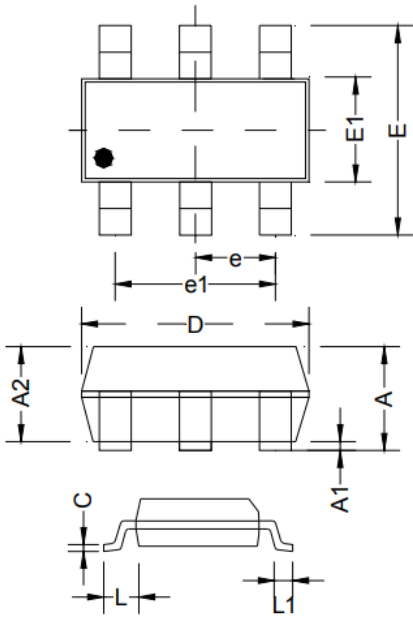
Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_{R}	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_{T}
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{PP}
V_{F}	Forward Voltage @ I_{F}



Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

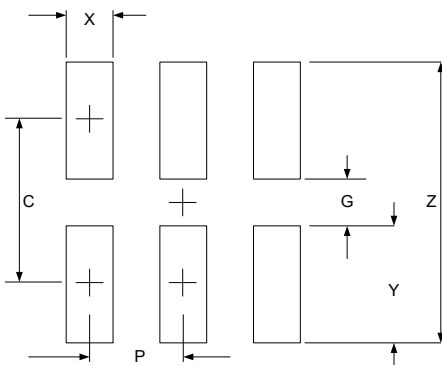


SOT-363 Package Outline Drawing



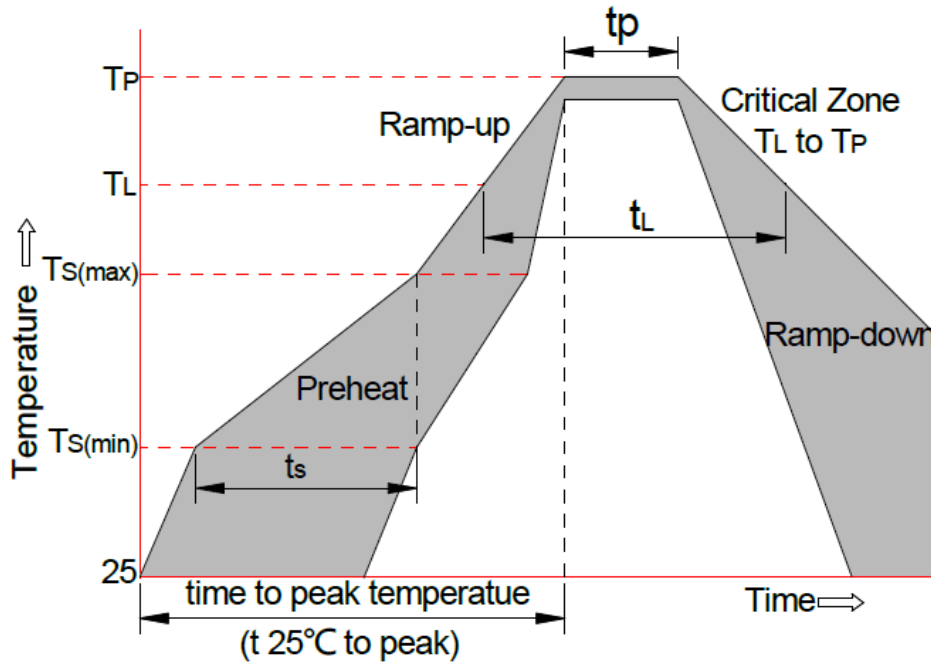
SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.00	0.03	0.10	0.000	0.001	0.004
A2	0.90	0.95	1.00	0.035	0.037	0.039
D	2.00	2.10	2.20	0.079	0.083	0.087
E1	1.15	1.20	1.35	0.045	0.047	0.053
E	2.15	-	2.45	0.085	-	0.096
e	0.65 Typ.			0.026 Typ.		
e1	1.2	1.3	1.4	0.047	0.051	0.055
L	0.525 Ref.			0.021 Ref.		
L1	0.15	-	0.46	0.006	-	0.018

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
C	1.94	0.076
G	1.14	0.045
P	0.65	0.026
X	0.4	0.016
Y	0.8	0.031
Z	2.74	0.108

Soldering Parameters



Reflow Condition		Pb-Free Assembly
Pre-heat	-Temperature Min (Ts (min))	+150°C
	-Temperature Max (Ts (max))	+200°C
	-Time (Min to Max) (ts)	60-180 secs
Average ramp up rate(Liquid us Temp (TL) to peak)		3°C/sec. Max
Ts (max) to TL-Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature (TL) (Liquid us)	+217°C
	-Temperature (tL)	60-150 secs
Peak Temp (Tp)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (tp)		30 secs. Max
Ramp-down Rate		6 °C/secs. Max
xTime 25°C to Peak Temp (TP)		8 min. Max
Do not exceed		+260°C