

## CTMLP100805F

### Series

From 0.47 $\mu$ H to 4.7 $\mu$ H



#### CHARACTERISTICS

**Description:** SMD Multi-layer Power Chip Inductor. Magnetically shielded.

**Applications:** Cellular phones, Power modules, DC/DC converters, Digital cameras, DVD, Portable devices, etc.

**Operating Temperature:** -55°C to 125°C (including self - temp. rise).

**Isat:** Inductance drop 30% from its value with current.

**Irms:** 40°C temperature rise from 25°C ambient with current.

**Inductance Tolerance:**  $\pm 20\%$  (M),  $\pm 30\%$  (N)

**Packaging:** Tape & Reel.

**Miscellaneous:** **RoHS Compliant.**

**Additional Information:** Additional electrical & physical information available upon request.

**Samples available. See website for ordering information.**

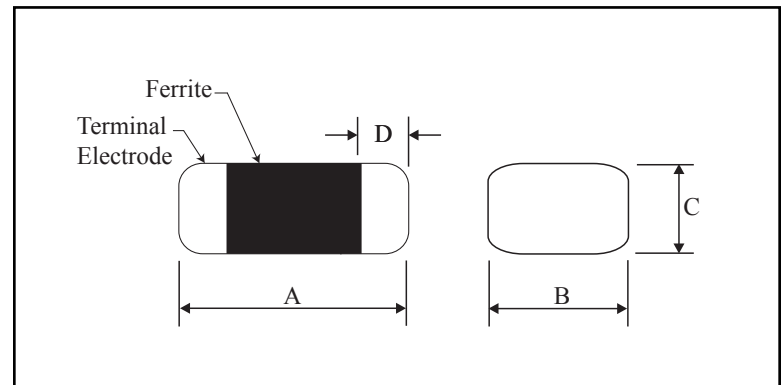
#### SPECIFICATIONS

Please specify tolerance code when ordering.  
CTMLP100805F-R47\_ ← M =  $\pm 20\%$ , N =  $\pm 30\%$

Part Number	Inductance ( $\mu$ H)	Test Frequency	DCR $\pm 30\%$ ( $\Omega$ )	Isat Max. (mA)	Irms Max. (mA)
CTMLP100805F-R47_	0.47	3.0MHz, 200mV	0.04	1500	1800
CTMLP100805F-1R0_	1.0	3.0MHz, 200mV	0.05	950	1600
CTMLP100805F-1R5_	1.5	3.0MHz, 200mV	0.07	900	1400
CTMLP100805F-2R2_	2.2	3.0MHz, 200mV	0.10	700	1200
CTMLP100805F-3R3_	3.3	3.0MHz, 200mV	0.12	500	1100
CTMLP100805F-4R7_	4.7	3.0MHz, 200mV	0.14	350	1000

#### PHYSICAL DIMENSIONS

Size	A	B	C Max.	D
mm	2.5 $\pm$ 0.20	2.0 $\pm$ 0.20	1.20	0.6 $\pm$ 0.2
inches	0.098 $\pm$ 0.008	0.079 $\pm$ 0.008	0.047	0.024 $\pm$ 0.008



#### PAD LAYOUT

