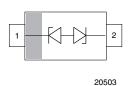


Low Capacitance, Single-Line ESD Protection Diode in SOD-323





MARKING (example only)



STANDARD

Ε

Е

Е

161

GREEN

4.30 mg

XYZ = type code (see table below) bar = pin 1

LINKS TO ADDITIONAL RESOURCES

Н

SOD-323



VLIN1616-02G

VLIN1616-02G

VLIN1616-02G

VLIN1616-02G



FEATURES

- For LIN-bus applications
- Small SOD-323 package
- 1-line ESD protection
- Working range: ± 16 V
- Low leakage current I_R < 0.05 μA
- Low load capacitance C_D < 24 pF
- ESD protection acc. IEC 61000-4-2 ± 30 kV contact discharge
 - ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- AEC-Q101 qualified available

15K/BOX = MOQ

-08

-08

• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

10K/BOX = MOQ

-18

MSL level 1

(according J-STD-020)

VLIN1616-02G-E3-08

VLIN1616-02GHE3-08

VLIN1616-02G-E3-18

Peak temperature

max. 260 °C

3D Models	Models						
ORDERING	G INFORMA	TION					
	ENVI	ENVIRONMENTAL AND QUALITY CODE			PACKAGING CODE		
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS	TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
(EXAMILE)	-/ QUALIFIED		FLAIED	15K/DOV - MOO	10K/DOV - MOO		

VLIN1616-02G	Н	E	-	3	-	-18	VLIN1616-02GHE3-18	
PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING O		MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	

UL 94 V-0

3

3

3

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	$T_A = 25$ °C; acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	I _{PPM}	6	Α			
Peak pulse power	T _A = 25 °C; acc. IEC 61000-4-5; t _p = 8/20 μs; single shot	P _{PP}	200	W			
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses; T _A = 25 °C	V	± 30	kV			
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses; T _A = 25 °C	V _{ESD} ± 30		kV			
Operating temperature	Junction temperature	TJ	-55 to +150	°C			
Storage temperature		T _{STG}	-55 to +150	°C			



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	16	V		
Reverse voltage	At I _R = 0.05 μA	V_R	16	-	-	V		
Reverse current	At $V_{RWM} = 16 V$	I _R	-	-	0.05	μΑ		
Reverse breakdown voltage	At I _R = 1 mA	V_{BR}	17.1	18.6	20	V		
Payaraa alamping valtage	At I _{PP} 1 A; t _p = 8/20 μs	V _C	-	22	25	V		
Reverse clamping voltage	At $I_{PP} = I_{PPM} = 6 \text{ A}$; $t_p = 8/20 \mu\text{s}$	V _C	-	29	33	V		
Capacitance	At $V_R = 0 V$, $f = 1 MHz$	C _D	-	18	24	pF		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

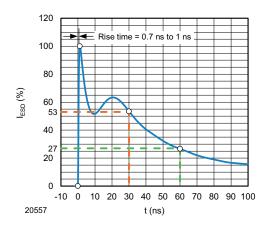


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

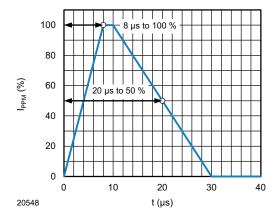


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

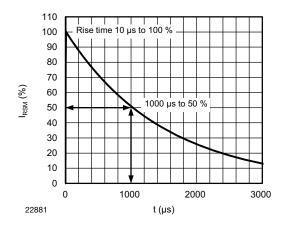


Fig. 3 - $10/1000 \ \mu s$ Peak Pulse Current Wave Form

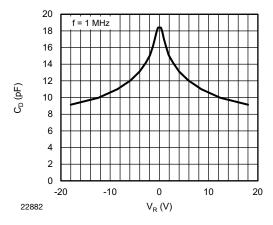


Fig. 4 - Typical Capacitance C_D vs. Reverse Voltage V_R



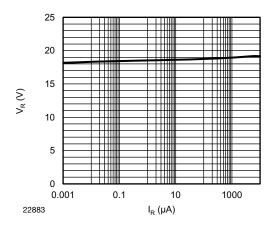


Fig. 5 - Typical Reverse Voltage V_{R} vs. Reverse Current I_{R}

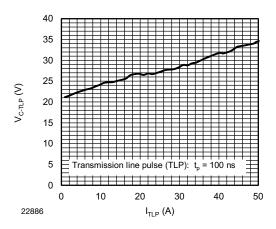


Fig. 8 - Typical Clamping Voltage V_{C-TLP} vs. Pulse Current I_{TLP}

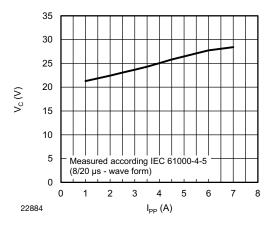


Fig. 6 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

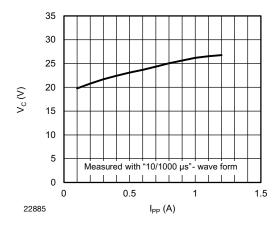
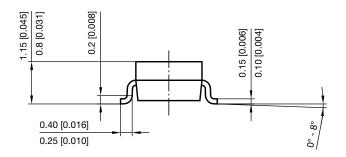
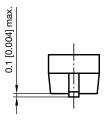


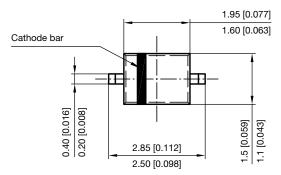
Fig. 7 - Typical Peak Clamping Voltage vs. Peak Pulse Current (10/1000 µs)



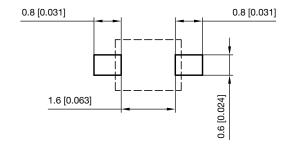
PACKAGE DIMENSIONS in millimeters (inches) SOD-323





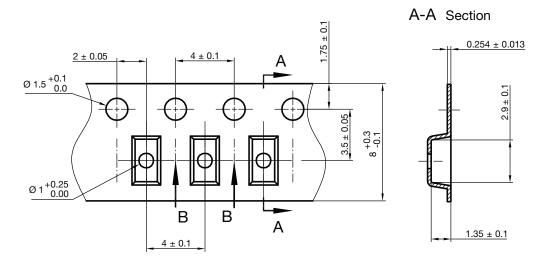


Footprint recommendation:

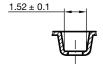


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CARRIER TAPE SOD-323



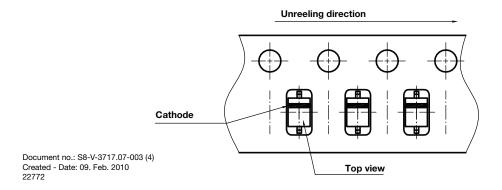
B-B Section



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22824

ORIENTATION IN CARRIER TAPE SOD-323





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