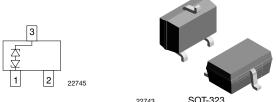


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Vishay Semiconductors

# Bidirectional Symmetrical (BiSy) Low Capacitance, **Single-Line ESD Protection Diode in SOT-323**



#### **FEATURES**

- For LIN-Bus applications
- Small SOT-323 package
- T<sub>J</sub> max. = 175 °C
- 1-line ESD protection
- Working range ± 26.5 V
- Low leakage current I<sub>R</sub> < 0.05 μA</li>
- Low load capacitance C<sub>D</sub> < 15 pF
- ESD immunity acc. IEC 61000-4-2
  - ± 30 kV contact discharge
  - ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ABC = type code (see table below) WW = date code working week VY = date code year

#### **LINKS TO ADDITIONAL RESOURCES**



ORDERING INFORMATION								
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE				PACKAG	ING CODE		
	AEC-Q101 QUALIFIED	Rohs-Compliant + Lead (Pb)-Free Terminations		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
	QUALIFIED	STANDARD	GREEN	PLATED	15K/BOX = MOQ	10K/BOX = MOQ		
VLIN26A1-03G	-	E		3	-08		VLIN26A1-03G-E3-08	
VLIN26A1-03G	Н	E		3	-08		VLIN26A1-03GHE3-08	
VLIN26A1-03G	-	E		3		-18	VLIN26A1-03G-E3-18	
VLIN26A1-03G	Н	E		3		-18	VLIN26A1-03GHE3-18	

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VLIN26A1-03G	SOT-323	6A1	5.65 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	$T_A = 25$ °C; acc. IEC 61000-4-5; $t_p = 8/20 \mu s$ ; single shot	I <sub>PPM</sub>	3	Α			
Peak pulse power	$T_A = 25$ °C; acc. IEC 61000-4-5; $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	150	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses; T <sub>A</sub> = 25 °C	V	± 30	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses; T <sub>A</sub> = 25 °C	- V <sub>ESD</sub>	± 30	kV			
Operating temperature	Junction temperature	TJ	-55 to +175	°C			
Storage temperature		T <sub>STG</sub>	-55 to +175	°C			



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<b>ELECTRICAL CHARACTERISTICS</b> (pin 1 to 3, 3 to 1) (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	26.5	V		
Reverse voltage	At I <sub>R</sub> = 0.05 μA	$V_{R}$	26.5	-	-	V		
Reverse current	At V <sub>RWM</sub> = 26.5 V	I <sub>R</sub>	-	-	0.05	μA		
Reverse breakdown voltage	At I <sub>R</sub> = 1 mA	$V_{BR}$	28	30	32	V		
B	At I <sub>PP</sub> 1 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	32	40	V		
Reverse clamping voltage	At $I_{PP} = I_{PPM} = 3 \text{ A}$ ; $t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	38	50	V		
Capacitance	At $V_R = 0 V$ , $f = 1 MHz$	C <sub>D</sub>	-	10	15	pF		

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

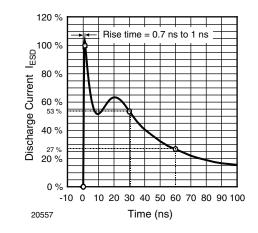


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$  / 150 pF)

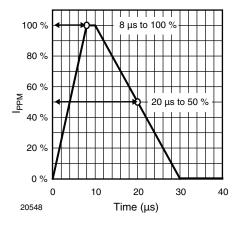


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

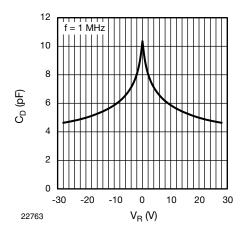


Fig. 3 - Typical Capacitance  $C_{\text{D}}$  vs. Reverse Voltage  $V_{\text{R}}$ 

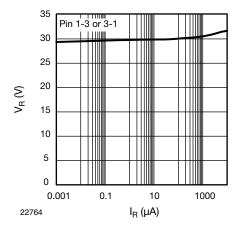


Fig. 4 - Typical Reverse Voltage  $V_{R}$  vs. Reverse Current  $I_{R}$ 



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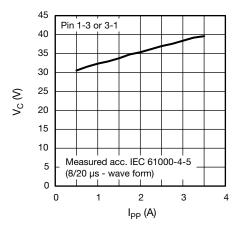


Fig. 5 - Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current  $I_{PP}$ 

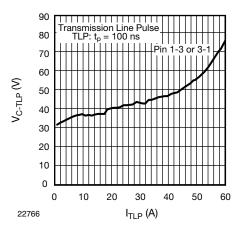
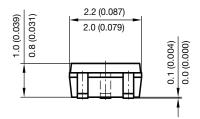
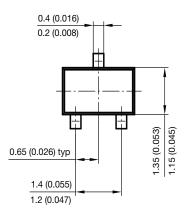


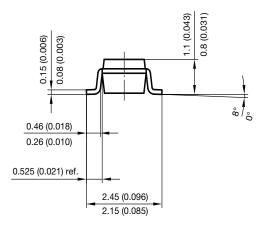
Fig. 6 - Typical Clamping Voltage  $V_{C-TLP}$  vs. Pulse Current  $I_{TLP}$ 

#### PACKAGE DIMENSIONS in millimeters (inches) SOT-323

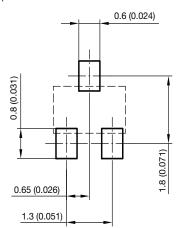




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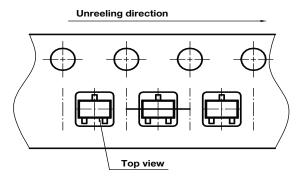


#### foot print recommendation:



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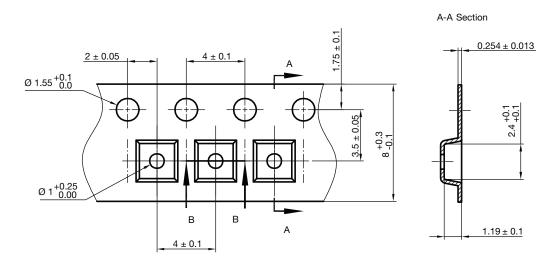
### **ORIENTATION IN CARRIER TAPE SOT-323**



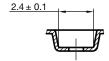
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#### **CARRIER TAPE SOT-323**



**B-B Section** 



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