

CHARACTERISTIC NAME	DRAWING SYMBOL	AIAG APQP REFERENCE MANUAL, GLOBAL SUPPLIER QUALITY MANUAL (G-SM-01) QMS-1004255, APPENDIX A	(INITIAL) SHORT TERM Cpk	LONG TERM Ppk		
ISO 26262 FUNCTIONAL SAFETY	+	A CHARACTERISTIC OF AN ITEM, ELEMENT OR PRODUCTION PROCESS FOR WHICH REASONABLY FORESEEABLE DEVIATION	≥ 1.67	MONITORING (COMPLIANCE REQUIRED)F	
RELATED	- - - - - - - - - -	COULD AFFECT, CONTRIBUTE TO OR CAUSE ANY POTENTIAL REDUCTION OF FUNCTIONAL SAFETY.	≥ 1.67	≥ 1.67		
CRITICAL	∇	CONTROL ITEM PRODUCTS HAVE CRITICAL CHARACTERISTICS THAT MAY AFFECT SAFE VEHICLE/PRODUCT OPERATION AND/OR	≥ 1.67	MONITORING (COMPLIANCE REQUIRED)F	
CRITICAL	∇ $\&$	COMPLIANCE WITH GOVERNMENT REGULATIONS. UNIQUE SYMBOLS IDENTIFYING SAFETY AND REGULATORY CHARACTERISTICS.	≥ 1.67	≥ 1.67		
SIGNIFICANT		SIGNIFICANT CHARACTERISTICS ARE THOSE PRODUCT PARAMETERS AND REQUIREMENTS THAT ARE IMPORTANT FOR CUSTOMER	≥ 1.67	MONITORING (COMPLIANCE REQUIRED	ЭF	
SIGNIFICANT		SATISFACTION (FORM, FIT AND FUNCTION) AND FOR WHICH QUALITY PLANNING ACTIONS MUST BE ADDRESSED ON A CONTROL PLAN.	≥ 1.67	≥ 1.33		
SPC	Ś	USED TO SPECIFY ONGOING SPC METHODOLOGIES TO BE PERFORMED.	≥ 1.67	≥ 1.33		
STANDARD		NON-KEY CHARACTERISTIC - STANDARD DIMENSION VS. STANDARD (INCL. TOLERANCE) FIRST ARTICLE FIRST AR OK FIRST PART OK FIRST				
		•				

I²C COMMUNICATION

SDA

TIMING DIAGRAM OVER TEMPERATURE RANGE:

2

l²C		E OVER TEMPERATUR	E RANGE			
	12 BITS MIN	1				
	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
AGE	V _{IH}	-	$0.8 \times V_{_{DD}}$	-	V _{DD}	V
AGE		-	-	-	$0.2 \times V_{DD}$	V
AGE	$V_{\rm OH}$	-	V _{DD} - 0.2	-	V _{DD}	V
AGE		-	0	-	0.2	V
		-				
RENT	I _{ol_sda}	SDA @ V _{ol} , MAX	2.3	3.9	6.2	mA
SDA		@400kHz	-	-	200	pF
TOR	R _{I²C_PU}	-	0.5	1	50	kΩ

рF

10

3



OUTPUT

VOLTAGE

PARAMETER

4

RESOLUTION

HIGH-LEVEL INPUT VOLTAGE

LOW-LEVEL INPUT VOLTAGE

HIGH-LEVEL OUTPUT VOLTAGE

LOW-LEVEL OUTPUT VOLTAGE

LOAD CAPACITANCE AT SDA

OUTPUT SINK CURRENT

PULL-UP RESISTOR

INPUT CAPACITANCE

- THE SDA SIGNAL IS OPEN DRAIN. IT REQUIRES AN EXTERNAL PULL-UP SIZED TO MEET 1. THE SPECIFIED TIMING REQUIREMENTS
- LIMITS ON LOAD CAPACITANCE, PULL-UP RESISTOR, AND INPUT PIN CAPACITANCE 2. ARE PROVIDED IN THE CASE WHERE MULTIPLE SLAVES ARE ON THE I²C B
- UPDATE RATE IS THE TIME INTERVAL BETWEEN NEW MEASUREMENTS WHEN THE 3. SENSOR IS POWERED CONTINUOUSLY.

R_{I2C PU} $C_{I^2C_IN}$

SCL IS INPUT ONLY. SENSOR IS A SLAVE ONLY AND DOES NOT CREATE ANY CLOCK 4. STRETCHING OR MULTIMASTER.

PARAMETER	SYMBOL	MIN	TYP	МАХ	UNITS			
SCL clock frequency	f _{SCL}	100		400	kHz			
Start condition hold time relative to SCL edge	t _{HDSTA}	0.1			μs			
Minimum SCL clock low width 1)	t _{LOW}	0.6			μs			
Minimum SCL clock high width 1)	t _{HIGH}	0.6			μs			
Start condition setup time relative to SCL edge	t _{susta}	0.1			μs			
Data hold time on SDA relative to SCL edge	t _{HDDAT}	0			μs			
Data setup time on SDA relative to SCL edge	t _{SUDAT}	0.1			μs			
Stop condition setup time on SCL	t _{susto}	0.1			μs			
Bus free time between stop condition and start condition	t _{BUS}	2			μs			
1) Combined low and high widths must equal or exceed minimum SCLK period.								

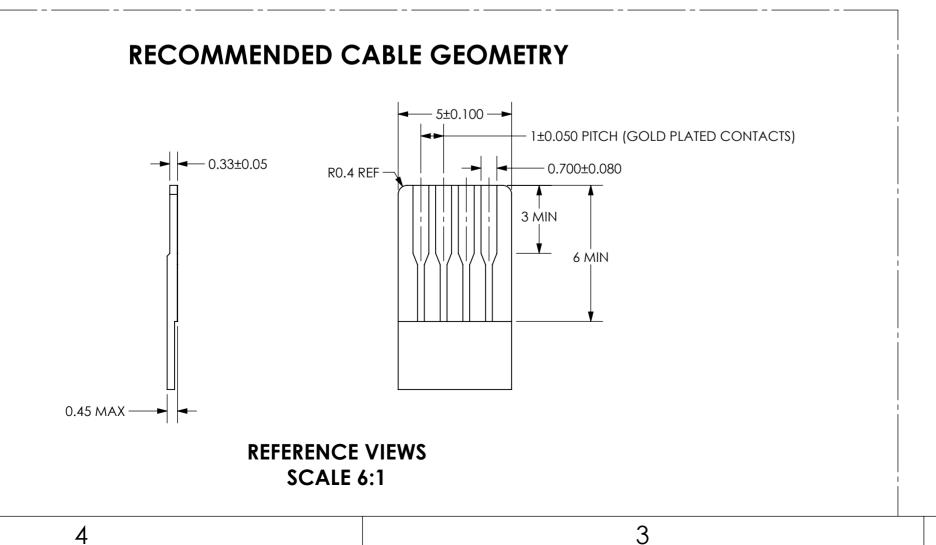


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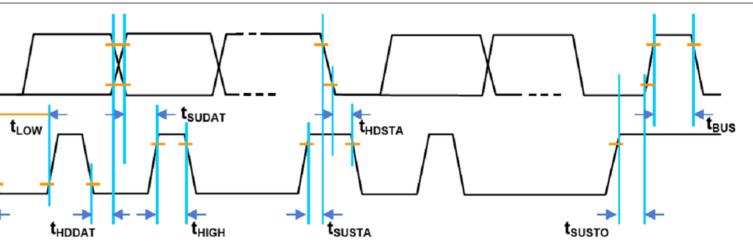
D

С

NO EXTERNAL CAPACITANCE NEEDED FOR Vs 5.



SCL **t**_{HDSTA}



D

С

В

NOTE: THERE ARE THREE ADJUSTMENTS TO THE I²C IMPLEMENTATION COMPARED WITH THE ORIGINAL I²C PROTOCOL:

SENDING A START-STOP CONDITION WITHOUT ANY TRANSITIONS ON THE CLK LINE (NO CLOCK PULSES IN BETWEEN) CREATES A COMMUNICATION ERROR FOR THE NEXT COMMUNICATION, EVEN IF THE NEXT START CONDITION IS CORRECT AND THE CLOCK PULSE IS APPLIED. AN ADDITIONAL START CONDITION MUST BE SENT, WHICH RESULTS IN RESTORATION OF PROPER COMMUNICATION.

THE RESTART CONDITION- A FALLING SDA EDGE DURING DATA TRANSMISSION WHEN THE CLK CLOCK LINE IS STILL HIGH- CREATES THE SAME SITUATION. THE NEXT COMMUNICATION FAILS, AND AN ADDITIONAL START CONDITION MUST BE SENT FOR THE CORRECT COMMUNICATION. A FALLING SDA EDGE IS NOT ALLOWED BETWEEN THE START CONDITION AND THE FIRST RISING SCL EDGE. IF USING AN I²C ADDRESS WITH THE FIRST BIT 0, SDA MUST BE HELD OW FROM THE START CONDITION THROUGH THE FIRST BIT.

