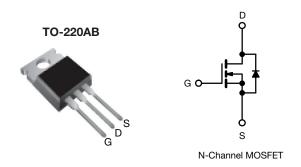
SiHP052N60EF



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EF Series Power MOSFET With Fast Body Diode



PRODUCT SUMMARY				
V _{DS} (V) at T _J max.	650			
R _{DS(on)} typ. (Ω) at 25 °C	$V_{GS} = 10 V$	0.045		
Q _g max. (nC)	101			
Q _{gs} (nC)	24			
Q _{gd} (nC)	22			
Configuration	Single			

FEATURES

- 4th generation E series technology
- Low figure-of-merit (FOM) Ron x Qg
- Low effective capacitance (Co(er))
- · Reduced switching and conduction losses
- Avalanche energy rated (UIS)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
 - High-intensity discharge (HID)
 - Fluorescent ballast lighting
- Industrial
 - Welding
 - Induction heating
 - Motor drives
 - Battery chargers
 - Solar (PV inverters)

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free and halogen-free	SiHP052N60EF-GE3

PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			V _{DS}	600	V	
Gate-source voltage			V _{GS}	± 30	V	
Continuous drain current ($T_J = 150 \ ^{\circ}C$)	V _{GS} at 10 V	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$ $T_{\rm C} = 100 \ ^{\circ}{\rm C}$	- I _D	48		
		T _C = 100 °C		31	А	
Pulsed drain current ^a			I _{DM}	148		
Linear derating factor				2.2	W/°C	
Single pulse avalanche energy ^b			E _{AS}	353	mJ	
Maximum power dissipation			PD	278	W	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C		
Drain-source voltage slope	T _J = 125 °C		du/dt	100		
Reverse diode dv/dt d		dv/dt	50	V/ns		
Soldering recommendations (peak temperature) ^c	For 10 s			260	°C	

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature
- b. V_{DD} = 120 V, starting T_J = 25 °C, L = 28.2 mH, R_g = 25 Ω , I_{AS} = 5 A

c. 1.6 mm from case

d. $I_{SD} \leq I_D$, di/dt = 240 A/µs, starting T_J = 25 °C

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COMPLIANT

HALOGEN

FREE



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PARAMETER	SYMBOL	TYP.		MAX.	MAX.		UNIT			
Maximum junction-to-ambient	R _{thJA}	-		62						
Maximum junction-to-case (drain)	R _{thJC}	- 0.45				°C/W				
SPECIFICATIONS (T _J = 25 $^{\circ}$ C,	unless otherw	ise noted)								
PARAMETER	SYMBOL			NS	MIN.	TYP.	MAX.			
Static		-								
Drain-source breakdown voltage	V _{DS}	V _{GS} =	= 0 V, I _D = 25	0 μA	600	-	-	V		
V _{DS} temperature coefficient	ΔV _{DS} /T _J		e to 25 °C, I _E		-	0.61	-	V/°C		
Gate-source threshold voltage (N)	V _{GS(th)}		= V _{GS} , I _D = 25		3.0	-	5.0	V		
		-	$V_{GS} = \pm 20 V$ $V_{GS} = \pm 30 V$		-	-	± 100	nA		
Gate-source leakage	I _{GSS}				-	-	± 1	μA		
	<u> </u>		$V_{DS} = 480 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		-	-	1	μA		
Zero gate voltage drain current	I _{DSS}		$V_{DS} = 480 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$		-	-	2	mA		
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	T	= 23 A	-	0.045	0.052	Ω		
Forward transconductance a	g _{fs}		= 30 V, I _D = 2	23 A	-	11	-	S		
Dynamic		1				•	1			
Input capacitance	C _{iss}	$V_{GS} = 0 V,$ $V_{DS} = 100 V,$ f = 1 MHz $V_{DS} = 0 V \text{ to } 480 V, V_{GS} = 0 V$		-	3380	-	pF			
Output capacitance	C _{oss}			-	158	-				
Reverse transfer capacitance	C _{rss}			-	6	-				
Effective output capacitance, energy related ^a	C _{o(er)}			-	116	-				
Effective output capacitance, time related ^b	C _{o(tr)}			-	715	-				
Total gate charge	Qg				-	67	101	1		
Gate-source charge	Q _{gs}	V _{GS} = 10 V I _D = 23 A, V _{DS} = 480 V		-	24	-	nC			
Gate-drain charge	Q _{gd}				-	22	-	1		
Turn-on delay time	t _{d(on)}	+ · · · · · · · · · · · · · · · · · · ·		-	31	62				
Rise time	t _r		V_{DD} = 480 V, I _D = 23 A, V _{GS} = 10 V, R _g = 9.1 Ω		-	80	160	ns		
Turn-off delay time	t _{d(off)}				-	68	136			
Fall time	t _f	1		-	50	100	1			
Gate input resistance	Rg	f = 1 MHz, open drain		0.4	0.8	1.6	Ω			
Drain-Source Body Diode Characterist		•								
Continuous source-drain diode current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	48	A			
Pulsed diode forward current	I _{SM}			-	-	148				
Diode forward voltage	V _{SD}	T _J = 25 °C, I _S = 23 A, V _{GS} = 0 V		-	-	1.2	V			
Reverse recovery time	t _{rr}	$T_{J} = 25 \text{ °C}, I_{F} = I_{S} = 23 \text{ A},$ di/dt = 100 A/µs, V _R = 400 V		-	148	296	ns			
Reverse recovery charge	Q _{rr}			-	1.1	1.2	μC			
Reverse recovery current	I _{RRM}			_	14	-	A			

Notes

a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS}

b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS}



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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

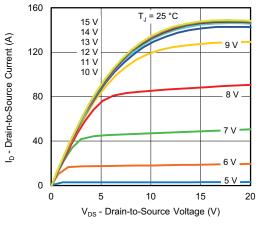


Fig. 1 - Typical Output Characteristics

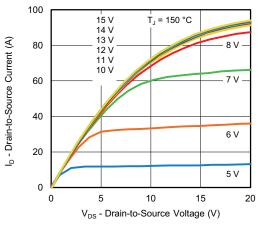


Fig. 2 - Typical Output Characteristics

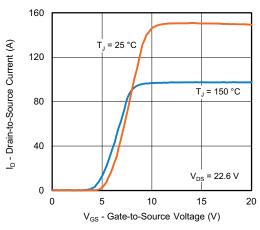


Fig. 3 - Typical Transfer Characteristics

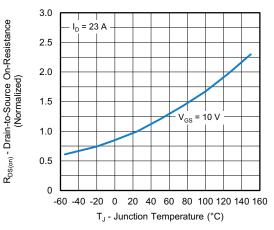


Fig. 4 - Normalized On-Resistance vs. Temperature

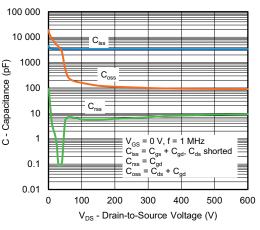
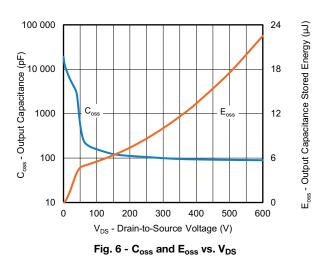


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage



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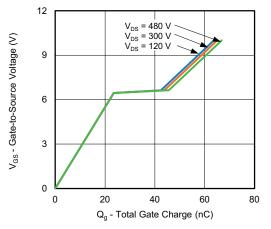


Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage

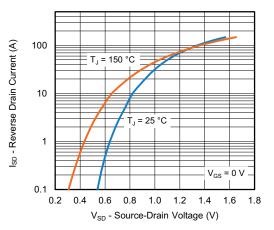


Fig. 8 - Typical Source-Drain Diode Forward Voltage

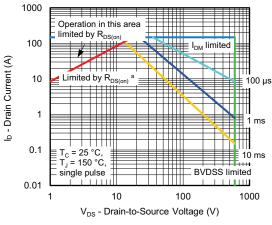
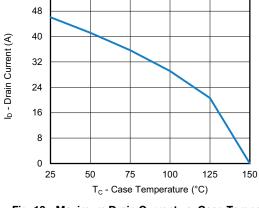


Fig. 9 - Maximum Safe Operating Area

Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



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Fig. 10 - Maximum Drain Current vs. Case Temperature

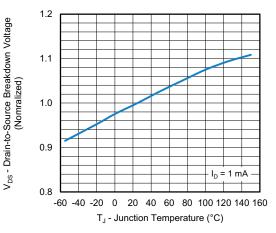


Fig. 11 - Temperature vs. Drain-to-Source Voltage

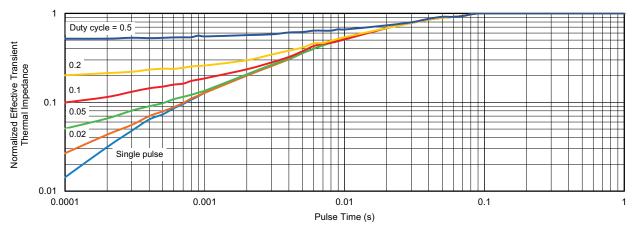
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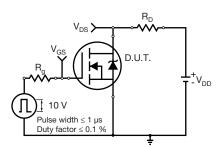


Fig. 13 - Switching Time Test Circuit

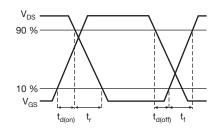


Fig. 14 - Switching Time Waveforms

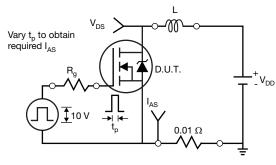
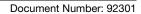
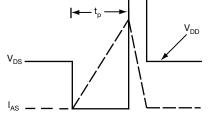


Fig. 15 - Unclamped Inductive Test Circuit

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Fig. 16 - Unclamped Inductive Waveforms

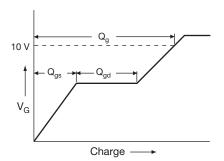
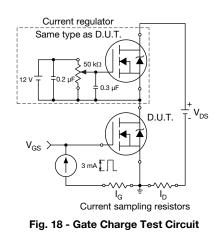


Fig. 17 - Basic Gate Charge Waveform



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Peak Diode Recovery dv/dt Test Circuit

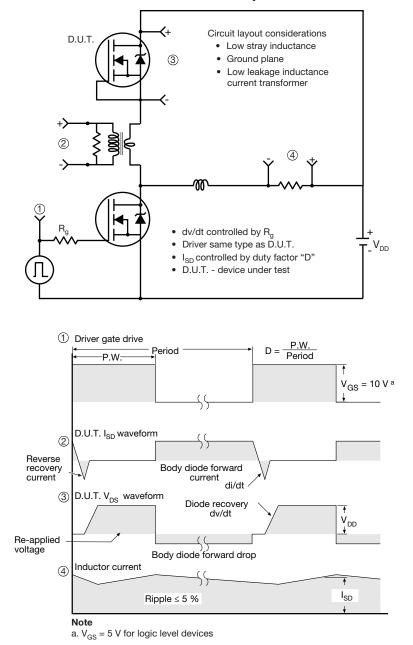


Fig. 19 - For N-Channel

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