

Vishay Siliconix

## P-Channel 30-V (D-S) MOSFET

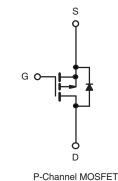
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)		
- 30	0.0075 at V <sub>GS</sub> = - 10 V	- 15		
	0.011 at V <sub>GS</sub> = - 4.5 V	- 12.3		

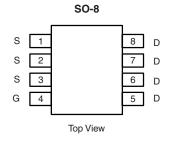


- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET

#### **APPLICATIONS**

- Notebook
  - Load Switch
  - Battery Switch





Ordering Information: Si4413ADY-T1-E3 (Lead (Pb)-free) Si4413ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \degree C$ , unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	- 15	- 10.5		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T <sub>A</sub> = 70 °C		- 11.8	- 8.3	А	
Pulsed Drain Current		I <sub>DM</sub>	- 50		А	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>s</sub>	- 2.7	- 1.36		
	T <sub>A</sub> = 25 °C	PD	3.0	1.5	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	' D	1.9	0.95	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55	to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mauina lugation to Anabianta	t ≤ 10 s	R <sub>thJA</sub>	33	42	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		70	84	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	16	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

HALOGEN

FREE

Available

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 3.0	V	
Gate-Body Leakage	I <sub>GSS</sub>				± 100	nA	
	I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70 ^{\circ}\text{C}$			- 1	μΑ	
Zero Gate Voltage Drain Current					- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -10 V$	- 30			Α	
Drain-Source On-State Resistance <sup>a</sup>	Р	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 13 A		0.0063	0.0075	0	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 10 A		0.0083	0.011	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -13 \text{ A}$		50		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{S} = -2.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.74	- 1.1	V	
Dynamic <sup>b</sup>			•				
Total Gate Charge	Qg			61	95	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 15 V, $V_{GS}$ = - 5 V, $I_{D}$ = - 13 A		15.5			
Gate-Drain Charge	Q <sub>gd</sub>			32			
Turn-On Delay Time	t <sub>d(on)</sub>			21	35		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 15 $\Omega$		18	30	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 10 V, $\text{R}_\text{G}$ = 6 $\Omega$		170	260		
Fall Time	t <sub>f</sub>			97	150		
Gate Resistance	R <sub>g</sub>		Ī	3.4		Ω	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 2.1 A, dl/dt = 100 A/μs		70	110	ns	

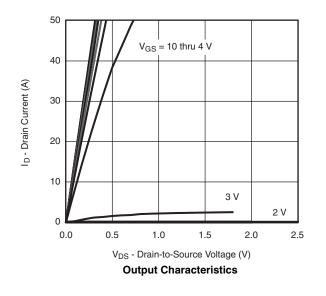
Notes:

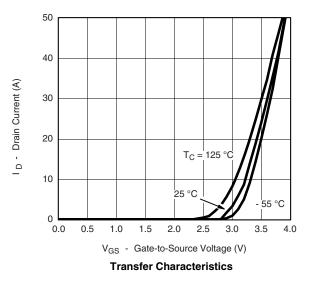
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

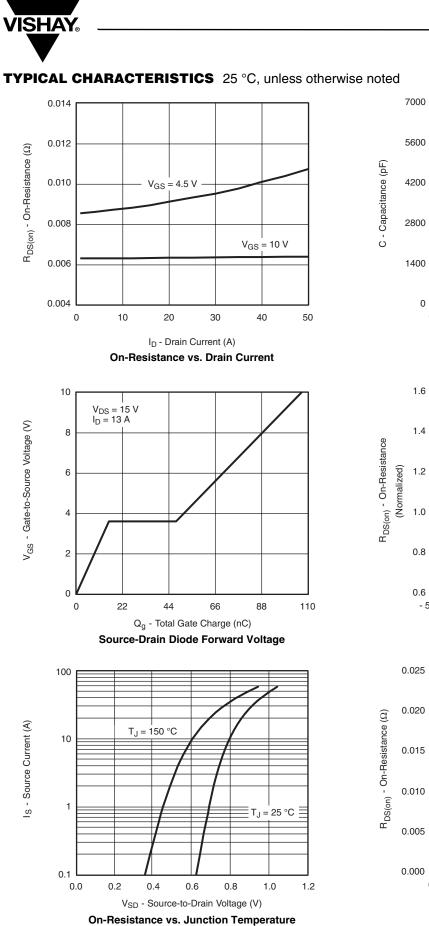
b. Guaranteed by design, not subject to production testing.

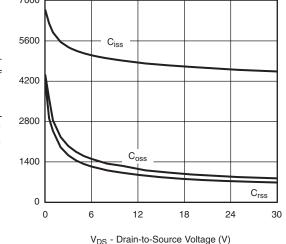
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





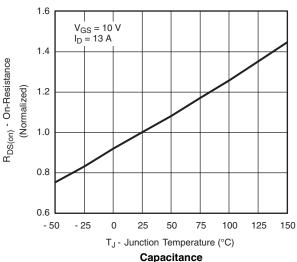


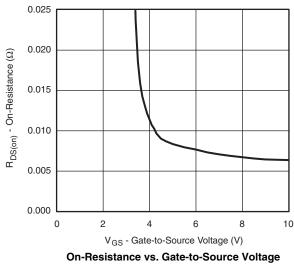


Si4413ADY

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Gate Charge



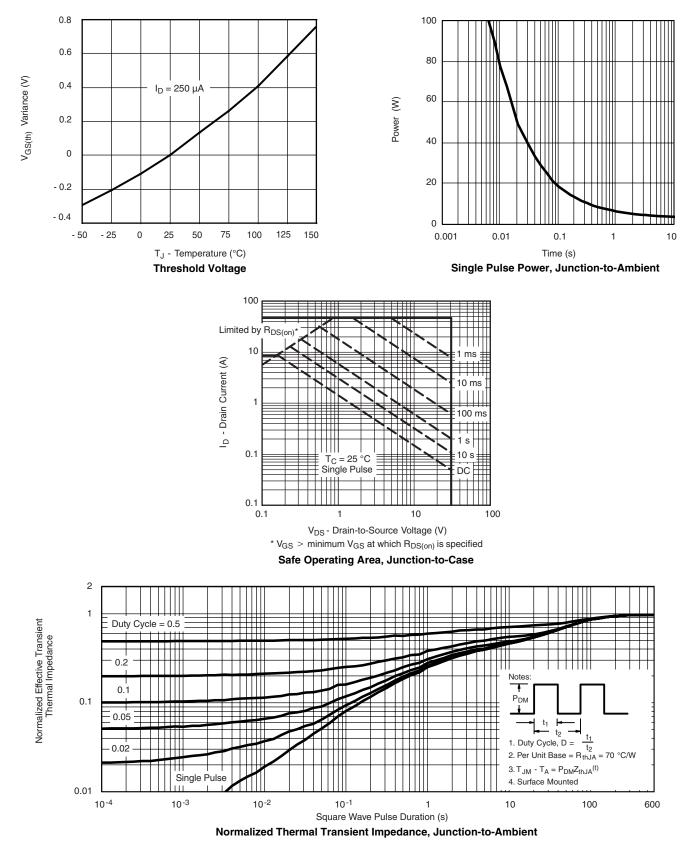


Document Number: 73792 S-83096-Rev. C, 29-Dec-08

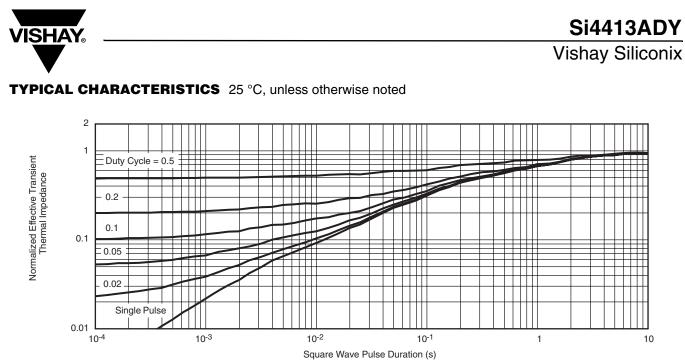
## Si4413ADY

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?73792">www.vishay.com/ppg?73792</a>.



# Package Information

Vishay Siliconix

# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

## **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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