

### General Description

The WSF55P06 is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSF55P06 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

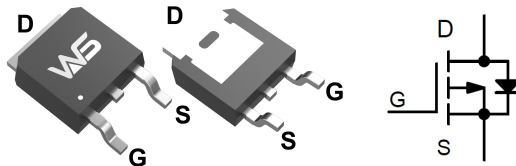
### Product Summary

BVDSS	RDSON	ID
-60V	23mΩ	-50A

### Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- CCFL Back-light Inverter

### TO-252 Pin Configuration



### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage	-60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current-Continuous	-50	A
I <sub>D</sub> (100°C)	Drain Current-Continuous(Tc=100°C)	-24.8	A
I <sub>DM</sub>	Pulsed Drain Current	-220	A
P <sub>D</sub>	Maximum Power Dissipation	110	W
	Derating factor	0.73	W/°C
E <sub>AS</sub>	Single pulse avalanche energy (Note 5)	273	mJ
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 To 175	°C
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case(Note 2)	1.36	°C/W

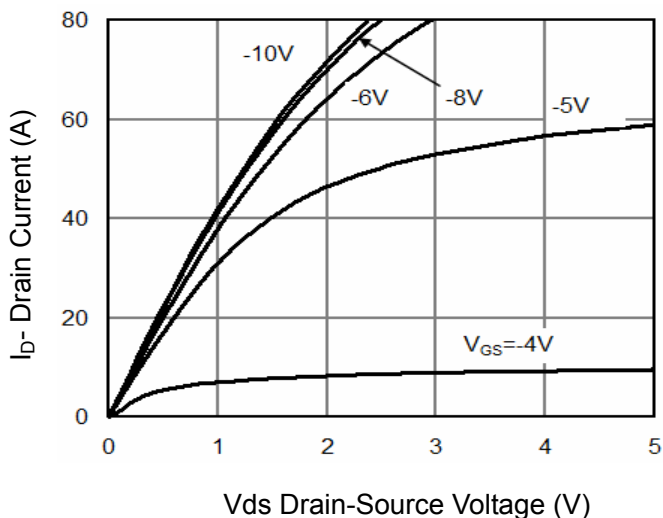
**Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-60	---	---	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	---	---	-1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
<b>On Characteristics (Note 3)</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1	-1.8	-3.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A	---	23	28	mΩ
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-20A	---	25	---	S
<b>Dynamic Characteristics (Note4)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, F=1.0MHz	---	3017	---	PF
C <sub>oss</sub>	Output Capacitance		---	180	---	PF
C <sub>rss</sub>	Reverse Transfer Capacitance		---	126	---	PF
<b>Switching Characteristics (Note 4)</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-30V, R <sub>L</sub> =1.5Ω, V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω	---	12	---	nS
t <sub>r</sub>	Turn-on Rise Time		---	15	---	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		---	38	---	nS
t <sub>f</sub>	Turn-Off Fall Time		---	15	---	nS
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-30, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V	---	49.8	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	10.6	---	nC
Q <sub>gd</sub>	Gate-Drain Charge		---	13.6	---	nC
<b>Drain-Source Diode Characteristics</b>						
V <sub>SD</sub>	Diode Forward Voltage (Note 3)	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A	---	---	-1.2	V
I <sub>S</sub>	Diode Forward Current (Note 2)		---	---	-50	A
t <sub>rr</sub>	Reverse Recovery Time	T <sub>J</sub> = 25°C, I <sub>F</sub> = - 20A di/dt = -100A/μs(Note3)	---	47	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	53	---	nC

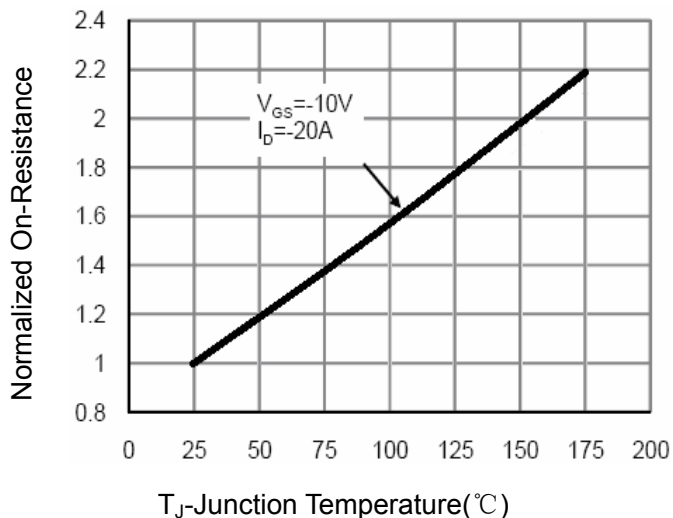
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. E<sub>AS</sub> condition: T<sub>j</sub>=25°C, V<sub>DD</sub>=-20V, V<sub>G</sub>=-10V, L=0.5mH, R<sub>G</sub>=25Ω

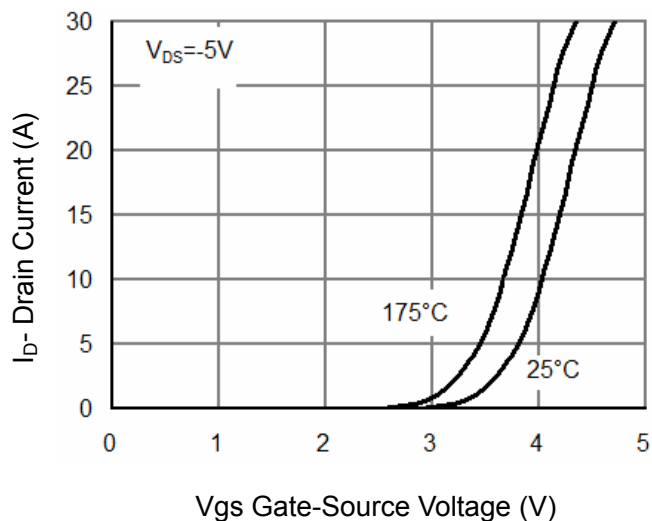
**Typical Electrical and Thermal Characteristics (Curves)**



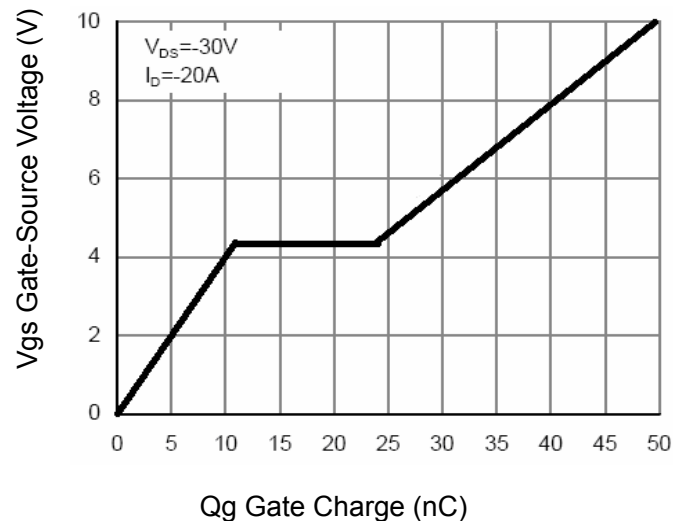
**Figure 1 Output Characteristics**



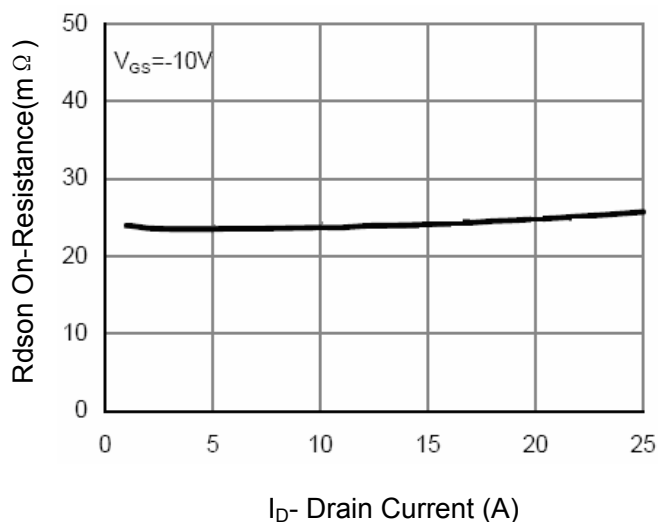
**Figure 4 Rdson-Junction Temperature**



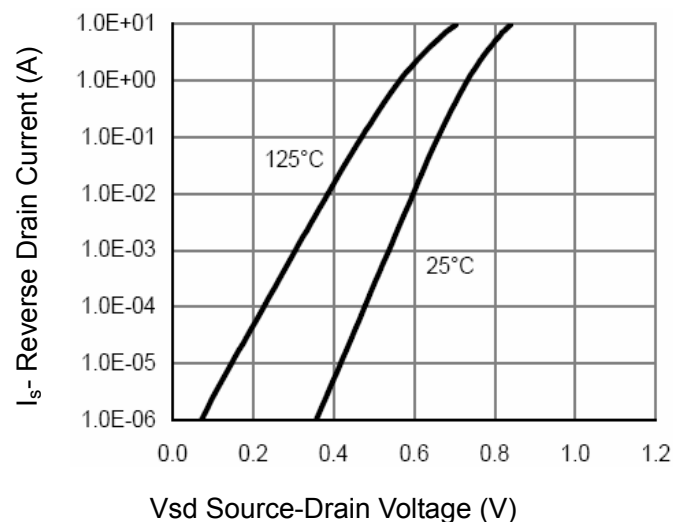
**Figure 2 Transfer Characteristics**



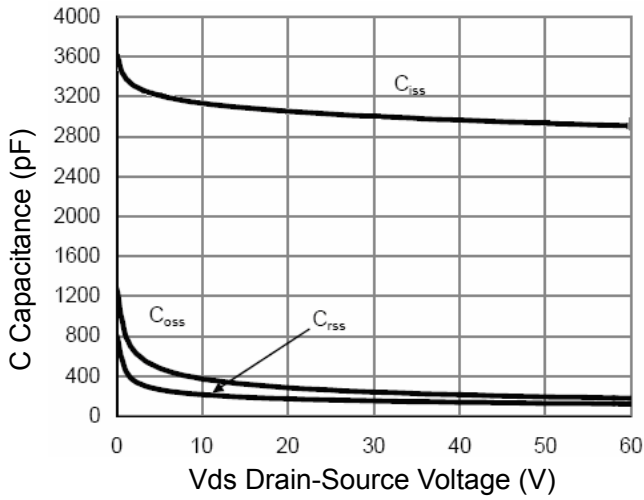
**Figure 5 Gate Charge**



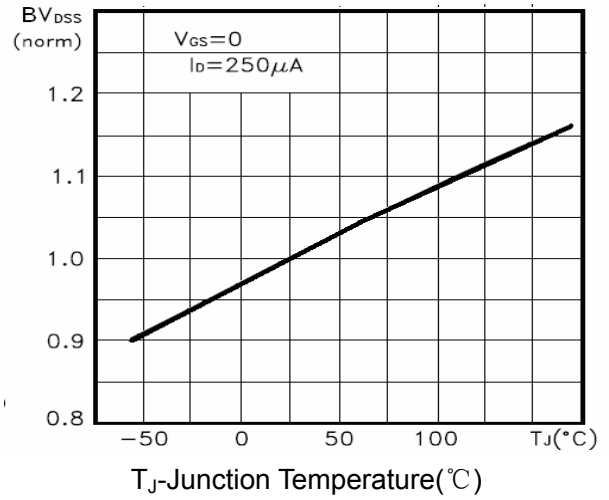
**Figure 3 Rdson- Drain Current**



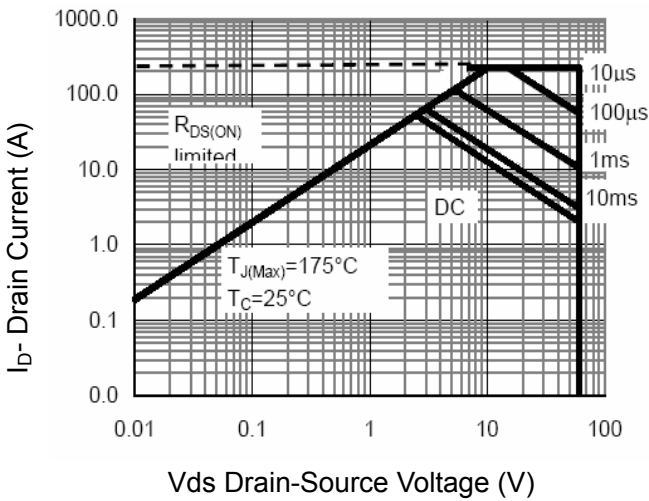
**Figure 6 Source- Drain Diode Forward**



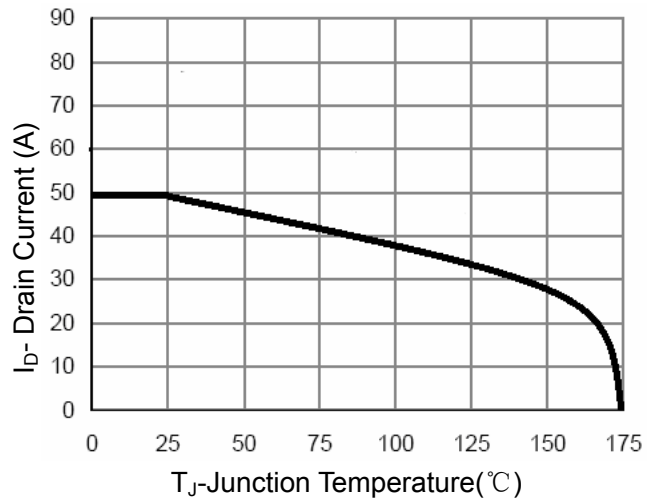
**Figure 7 Capacitance vs Vds**



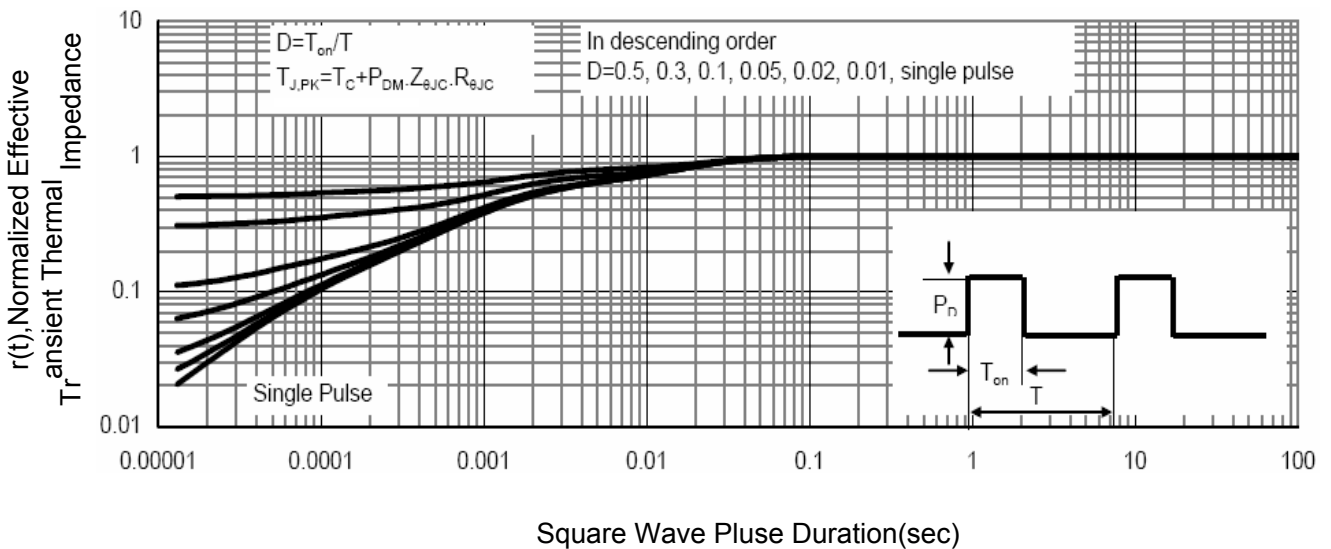
**Figure 9  $BV_{DSS}$  vs Junction Temperature**



**Figure 8 Safe Operation Area**



**Figure 10  $I_D$  Current De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**



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