

**JX3400ASS****N-Channel Enhancement Mode MOSFET**

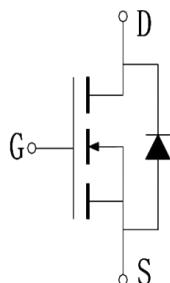
V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
30V	21.0m Ω @ 10V	5.8A
	22.5m Ω @ 4.5V	
	27.5m Ω @ 2.5V	

1.Features

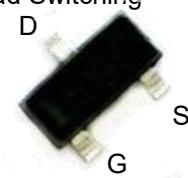
- ◆ 30V MOSFET technology
- ◆ Low on-state resistance
- ◆ Fast switching
- ◆ $V_{GS} \pm 12V$

2.Applications

- ◆ Power Switching Application
- ◆ Load Switching



Schematic Diagram



SOT23

Pin Description

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
JX3400ASS	3400	SOT23	3,000	120,000

4.Absolute Max Ratings at $T_a=25^\circ C$ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	30	V
Gate to Source Voltage	V_{GSS}	± 12	V
Drain Current (DC)	I_D	5.8	A
Drain Current (Pulse), $PW \leq 300\mu s$	I_{DP}	23	A
Total Dissipation	P_D	1.36	W
Junction Temperature	T_j	150	$^\circ C$
Storage Temperature	T_{stg}	-55 to +150	$^\circ C$

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**JX3400ASS****N-Channel Enhancement Mode MOSFET****5.Thermal Resistance Ratings**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R _{θJA}	92	°C/W

Note 2 : When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

6.Electrical Characteristics at Ta=25°C (Note 3)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	I _D = 250μA, V _{GS} = 0V	30	32		V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5	0.95	1.5	V
Static Drain to Source On-State Resistance	R _{DS(on)}	I _D = 4.2A, V _{GS} = 10V	-	21.0	25	mΩ
		I _D = 4A, V _{GS} = 4.5V	-	22.5	27	mΩ
		I _D = 1, V _{GS} = 2.5V	-	27.5	39	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V, Frequency=1.0MHz		785		pF
Output Capacitance	C _{oss}			66		pF
Reverse Transfer Capacitance	C _{rss}			54		pF
Turn-ON Delay Time	t _{d(on)}	V _{DS} = 15V, I _D =3A V _{GS} = 10V, R _G = 3Ω		4		ns
Rise Time	t _r			11		ns
Turn-OFF Delay Time	t _{d(off)}			24		ns
Fall Time	t _f			2		ns
Total Gate Charge	Q _g	V _{DS} = 15V, V _{GS} = 10V, I _D = 3A		19		nC
	Q _{gs}			2		nC
	Q _{gd}			2.1		nC
Diode Forward Voltage	V _{FSD}	I _S = 5.8A, V _{GS} = 0	0.4	0.85	1.2	V

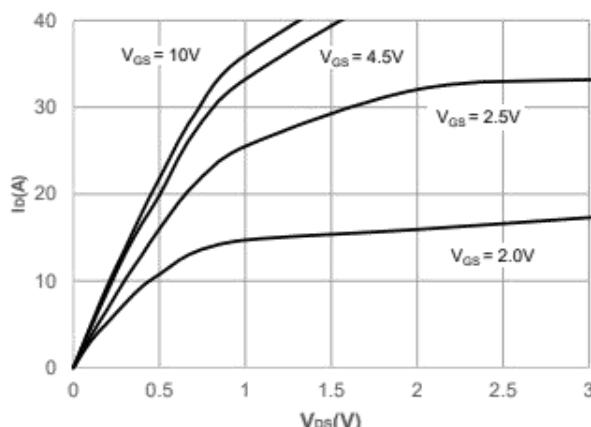
Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



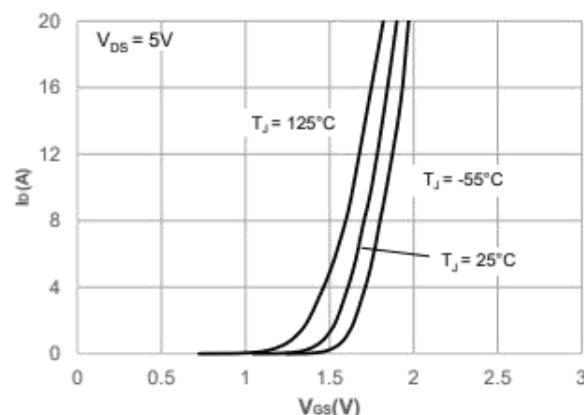
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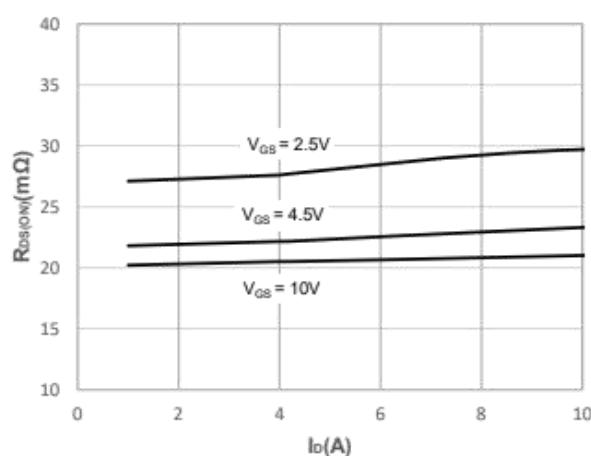
7.Typical electrical and thermal characteristics



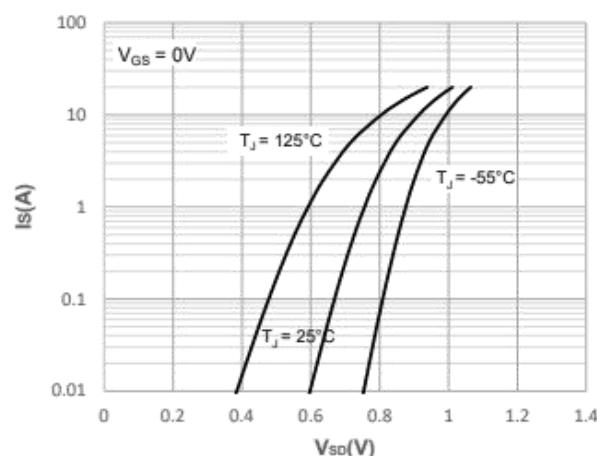
Output Characteristics



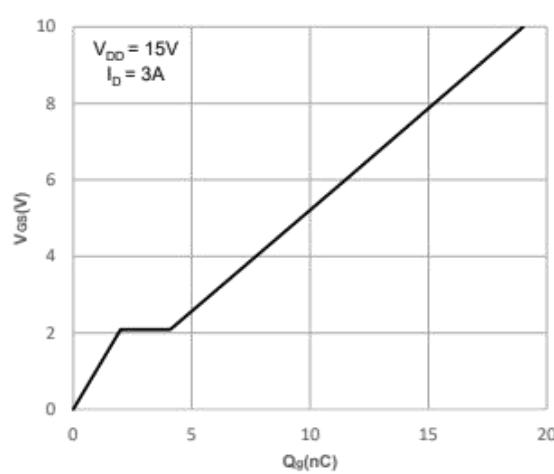
Typical Transfer Characteristics



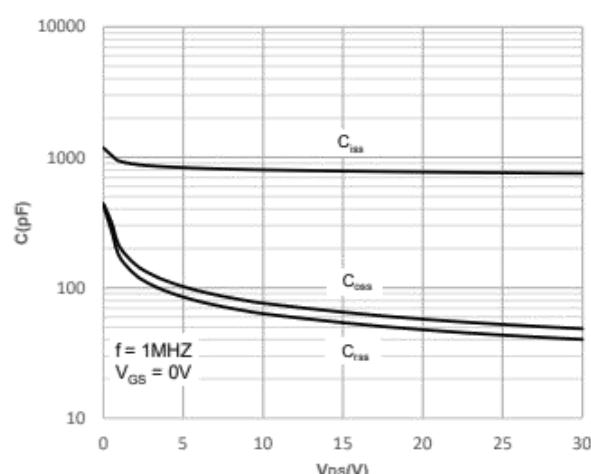
On-resistance vs . Drain Current



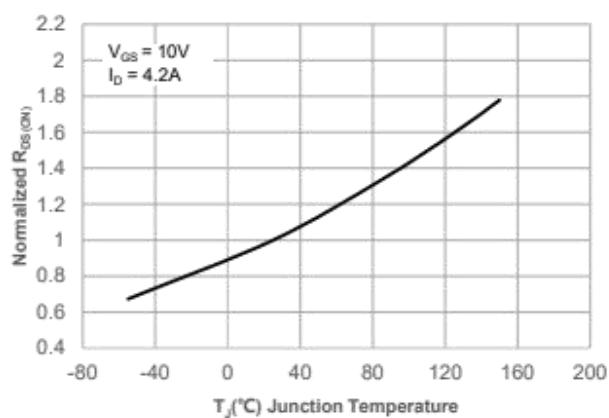
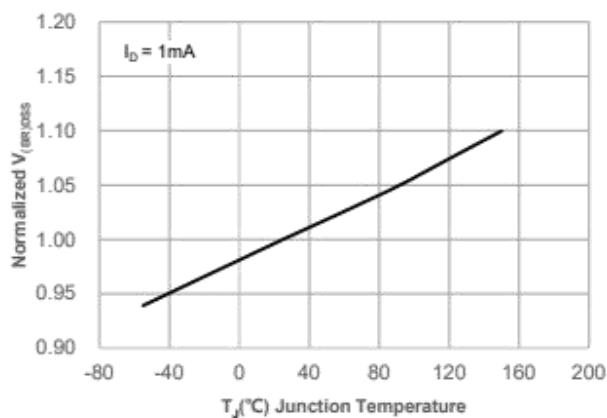
Body Diode Characteristics



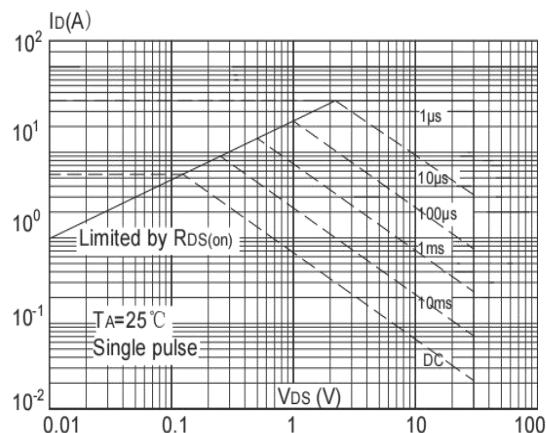
Gate Charge Characteristics



Capacitance Characteristics

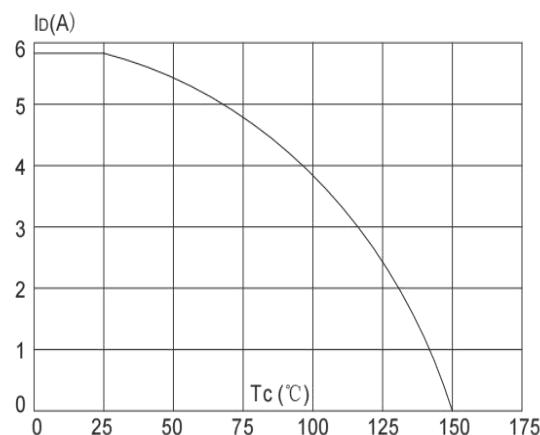
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**Normalized Breakdown Voltage vs .
Junction Temperature**

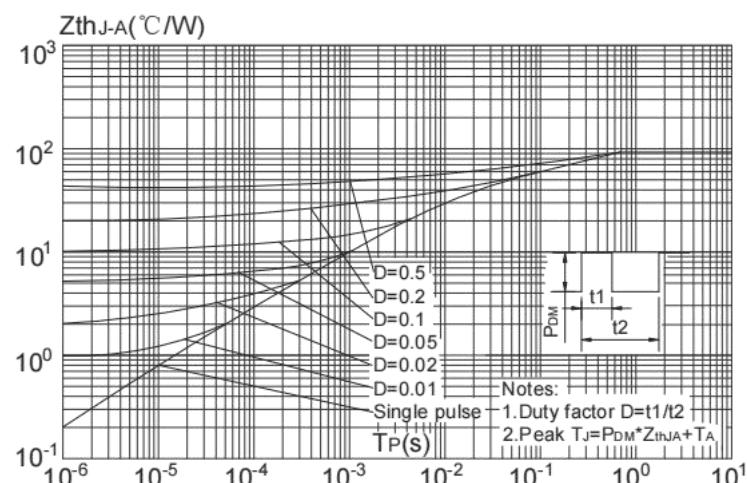


Maximum Safe Operating Area

**Normalized on Resistance vs .
Junction Temperature**



**Maximum Continuous Drain Current vs.
Case Temperature**



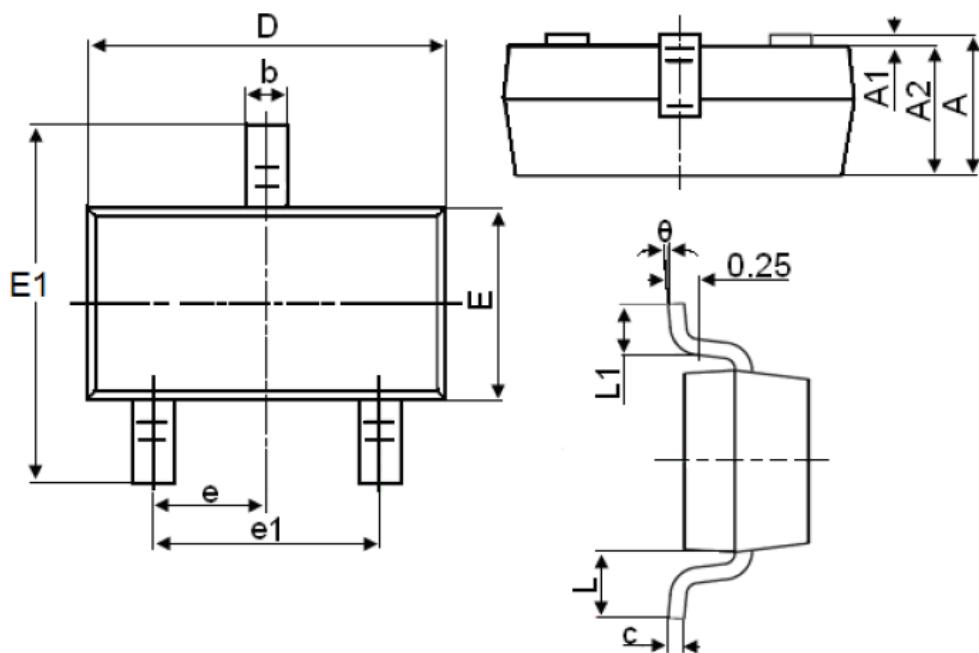
**Maximum Effective Transient Thermal
Impedance, Junction-to-Ambient**



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8.Package Dimensions



Symbol	Dimensions in Millimeters		
	MIN.	TYP.	MAX.
A	0.900		1.150
A1	0.000		0.100
A2	0.900		1.050
b	0.300		0.500
c	0.080		0.150
D	2.800		3.000
E	1.200		1.400
E1	2.250		2.550
e		0.950	
e1	1.800		2.000
L		0.550	
L1	0.300		0.500
θ	0°		8°