

## FEATURES

- Wide range of available, fixed output voltage.
- Low cost.
- Internal short-circuit current limiting.
- Internal thermal overload protection.
- No extermal components required.

## **APPLICATIONS**

• Three-terminal positive voltage regulator.

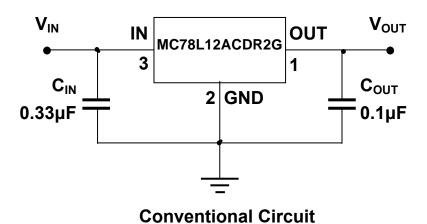
# **PIN DESCRIPTION**

| _ | SOP-8 |   |     |
|---|-------|---|-----|
|   | 1     | 8 | IN  |
|   | 2     | 7 | GND |
|   | 3     | 6 | GND |
|   | 4     | 5 | NC  |

#### MAXIMUM RATING operating temperature range applies unless otherwise specified

| Symbol           | Parameter                      | Value       | Units |
|------------------|--------------------------------|-------------|-------|
| V <sub>1</sub>   | Input voltage                  | 35          | V     |
| I <sub>CM</sub>  | Maximum output current         | 100         | mA    |
| P <sub>D</sub>   | Power dissipation              | 500         | mW    |
| T <sub>OPR</sub> | Operating junction temperature | 0 to +125   | °C    |
| Tj,Tstg          | Storage temperature range      | -40 to +150 | Ċ     |

### TYPICAL APPLICATION CIRCUIT





## ELECTRICAL CHARACTERISTICS

 $(V_l=19V, I_O=40mA, 0^{\circ}C < T_j < 125^{\circ}C, C_l=0.33 \mu F, C_O=0.1 \mu f, unless otherwise specified)$ 

| Domorro of on             | 0                              | <b>T</b> = 1 = 1 = 1 <sup>1</sup> 11 = 1 = 1          | 78L12 |     |      |      |  |
|---------------------------|--------------------------------|---|-------|-----|------|------|--|
| Parameter                 | Symbol                         | Test conditions                                       | MIN   | TYP | MAX  | UNIT |  |
|                           |                                | T <sub>j</sub> =25℃                                   | 11.5  | 12  | 12.5 |      |  |
| Output voltage            | Vo                             | V <sub>I</sub> =14.5V-27V, I <sub>O</sub> =1mA-40mA   | 11.4  |     | 12.6 | V    |  |
|                           |                                | V <sub>I</sub> =19V, I <sub>O</sub> =1mA-70mA         | 11.4  |     | 12.6 |      |  |
| Load regulation           | Dec                            | T <sub>j</sub> =25℃, I <sub>O</sub> =1mA-100mA        |       | 20  | 100  |      |  |
|                           | Reg <sub>load</sub>            | T <sub>j</sub> =25℃, I <sub>O</sub> =1mA-40mA         |       | 10  | 50   | mV   |  |
| Line regulation           | Dec                            | 14.5V≤V <sub>i</sub> ≤27V, T <sub>j</sub> =25℃        |       | 120 | 250  | mV   |  |
|                           | Reg <sub>line</sub>            | 16V≤Vi≤27V, Tj=25℃                                    |       | 100 | 200  |      |  |
| Input Bias Current        |                                | Tj <b>=25℃</b>  |       | 4.2 | 6.5  |      |  |
|                           | I <sub>IB</sub>                | T <sub>j</sub> =125℃                                  |       |     | 6.0  | mA   |  |
| Input Bias Current Change | A 1                            | 16V≤Vi≤27V  |       |     | 1.5  | mA   |  |
|                           | $\triangle I_{IB}$             | 1mA≤I <sub>O</sub> ≤40mA                              |       |     | 0.1  |      |  |
| Output Noise Voltage      | V <sub>N</sub>                 | 10Hz≤f≤100KHz,T <sub>A</sub> =25℃                     |       | 80  |      | μV   |  |
| Ripple rejection          |                                | I <sub>0</sub> =40mA,15V≤V <sub>i</sub> ≤25V,f=120Hz, | 07 40 | 40  |      | dB   |  |
|                           | RR                             | <b>T</b> <sub>j</sub> = <b>25</b> ℃                   | 37    | 42  |      |      |  |
| Dropout voltage           | V <sub>I</sub> -V <sub>O</sub> | Tj=25℃  |       | 1.7 |      | V    |  |





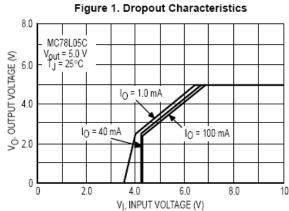


Figure 2. Dropout Voltage versus Junction Temperature VI -VO, INPUT/OUTPUT DIFFERENTIAL VOLTAGE (V) 2.5 l<sub>O</sub> = 70 mA 2.0 1.5 l<sub>O</sub> = 40 mA 1.0 lo = 1.0 mA Dropout of Regulation is defined as when V<sub>O</sub> = 2% of V<sub>O</sub> 0.5 0 50 0 25 75 100 125 TJ, JUNCTION TEMPERATURE (°C)

Figure 3. Input Bias Current versus Ambient Temperature

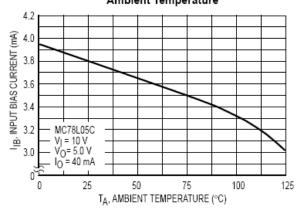
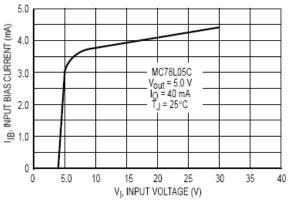
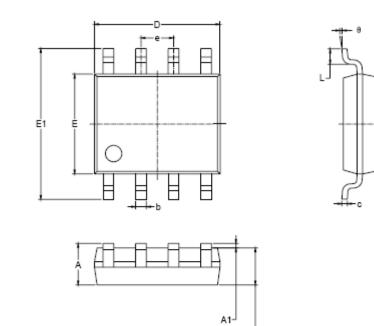


Figure 4. Input Bias Current versus Input Voltage





#### SOP-8



| Symbol | Dimensions<br>In Millimeters |       | Dimensions<br>In Inches |       |  |
|--------|------------------------------|-------|-------------------------|-------|--|
| ,      | MIN                          | MAX   | MIN                     | MAX   |  |
| А      | 1.350                        | 1.750 | 0.053                   | 0.069 |  |
| A1     | 0.100                        | 0.250 | 0.004                   | 0.010 |  |
| A2     | 1.350                        | 1.550 | 0.053                   | 0.061 |  |
| b      | 0.330                        | 0.510 | 0.013                   | 0.020 |  |
| с      | 0.170                        | 0.250 | 0.006                   | 0.010 |  |
| D      | 4.700                        | 5.100 | 0.185                   | 0.200 |  |
| E      | 3.800                        | 4.000 | 0.150                   | 0.157 |  |
| E1     | 5.800                        | 6.200 | 0.228                   | 0.244 |  |
| е      | 1.27 BSC                     |       | 0.050 BSC               |       |  |
| L      | 0.400                        | 1.270 | 0.016                   | 0.050 |  |
| 6      | 0°                           | 8°    | 0°                      | 8°    |  |

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