

DESCRIPTION

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 1.5A of load current with an output voltage adjustable over a 1.2 to 37V.

It employs internal current limiting, thermal shut-down and safe area compensation.

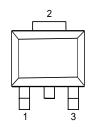
FEATURE

- Internal thermal overload protection
- Internal short circuit current limiting
- Output transistor safe operating area compensation

PIN CONFIGURATION

TO252-2L

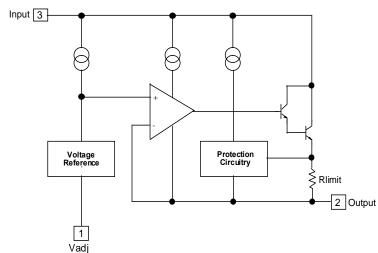




PIN DESCRIPTION

PIN No.	Name	Functions		
TO252-2L	Name	Description		
1	ADJ	Adjustable		
2	V_{OUT}	Output Voltage		
3	V_{IN}	Input Voltage		

INTERNAI Internal Block Diagram





Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V _I -V _O	Input-Output Voltage Differential	40	V
T _{LEAD}	Lead Temperature	230	°C
P_D	Power Dissipation	Internally limited	W
TJ	Operating Junction Temperature Range	0~125	
T _{stg}	Storage Temperature Range	-55~125	
ΔV _O /ΔΤ	Temperature Coefficient of Output Voltage	±0.02	%/℃

ELECTRICAL CHARACTERISTICS

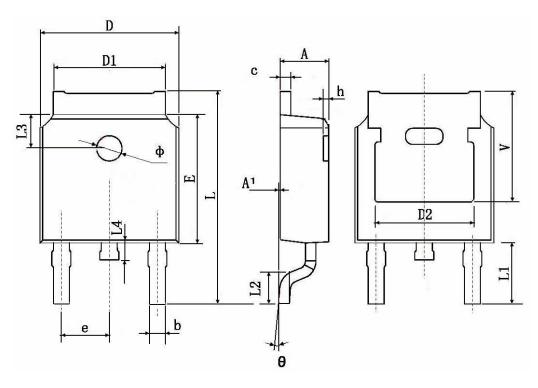
 $(V_O - V_I = 5V, I_O = 0.5A, 0^{\circ}C \leq T_J \leq +125^{\circ}C, I_{MAX} = 1.5A, P_{DMAX} = 20W, unless \ otherwise \ specified)$

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT	
Line Regulation(note1)	R _{line}	T _A =25°C 3V≤V _I -V _O ≤40V		0.01	0.04	%/V	
		3V≤V _I -V _O ≤40V		0.02	0.07		
Load Regulation(note1)	R _{load}	T _A =25°C, 10mA≤I _O ≤I _{MAX} V _O <5V V _O ≥5V		18 0.4	25 0.5	mV	
		10mA≤I _O ≤I _{MAX} V _O <5V V _O ≥5V		40 0.8	70 1.5	%Vo	
Adjustable Pin Current	I_{ADJ}	-		46	100		
Adjustable Pin Current Change	Δl _{ADJ}	$3V \le V_I - V_O \le 40V$ $10mA \le I_O \le I_{MAX}, P_D \le P_{MAX}$		2.0	5	μΑ	
Reference Voltage	V_{REF}	$3V \le V_{IN} - V_O \le 40V$ $10mA \le I_O \le I_{MAX}$, $P_D \le P_{MAX}$	1.20	1.25	1.30	V	
Temperature Stability	ST_T	-		0.7		$\%/V_{0}$	
Minimum Load Current to Maintain Regulation	I _{L(MIN)}	V _I -V _O =40V		3.5	12	mA	
Maximum Output Current	I _{O(MAX)}	V _I -V _O ≤15V, P _D ≤P _{MAX} V _I -V _O ≤40V, P _D ≤P _{MAX} T _A =25°C	1.0	2.2 0.3		Α	
RMS Noise,% of V _{OUT}	e _N	T _A =25°C,10Hz≤f≤10KHz		0.003	0.01	%/ V ₀	
Ripple Rejection	RR	Vo=10V, f =120Hz without C_{ADJ} C_{ADJ} =10 μ F(note2)	66	60 75		dB	
Long-Term Stability,T _J =T _{HIGH}	ST	T _A =25°C for end point mesasurements,1000HR		0.3	1	%	
Thermal Resistance Junction to case	$R_{ heta JC}$	-		5		°C/W	



TRANSISTOR OUTLINE

TO252-2L



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	P. 0.211 TYP.		



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