



Industrial



FEATURES AND BENEFITS

3" x 5" x 1.40" Package	Inhibit, Power Fail, Output OK Signals
Up to 275W of AC-DC Power	Approved to EN/CSA/IEC/UL62368-1
For 1U Applications	Efficiency 92% Typical
Universal Input 90-264VAC Input Range	3 Years Warranty
Standby and Fan Output Voltages	RoHS Compliant
Forced Current Share	

MODEL SELECTION

Model Number	Volts	Output Current		Fan Output	Ripple & Noise**	Total Regulation	OVP Threshold***
		w/200LFM air	Convection*				
CINT1275A1214K01	12V	21.8A	15.0A	12V DC/1A	120mV pk-pk	±3%	14.0 ± 1.1V
CINT1275A1514K01	15V	18.3A	12.0A	12V DC/1A	150mV pk-pk	±3%	19.5 ± 1.5V
CINT1275A2414K01	24V	10.9A	7.50A	12V DC/1A	240mV pk-pk	±3%	28.0 ± 2.5V
CINT1275A4814K01	48V	5.46A	3.75A	12V DC/1A	480mV pk-pk	±3%	55.0 ± 4.0V
CINT1275A5614K01	56V	4.68A	3.21A	12V DC/1A	560mV pk-pk	±3%	59.0 ± 1.0V

Note: * Total convection power is 180 Watts.

** Measured with noise probe directly across output terminals, and load terminated with 0.1µF ceramic and 10µF low ESR capacitors.

*** No output adjustment on 56V.

INPUT

AC Input	100-240VAC, ±10%, 47-63Hz, 1Ø 120-370V DC
Input Current	115VAC: 3A, 230VAC: 1.5A 3.7A max at 90VAC
Inrush Current	264VAC, cold start: will not exceed 50A
Input Fuses	F1, F2: 5A, 275VAC fuses provided on all models
Earth Leakage Current	<750µA @ 264VAC, 60Hz, NC
Efficiency	92% typical

OUTPUT

Hold-Up Time	16ms at 250W, 120VAC/60Hz
Turn On Time	Less than 2 sec @115VAC (inversely proportional to input voltage and thermistor temperature)
Switching Frequency	PFC: Variable, 30kHz - 400kHz Main converter: Variable 30-250kHz, 65-70kHz at full load
Output Power	275W continuous, with 200 LFM airflow, 180W convection cooled - See chart for specific voltage model ratings
Output Voltage	See chart
Ripple and Noise	0.5%rms, 1% pk-pk, See chart
Transient Response	500µS typical, Return to 0.5% of nominal, 50% load step Di/Dt: <0.2A/µS. Max voltage deviation = 3%
Voltage Adjustability	+/-5% from nominal (except 56V)
Minimum Load	Not required
Total Regulation	+/- 3% combined line, load and initial setting

SAFETY

Safety Standards	EN/CSA/IEC/UL62368-1
Shock	Operating: Half-sine, 20gpk, 10ms, 3 axes, 6 shocks total Non-operating: Half-sine, 40gpk, 10ms, 3 axes, 6 shocks total



PROTECTION

Overtemperature Protection	Sensing transformer temperature, 135°C (55°C ambient temperature at full load), Latching type
Overload Protection	120 to 150% of rating, Hiccup mode
Short Circuit Protection	Hiccup mode, Auto recovery
Overvoltage Protection	OVP latch at 110 to 130% of output voltage

ENVIRONMENT

Operating Temperature	-10°C to +70°C Start up at -40°C, Full load
Temperature Derating	Derate output power linearly above 50°C to 50% at 70°C
Storage Temperature	-40°C to +85°C
Altitude	Operating: -500 to 10,000 ft Non-operating: -500 to 40,000 ft
Relative Humidity	5% to 95%, Non-condensing
Vibration	Operating: 0.003g/Hz, 1.5grms overall, 3 axes, 10 min/axis Non-operating: 0.026g2/Hz, 5.0grms overall, 3 axes, 1 hr/axis
Dimensions	W: 3.0" x L: 5.0" x H: 1.40" (from bottom of PC board)
Weight	325g

EMI/EMC COMPLIANCE

Conducted Emissions	EN55011/22 Class B, FCC Part 15, Subpart B, Class B
Radiated Emissions	EN55011/22 Class A, FCC Part 15, Subpart B, Class A w/6db margin
Static Discharge Immunity	EN61000-4-2, 6kV contact discharge, 8kV air discharge
Radiated RF Immunity	EN61000-4-3, 3V/m
EFT/Burst Immunity	EN61000-4-4, 2kV/5kHz
Line Surge Immunity	EN61000-4-5, 1kV differential, 2kV common-mode
Conducted RF Immunity	EN61000-4-6, 3Vrms
Power Frequency Magnetic Field Immunity	EN61000-4-8, 3A/m
Voltage Dip Immunity	EN61000-4-11, 100%, 10ms; 30%, 275ms; 60%, 100ms; Performance Criteria A, A, & A at 70% load
Line Harmonic Emissions	EN61000-3-2, Class A, B, C, & D
Flicker Test	EN61000-3-3, Complies (dmax<6%)

RELIABILITY

MTBF	4,65,000 hours, 275W load, 110VAC input, 25°C ambient
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ISOLATION SPECIFICATION

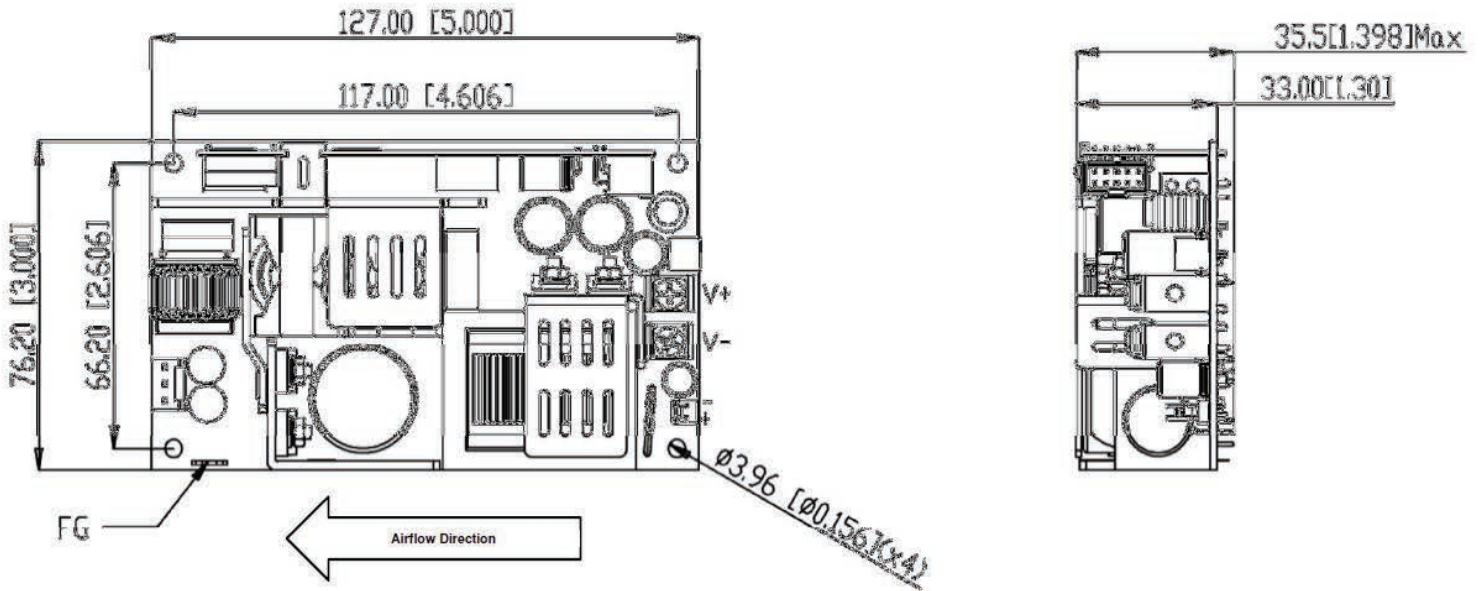
Isolation	Input-Output: 4,000VAC Input-Ground: 1,800VAC Output-Ground: 1,500VAC
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AUXILIARY SIGNALS

Standby Output	5V @ 200mA
AC Power Fail	Goes LOW with 5ms warning before loss of DC output after loss of AC power
Inhibit	Connect to inhibit pin (J201 pin 5) to output common to inhibit the DC output
Remote Sense	Compensates for up to 250mV drop in load lines
DC OK	Open collector logic signal goes and stays HIGH 100ms to 500ms after main output reaches regulation
Fan Output	12V @ 1A
Current Share	Forced current sharing provided for up to 5 units connected in parallel



MECHANICAL DRAWING



- Note: 1. All dimensions in inches (mm), tolerance is +/-0.02".
 2. Mounting holes should be grounded for EMI purposes.
 3. FG is safety ground connection.
 4. The power supply requires mounting on metal standoffs 0.20" (5mm) in height, min.

CONNECTOR INFORMATION

Input Connector	Ground	DC Output Connector	Fan Output Connector	Signal Connector
PIN 1) AC LINE PIN 2) EMPTY PIN 3) AC NEUTRAL	0.25" FASTON TAB	Term. 1: +V _{out} Term. 2: -V _{out}	PIN 1) +12V fan RTN PIN 2) +12V fan	PIN 1) Remote Sense (+) PIN 2) Common PIN 3) Remote Sense (-) PIN 4) Current Share PIN 5) Inhibit PIN 6) Common PIN 7) Power Good PIN 8) +5Vsb PIN 9) DC OK PIN 10) +5Vsb RTN
Mating Connector: Molex 09-50-3031 Pins: 08-52-0072	Mating Connector: Molex 01 - 90020001	Mating Connector: Molex 19141- 0058/0063/0083	Mating Connector: Molex 22-01-3027 Pins: 08-50-0114	Mating Connector: Molex 90142-0010 Pins: 90119-2109 or 2120

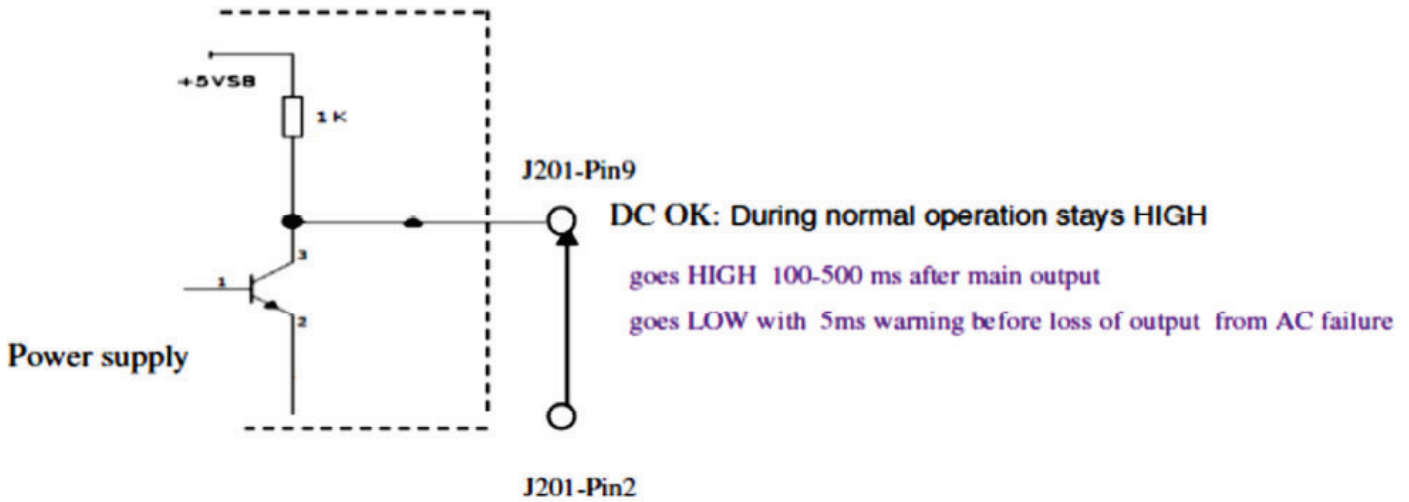
FAN OUTPUT – J301

J301 provides a 12V@1A output to support a system cooling fan.



AC POWER FAILURE/DC OK CURRENT SHARE AND INHIBIT SIGNALS – J201

1. POWER FAIL / DC OK (FIGURE 1)

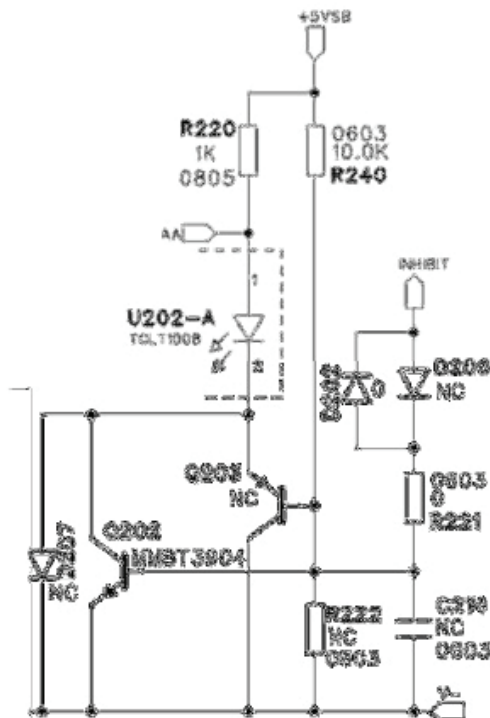


Note: Because Power Fail and DC OK use the same pin, the signals can be monitored as follows:

DC OK: When J201-Pin 9 is HIGH

AC Power Fail: When J201-Pin 9 is LOW

2. INHIBIT CIRCUIT (FIGURE 2)



Remote inhibit control of the DC output.

J201 pin 5 open = ON

J201 pin 5 LOW or GND = OFF



3. CURRENT SHARING/REMOTE SENSE (FIGURE 3)

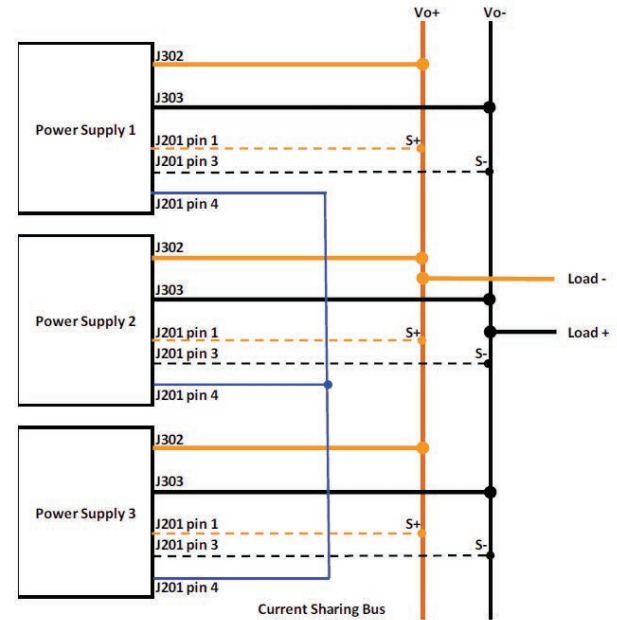
The outputs of N+1 (N=1,2...5) models can be shared. It is shown in Figure 3, one load-share controller is required for each model and circuits are identical when N+ 1 identical models are used.

Terminals J302 and J303 are connected to the Vo+ and Vo-, respectively, of the first power model. The Vo+ and Vo- correspond to the other models positive and negative output pins. The Vo+ connects to positive output bus to the load and Vo connects the negative output bus to the load.

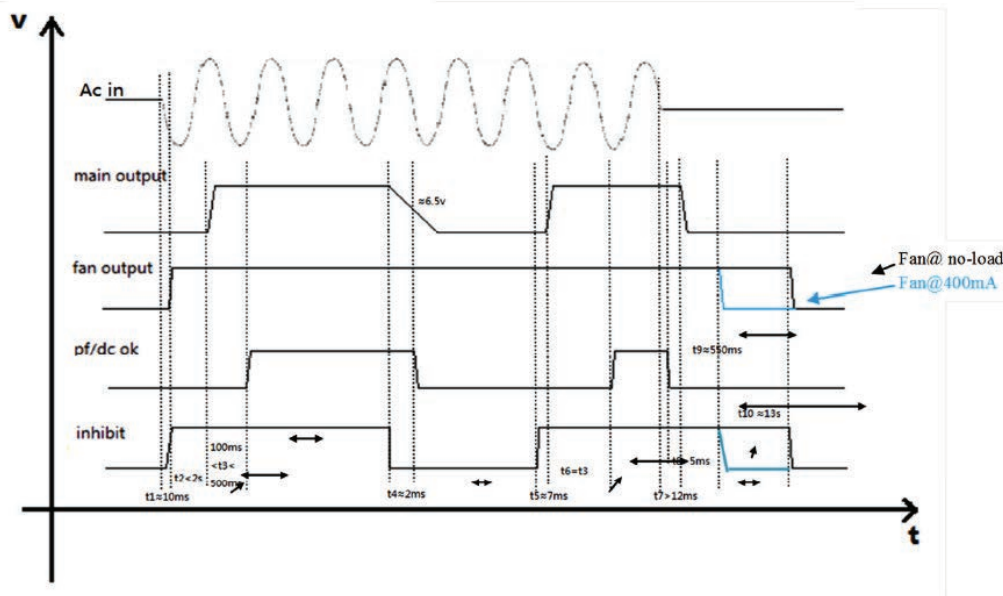
The J201 pin1 and pin3 connects to the S+ and S-, respectively, of the first power models. The S+ and S- correspond to the other models J201 pin1 and pin3. The S+ connects to positive output bus and S- connects to negative output bus.

Remote Sense < 250mV drop compensation:

The J201 Pin4 connects to current sharing bus that it connects to other models J201 pin4.



TIMING SEQUENCE



ISOLATION SPECIFICATIONS

Parameter	Conditions/Description	Min	Nom	Max	Units
Insulation Safety Rating	Input/Ground Input/Output Output/Ground		Basic Reinforced	n/a	
Electric Strength Test Voltage	Input/Ground	1800			VAC
	Input/Output	4000	-	-	VAC
	Output/Ground	1500			VAC



INPUT SPECIFICATIONS

Parameter	Conditions/Description	Min	Nom	Max	Units
Input Voltage		90	115/230	264	VAC
Turn-On Input Voltage	Ramping up		80		VAC
Turn-Off Input Voltage	Ramping down		75		VAC
Input Frequency		47	50/60	63	Hz
Inrush Current Limitation	264VAC, Cold start	-	-	50	A
Power Factor	Vi nom, Io nom	0.9	-	-	
Efficiency	Vi nom, Io nom CINT1275A1214K01 CINT1275A1514K01 CINT1275A2414K01 CINT1275A4814K01 CINT1275A5614K01	-	92%	-	%

Note: All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

OUTPUT SPECIFICATIONS

Parameter	Conditions/Description	Min	Nom	Max	Units
Output Voltage Setpoint Accuracy CINT1275A1214K01	Vi nom,, Io1 @ ADC, TC = 25 °C	-3	-	3	% Vo nom
Output Voltage Setpoint Accuracy CINT1275A1514K01	Vi nom,, Io1 @ ADC, TC = 25 °C	-3	-	3	% Vo nom
Output Voltage Setpoint Accuracy CINT1275A2414K01	Vi nom,, Io1 @ ADC, TC = 25 °C	-3	-	3	% Vo nom
Output Voltage Setpoint Accuracy CINT1275A4814K01	Vi nom,, Io1 @ ADC, TC = 25 °C	-3	-	3	% Vo nom
Output Voltage Setpoint Accuracy CINT1275A5614K01	Vi nom,, Io1 @ ADC, TC = 25 °C	-3	-	3	% Vo nom
Output Current V1 Output Current V2	CINT1275A1214K01	0 0	15.0 -	21.83 1.0	ADC ADC
Output Current V1 Output Current V2	CINT1275A1514K01	0 0	12.0 -	17.47 1.0	ADC ADC
Output Current V1 Output Current V2	CINT1275A2414K01	0 0	7.5 -	10.92 1.0	ADC ADC
Output Current V1 Output Current V2	CINT1275A4814K01	0 0	3.75 -	5.46 1.0	ADC ADC
Output Current V1 Output Current V2	CINT1275A5614K01	0 0	3.21 -	4.68 1.0	ADC ADC
Static Line Regulation V1	Vi min-Vi max, Vi nom, 0-100% Io nom	-1	-	1	% Vo nom
Static Load Regulation V1 (Droop Characteristic)	Vi min-Vi max, Vi nom, 0-100% Io nom	-3	-	3	% Vo nom
Hold-Up Time	Starting at Vi = 230 VAC, Po nom	-	16	-	ms
Dynamic Load Regulation	Load change =50%, di/dt =0.2A/μS voltage deviation 3%	0		3	% Vo nom
Start-Up Time	Vi nom, Io nom	0	-	2	s



PROTECTION

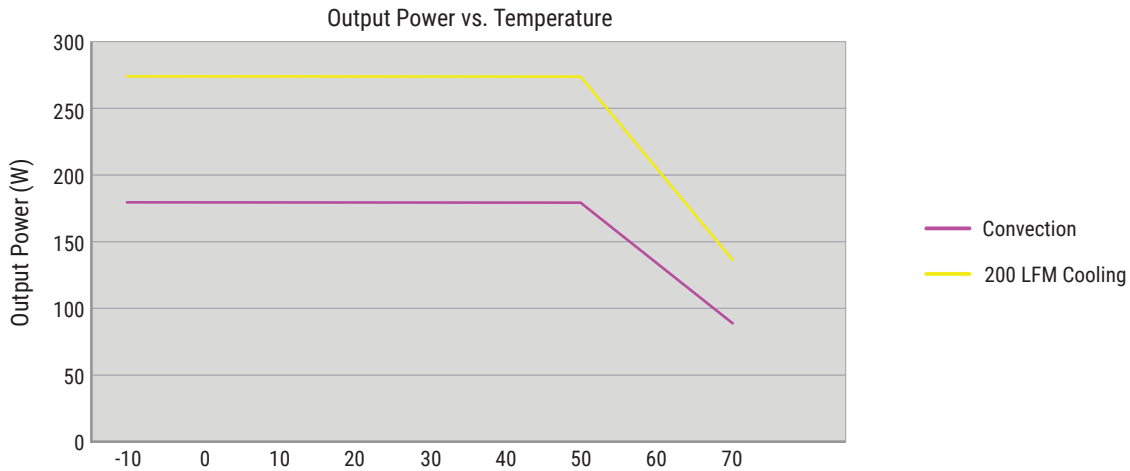
Parameter	Conditions/Description	Min	Nom	Max	Units
Input Fuse	Not user accessible				
Input Transient Protection	2KV(CM) and 1KV(DM) surge			2	KV(CM)
Output	No-load and short circuit proof		Hiccup		
	Short circuit proof		Hiccup		
	Overload (latch style)		Hiccup		
Power Factor	Latch style		Latch		
Power Factor	Automatic power shutdown at TC =135°C				

Note: All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted

CHARACTERISTIC CURVES

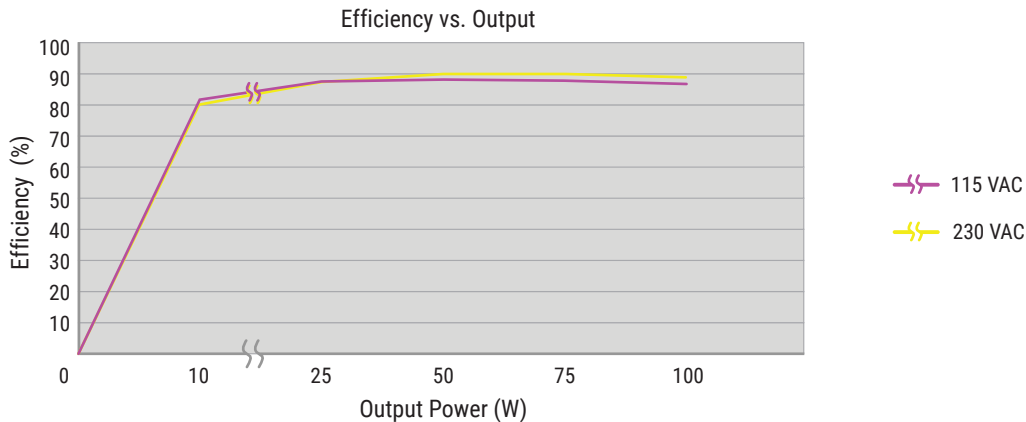
OUTPUT VS. TEMPERATURE

180W convection cooled and 275W continuous with 200 LFM airflow, derate output power to 50% at 70°C.



EFFICIENCY VS. LOADING

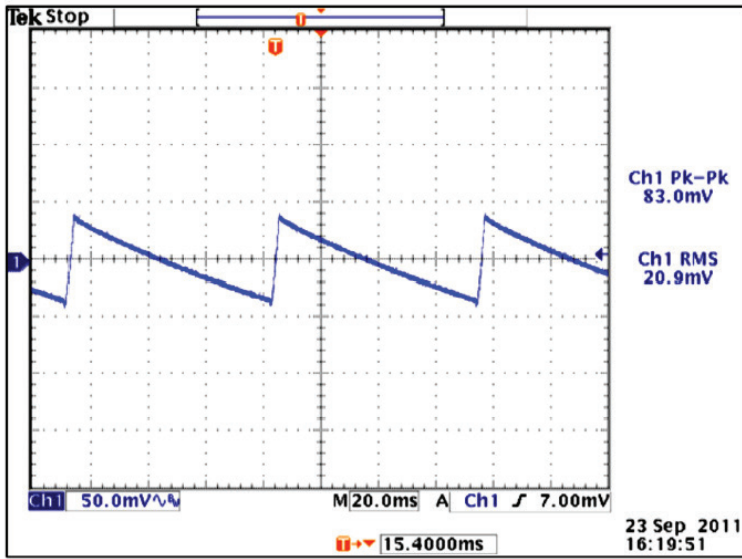
The high efficiency is achieved by using LLC technology with CCM mode PFC topology, and synchronous rectifiers on the output in all of this family models, minimizing switching losses.



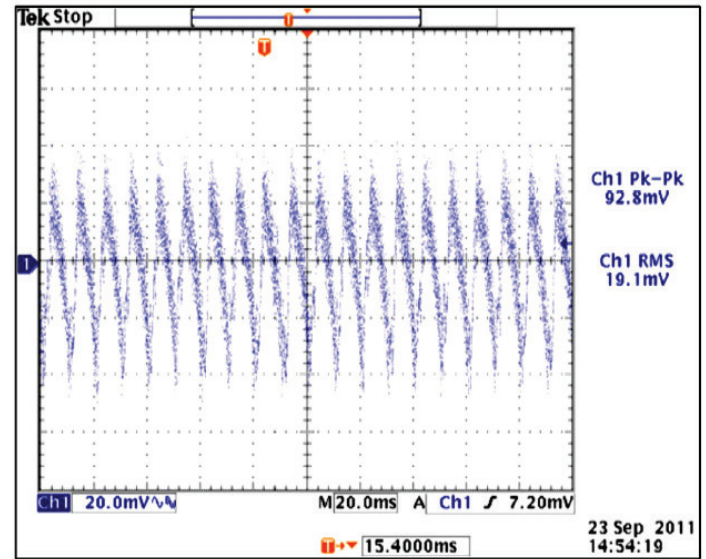


NOISE & RIPPLE

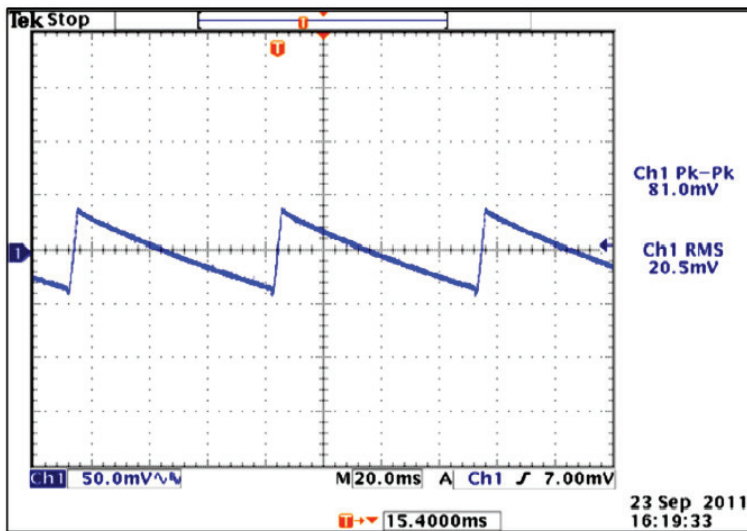
To verify that the output ripple and noise does not exceed the level specified in the product specification. Measured using a scope probe socket with 0.1uF ceramic and a 10uF electrolysis capacitor connected in parallel across it, BW limit with 20MHz.



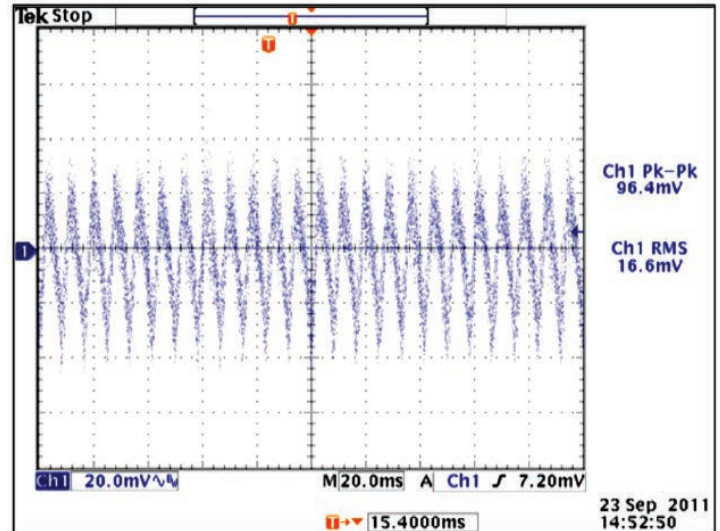
12V OUT, NO LOAD, 115VAC, 60HZ



12V OUT, FULL LOAD, 115VAC, 60HZ



12V OUT, NO LOAD, 230VAC, 60HZ

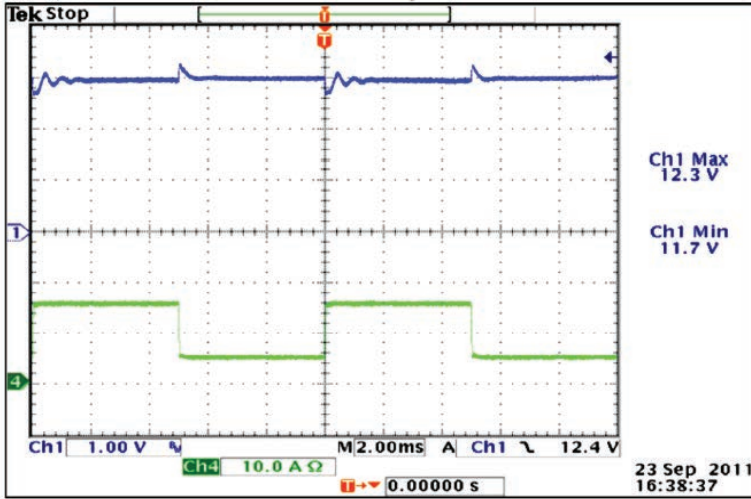


12V OUT, FULL LOAD, 230VAC, 60HZ

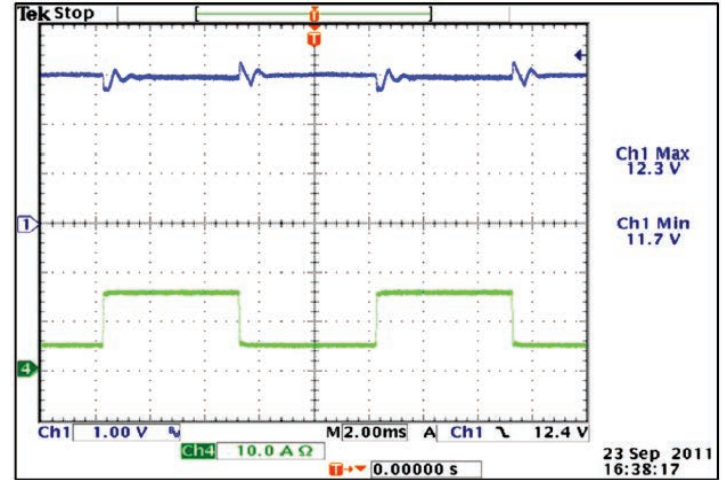


OUTPUT TRANSIENT RESPONSE

50% load step within the regulation limits of minimum and maximum load, $di/dt < 0.2A/\mu Sec$. Recovery time not specified as there is no laps in regulation with a 50% Load Step. Maximum voltage deviation is 3%, This test is performed on the MAIN OUTPUT ONLY.



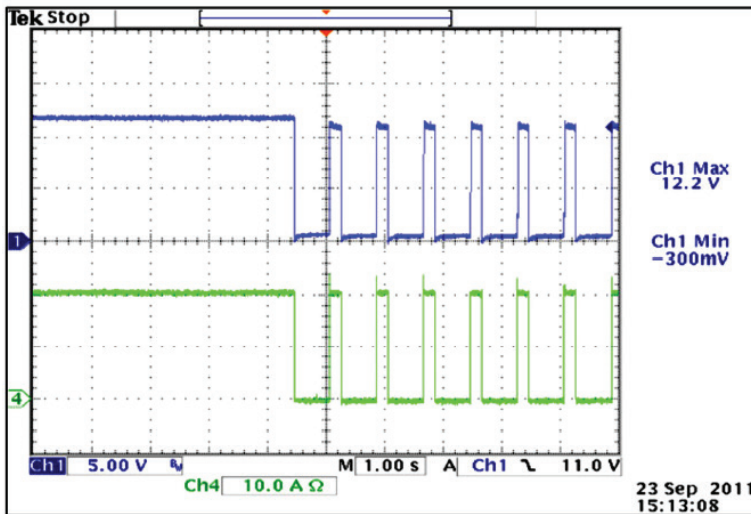
12V OUT, 115VAC, 25% TO 75% LOAD STEP



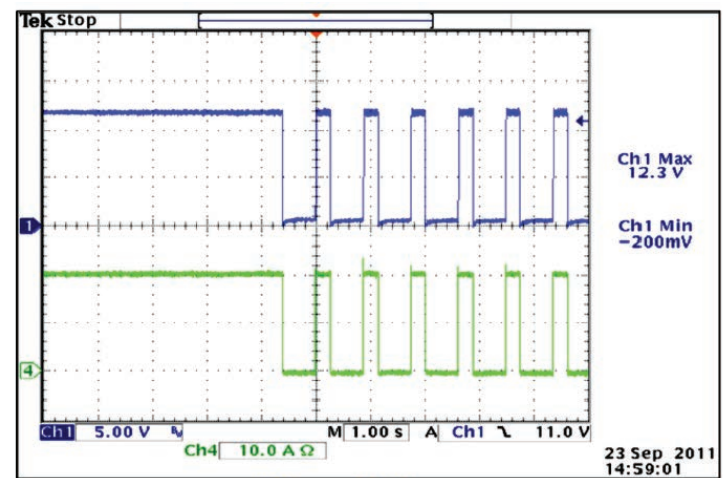
12V OUT, 230VAC, 25% TO 75% LOAD STEP

OUTPUT OVERLOAD CHARACTERISTIC

Supply shall protect itself against Overload conditions. The Power Supply shall recover from Overload Conditions without operator intervention.



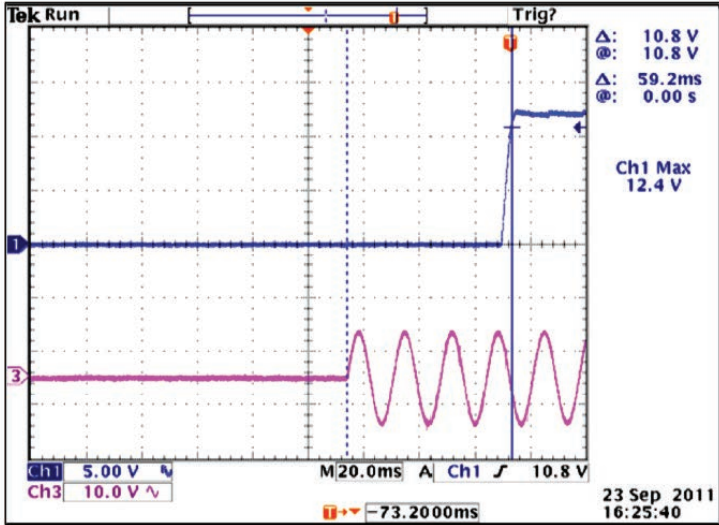
12V OUT, 90VAC



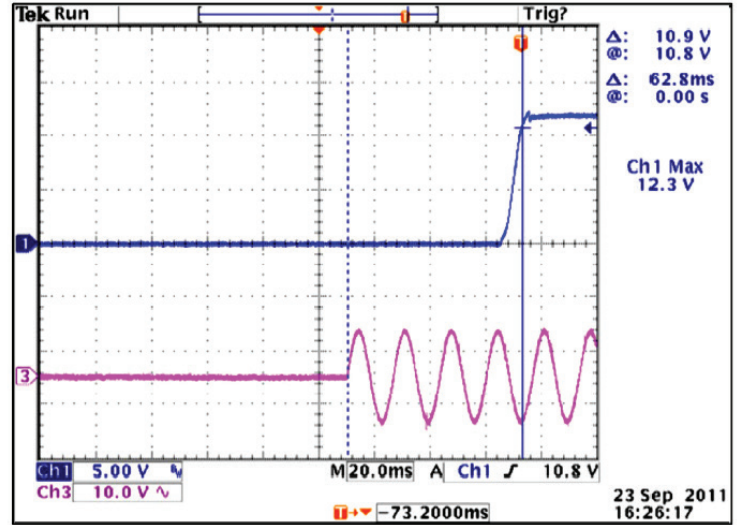
12V OUT, 264VAC



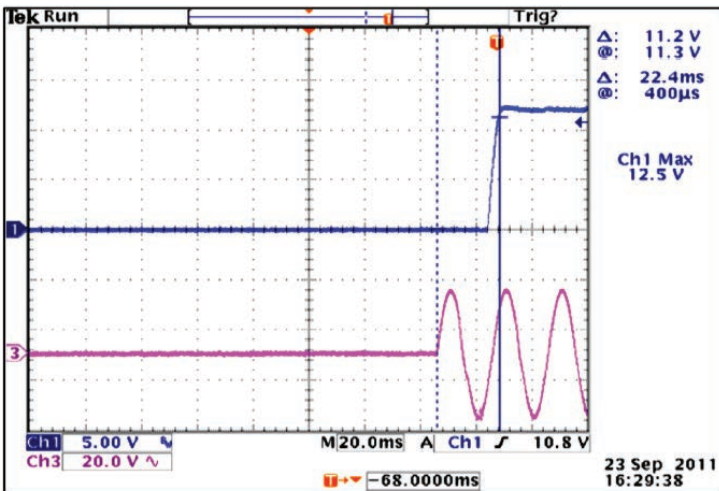
TURN-ON TIME



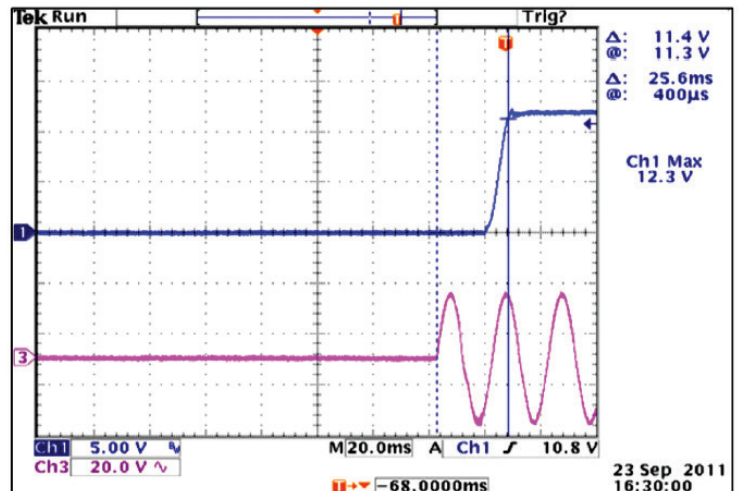
12V OUT, NO LOAD, 90VAC



12V OUT, FULL LOAD, 90VAC



12V OUT, NO LOAD, 264VAC



12V OUT, FULL LOAD, 264VAC