

PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

28701-BMR 717 Rev. A

July 2021

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Key Features

- Ultra-wide input voltage range 12:1
- Industry standard case dimensions
60.96 * 57.91 * 12.70 mm (2.40 * 2.28 * 0.5 in)
- High Efficiency up to 90%
- 4000 Vdc input to output isolation
- Compliant with functional insulation and safety standard of IEC/UL 62368
- Compliant with EN50155 standard
- EN45545-2 compliant



General Characteristics

- Input under voltage shutdown
- Remote control
- Output over voltage protection
- Over temperature protection
- Output short-circuit protection
- Output voltage adjust function
- ISO 9001/14001 certified supplier

Safety Approvals



Design for Environment



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Technical Specification

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Ordering Information

Product program	Output
PKJ7213WPI	12V, 16.67A / 200 W
PKJ7217KWPI	13.8V, 14.5A / 200 W
PKJ7215WPI	15V, 13.33A / 200 W
PKJ7216ZWPI	24V, 8.33A / 200 W
PKJ7216JWPI	48V, 4.16A / 200 W
PKJ7216HWPI	54V, 3.7A / 200 W

Product number and Packaging

PKJ721XXWPI n ₁	
Options	n ₁
Remote Control logic	0

Options	Description
n ₁	P Negative* Positive

Example: a 12V output, positive logic module would be PKJ7213WPIP.

The products are delivered in trays. See details in Delivery Package Information.

* Standard variant (i.e. no option selected).

General Information**Reliability**

The failure rate (λ) and mean time between failures (MTBF = $1/\lambda$) is calculated at max output power and an operating ambient temperature (T_A) of +45°C. Flex uses Telcordia SR-332 Issue 3 Method 1 and/or MIL-HDBK-217F, Notice 2 to calculate the mean steady-state failure rate and standard deviation (σ).

Telcordia SR-332 Issue 3 also provides techniques to estimate the upper confidence levels of failure rates based on the mean and standard deviation.

Mean steady-state failure rate, λ	Std. deviation, σ
246.279nFailures/h	362.249nFailures/h

MTBF (mean value) for the PKJ721XXW series = 4.06Mh.
MTBF at 90% confidence level = 2.56Mh

In MIL-HDBK-217F, all part reliability models include the effects of environmental stresses through the environmental factor, πE . It encompasses the major areas of equipment use, here we use ground benign, GB.

Mean Time Between Failure (MTBF) According to MIL-HDBK-217F

Conditions	Temperature	Specification
Ground Benign (GB)	Case at 25°C	292.71 Kh
	Case at 85°C	50.73 Kh

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2011/65/EU and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Flex products are found in the Statement of Compliance document.

Flex fulfills and will continuously fulfill all its obligations under regulation (EC) No 1907/2006 concerning the registration, evaluation, authorization and restriction of chemicals (REACH) as they enter into force and is through product materials declarations preparing for the obligations to communicate information on substances in the products.

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, Six Sigma, and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of the products.

Warranty

Warranty period and conditions are defined in Flex General Terms and Conditions of Sale.

Limitation of Liability

Flex does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

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Technical Specification

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Safety Specification**General information**

Flex Power DC/DC converters and DC/DC regulators are designed in accordance with the safety standards IEC 62368-1, EN 62368-1 and UL 62368-1 *Audio/video, information and communication technology equipment - Part 1: Safety requirements*

IEC/EN/UL 62368-1 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Electrically-caused fire
- Injury caused by hazardous substances
- Mechanically-caused injury
- Skin burn
- Radiation-caused injury

On-board DC/DC converters, Power interface modules and DC/DC regulators are defined as component power supplies. As components they cannot fully comply with the provisions of any safety requirements without “conditions of acceptability”. Clearance between conductors and between conductive parts of the component power supply and conductors on the board in the final product must meet the applicable safety requirements. Certain conditions of acceptability apply for component power supplies with limited stand-off (see Mechanical Information for further information). It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable safety standards and regulations for the final product.

Component power supplies for general use shall comply with the requirements in IEC/EN/UL 62368-1. Product related standards, e.g. IEEE 802.3af *Power over Ethernet*, and ETS-300132-2 *Power interface at the input to telecom equipment, operated by direct current (dc)* are based on IEC/EN/UL 62368-1 with regards to safety.

Flex Power DC/DC converters, Power interface modules and DC/DC regulators are UL 62368-1 recognized and certified in accordance with EN 62368-1. The flammability rating for all construction parts of the products meet requirements for V-0 class material according to IEC 62368-11-10, *Fire hazard testing, test flames – 50 W* horizontal and vertical flame test methods.

Isolated DC/DC converters & Power interface modules

The product may provide basic or functional insulation between input and output according to IEC/EN/UL 62368-1 (see Safety Certificate), different conditions shall be met if the output of a basic or a functional insulated product shall be considered as ES1 energy source.

For basic insulated products (see Safety Certificate) the output is considered as ES1 energy source if one of the

following conditions is met:

- The input source provides supplementary or double or reinforced insulation from the AC mains according to IEC/EN/UL 62368-1.
- The input source provides functional or basic insulation from the AC mains and the product’s output is reliably connected to protective earth according to IEC/EN/UL 62368-1.

For functional insulated products (see Safety Certificate) the output is considered as ES1 energy source if one of the following conditions is met:

- The input source provides double or reinforced insulation from the AC mains according to IEC/EN/UL 62368-1.
- The input source provides basic or supplementary insulation from the AC mains and the product’s output is reliably connected to protective earth according to IEC/EN/UL 62368-1.
- The input source is reliably connected to protective earth and provides basic or supplementary insulation according to IEC/EN/UL 62368-1 and the maximum input source voltage is 60 Vdc.

Galvanic isolation between input and output is verified in an electric strength test and the isolation voltage (V_{iso}) meets the voltage strength requirement for basic insulation according to IEC/EN/UL 62368-1.

It is recommended to use a slow blow fuse at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter. In the rare event of a component problem that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the fault from the input power source so as not to affect the operation of other parts of the system
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating

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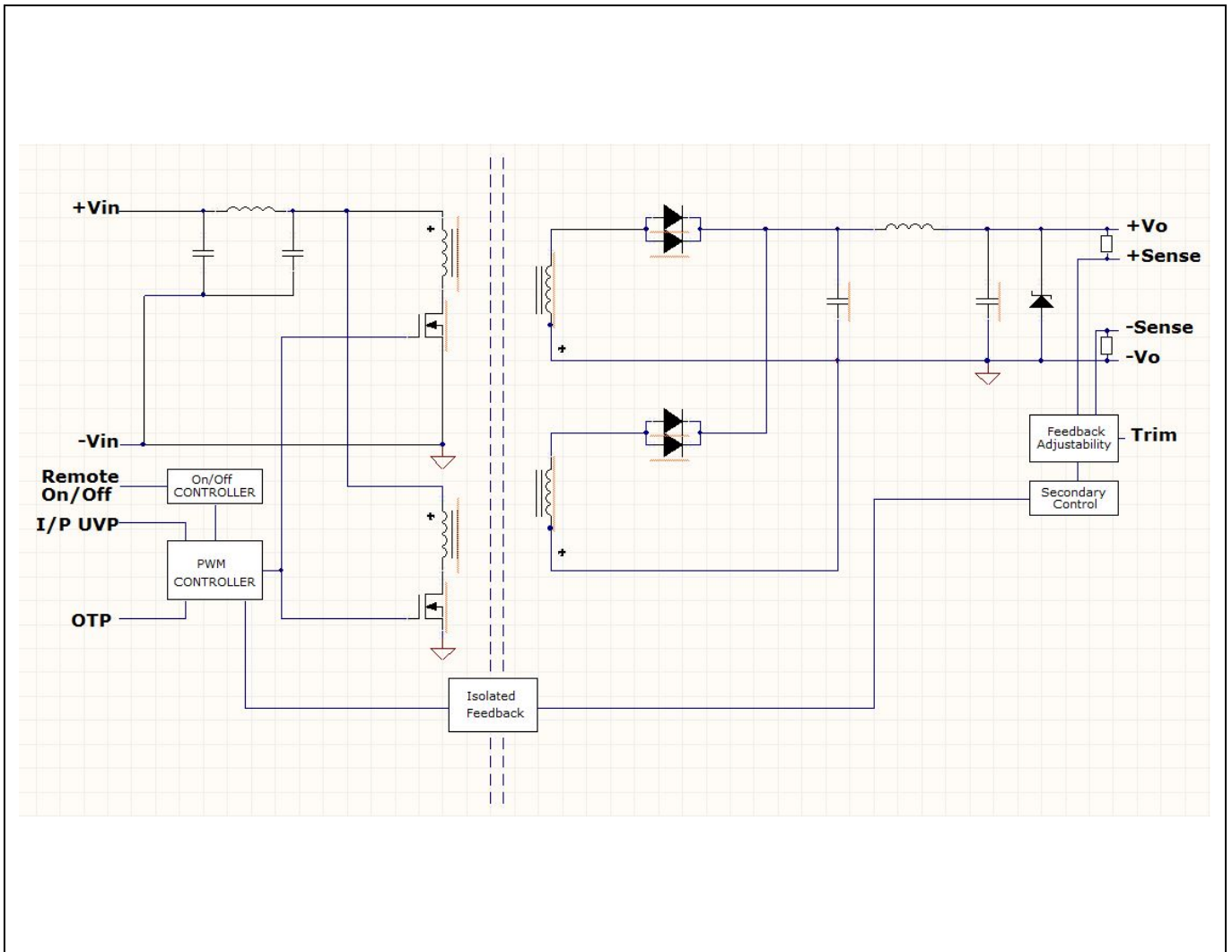
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Absolute Maximum Ratings

Characteristics		min	typ	max	Unit
T_{P1}	Operating Temperature (see Thermal Consideration section)	-40		+115	°C
T_S	Storage temperature	-55		+125	°C
V_I	Input voltage	14		160	V
V_{iso}	Isolation voltage (input to output)			4000	Vdc
V_{iso}	Isolation voltage (input to baseplate)			2250	Vdc
V_{iso}	Isolation voltage (output to baseplate)			1600	Vdc
V_{tr}	Input voltage transient (tp 1s)			200	V
V_{adj}	Adjust pin voltage (see Operating Information section)	0		$1.15 \times V_o$	V
V_{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option		8	V
		Negative logic option		8	V

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the Electrical Specification section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Fundamental Circuit Diagram



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Electrical Specification
12 V, 16.67 A / 200 W

PKJ7213WPI(P)

$T_{P1} = -40$ to 115°C , $V_I = 14$ to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = \text{max } I_O$, unless otherwise specified under Conditions.

Additional $C_{in} = 100$ μF , $C_{out} = 0.1$ μF ceramic Cap. + 10 μF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		14		160	V
V_{loff}	Turn-off input voltage	Decreasing input voltage		12		V
V_{lon}	Turn-on input voltage	Increasing input voltage		13		V
C_i	Internal input capacitance			10		μF
P_O	Output power		0		200	W
η	Efficiency	50% of max I_O , $V_I = 110$ V		88		%
		max I_O , $V_I = 110$ V		90		
P_d	Power Dissipation	max I_O		15	28	W
P_{ii}	Input idling power	$I_O = 0$ A, $V_I = 110$ V		0.3		W
P_{RC}	Input standby power	$V_I = 110$ V (turned off with RC)		0.1		W
f_s	Switching frequency	0-100 % of max I_O	170	200	230	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = 16.67$ A	11.88	12	12.12	V
V_O	Output adjust range	See operating information	10.8	12	13.2	V
	Output voltage tolerance band	0-100% of max I_O	11.64		12.36	V
	Idling voltage	$I_O = 0$ A	11.64		12.36	V
	Line regulation	max I_O		30	60	mV
	Load regulation	$V_I = 110$ V, 0-100% of max I_O		60	120	mV
V_{tr}	Load transient voltage deviation	$V_I = 110$ V, Load step 50-75-50% of max I_O , $di/dt = 100$ mA/ μs		± 275	± 500	mV
t_{tr}	Load transient recovery time			250	500	μs
t_r	Ramp-up time (from 10-90% of V_{Oi})	100% of max I_O		5	10	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8	15	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		2	5	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		16.67	A
I_{lim}	Current limit threshold	$V_I = 110$ V, $T_{P1} < \text{max } T_{P1}$	20	24	33	A
I_{sc}	Short circuit current	$T_{P1} = 25^{\circ}\text{C}$, see Note 1		1.95		A
C_{out}	Recommended Capacitive Load	$T_{P1} = 25^{\circ}\text{C}$	0		3000	μF
V_{Oac}	Output ripple & noise	See ripple & noise section, $V_I = 110$ V, max I_O , see Note 2		100	200	mVp-p
OVP	Over voltage protection	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, 0-100% of max I_O		15		V

Note 1: Output Current (RMS): hiccup mode

Note 2: Measured with 0.1 μF ceramic Cap. and 10 μF tantalum (or E-Cap.) cross to output.

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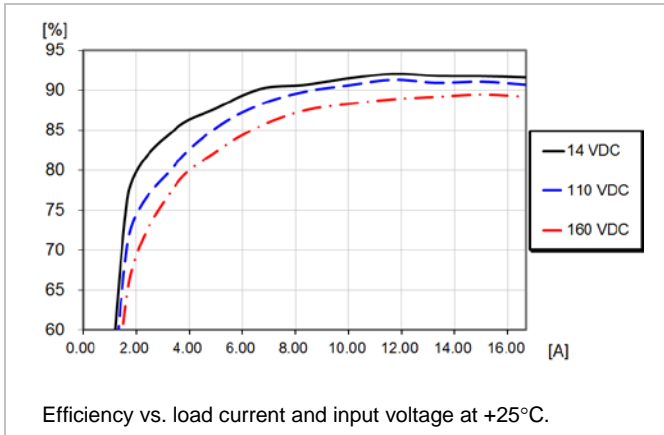
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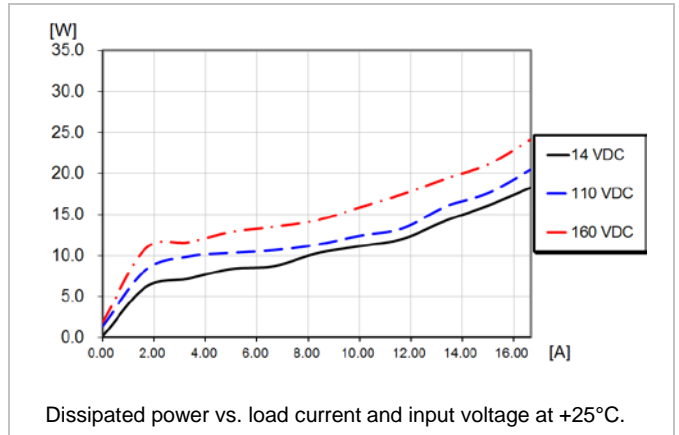
Typical Characteristics
12 V, 16.67 A / 200 W

PKJ7213WPI(P)

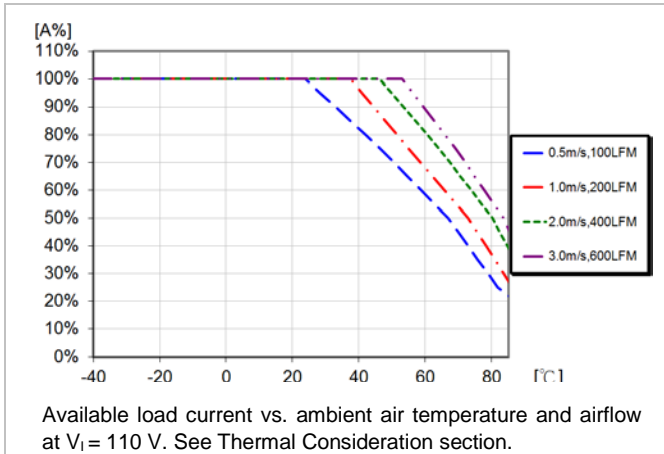
Efficiency



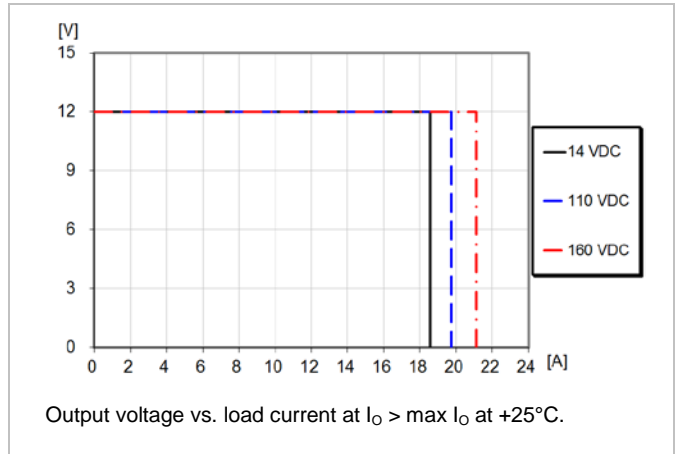
Power Dissipation



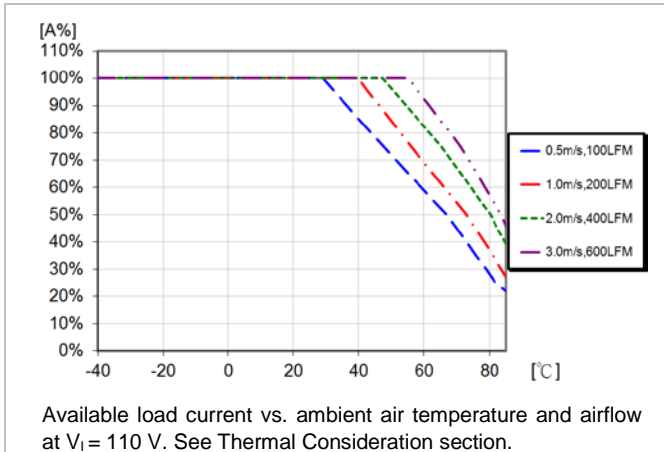
Output Current Derating



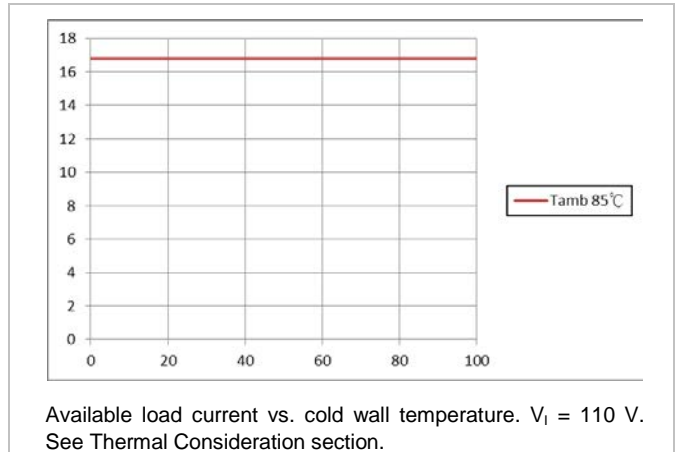
Current Limit Characteristics



Output Current Derating with 0.5" heatsink



Output Current Derating - Cold wall sealed box



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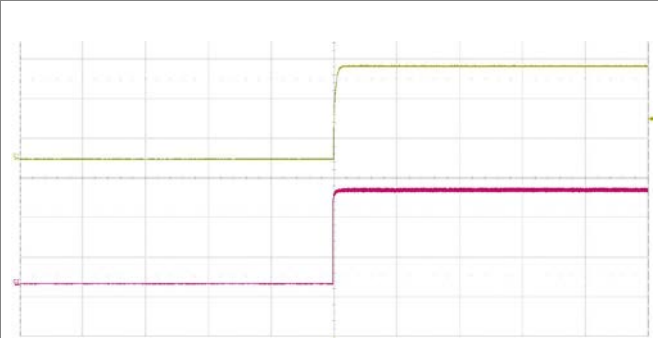
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Typical Characteristics
12 V, 16.67 A / 200 W

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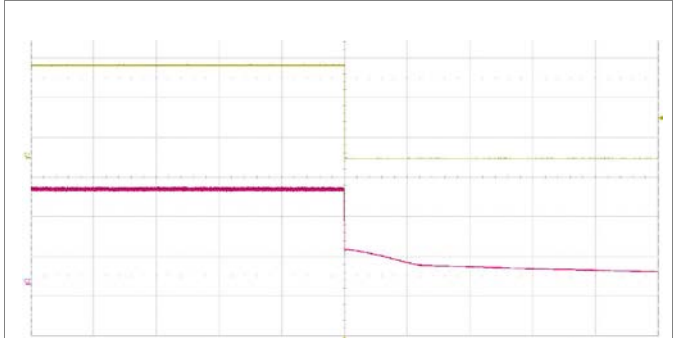
Start-up



Start-up enabled by connecting V_I at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$,
 $I_o = 16.67\text{ A}$ resistive load.

Top trace: output voltage (5 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

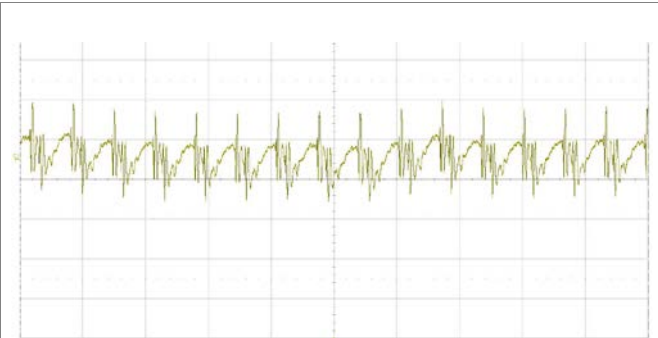
Shut-down



Shut-down enabled by disconnecting V_I at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$,
 $I_o = 16.67\text{ A}$ resistive load.

Top trace: output voltage (5 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

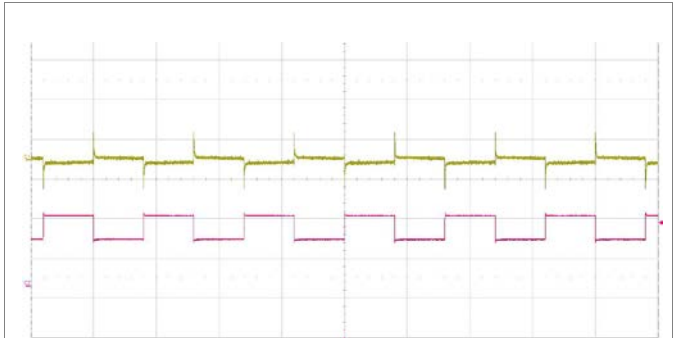
Output Ripple & Noise



Output voltage ripple at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$,
 $I_o = 16.67\text{ A}$ resistive load.

Trace: output voltage (20 mV/div.).
 Time scale: (5 μs /div.).
 20MHz bandwidth

Output Load Transient Response



Output voltage response to load current step-change (8.33-12.5-8.33 A) at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$.

Top trace: output voltage (500 mV/div.).
 Bottom trace: load current (5 A/div.).
 Time scale: (2 ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

Output Voltage = 12 V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{\text{ADJ_UP}} = \left(\frac{7.246}{\Delta} - 62 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{\text{ADJ_DOWN}} = \left(\frac{9.125}{\Delta} - 78.371 \right) \text{ k}\Omega$$

Example:

To trim up the 12 V model by 8% to 12.96 V the required external resistor is:

$$R_{\text{ADJ_UP}} = \left(\frac{7.246}{0.08} - 62 \right) = 28.58 \text{ k}\Omega$$

Example:

To trim down the 12 V model by 7% to 11.16 V the required external resistor is:

$$R_{\text{ADJ_DOWN}} = \left(\frac{9.125}{0.07} - 78.371 \right) = 51.99 \text{ k}\Omega$$

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Electrical Specification**13.8 V, 14.5 A / 200 W****PKJ7217KWPI(P)**

$T_{P1} = -40$ to 115°C , $V_I = 14$ to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = \text{max } I_O$, unless otherwise specified under Conditions.

Additional $C_{in} = 100$ μF , $C_{out} = 0.1$ μF ceramic Cap. + 10 μF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		14		160	V
V_{loff}	Turn-off input voltage	Decreasing input voltage		12		V
V_{lon}	Turn-on input voltage	Increasing input voltage		13		V
C_i	Internal input capacitance			10		μF
P_O	Output power		0		200	W
η	Efficiency	50% of max I_O , $V_I = 110$ V		88		%
		max I_O , $V_I = 110$ V		90		
P_d	Power Dissipation	max I_O		15	28	W
P_{ii}	Input idling power	$I_O = 0$ A, $V_I = 110$ V		0.3		W
P_{RC}	Input standby power	$V_I = 110$ V (turned off with RC)		0.1		W
f_s	Switching frequency	0-100 % of max I_O	170	200	230	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = 14.5$ A	13.662	13.8	13.938	V
V_O	Output adjust range	See operating information	12.42	13.8	15.18	V
	Output voltage tolerance band	0-100% of max I_O	13.386		14.214	V
	Idling voltage	$I_O = 0$ A	13.386		14.214	V
	Line regulation	max I_O		30	69	mV
	Load regulation	$V_I = 110$ V, 0-100% of max I_O		60	138	mV
V_{tr}	Load transient voltage deviation	$V_I = 110$ V, Load step 50-75-50% of max I_O , $di/dt = 100$ mA/ μs		± 275	± 500	mV
t_{tr}	Load transient recovery time			250	500	μs
t_r	Ramp-up time (from 10-90% of V_{Oi})	100% of max I_O		5	10	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8	15	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		2	5	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		14.5	A
I_{lim}	Current limit threshold	$V_I = 110$ V, $T_{P1} < \text{max } T_{P1}$	17.4	21.75	29	A
I_{sc}	Short circuit current	$T_{P1} = 25^{\circ}\text{C}$, see Note 1		1.95		A
C_{out}	Recommended Capacitive Load	$T_{P1} = 25^{\circ}\text{C}$	0		3000	μF
V_{Oac}	Output ripple & noise	See ripple & noise section, $V_I = 110$ V, max I_O , see Note 2		100	200	mVp-p
OVP	Over voltage protection	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, 0-100% of max I_O		18		V

Note 1: Output Current (RMS): hiccup mode

Note 2: Measured with 0.1 μF ceramic Cap. and 10 μF tantalum (or E-Cap.) cross to output.

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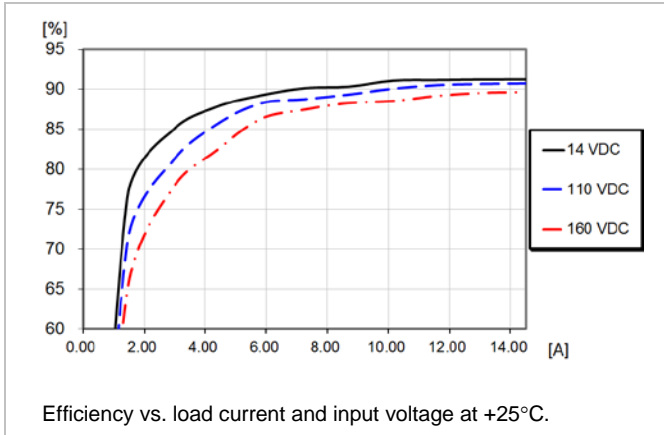
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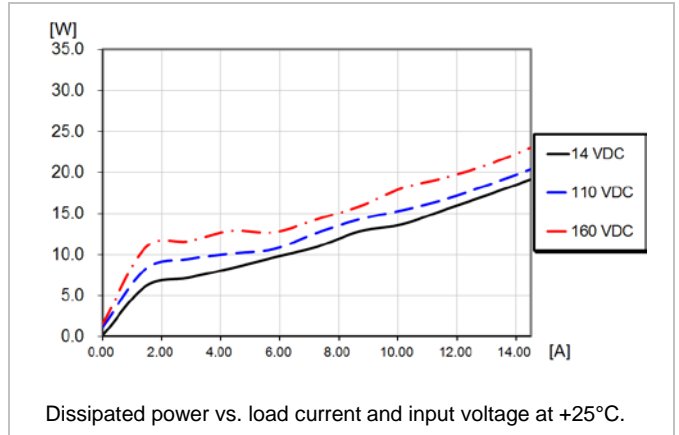
Typical Characteristics
13.8 V, 14.5 A / 200 W

PKJ7217KWPI(P)

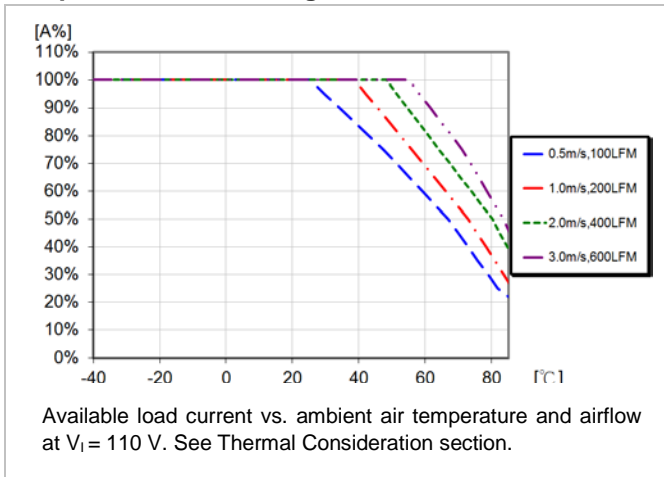
Efficiency



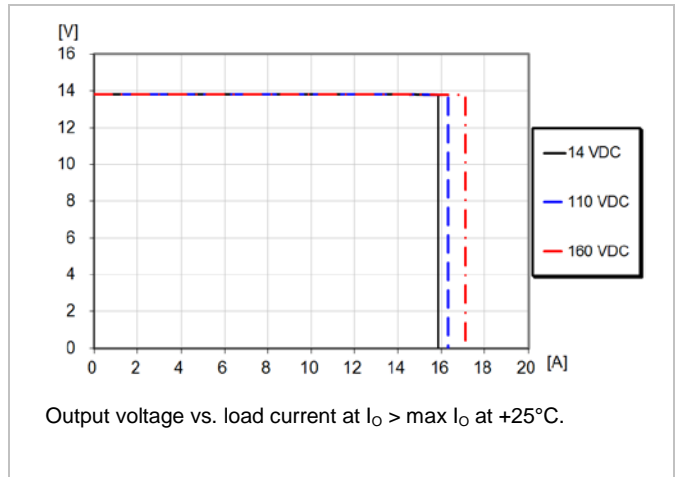
Power Dissipation



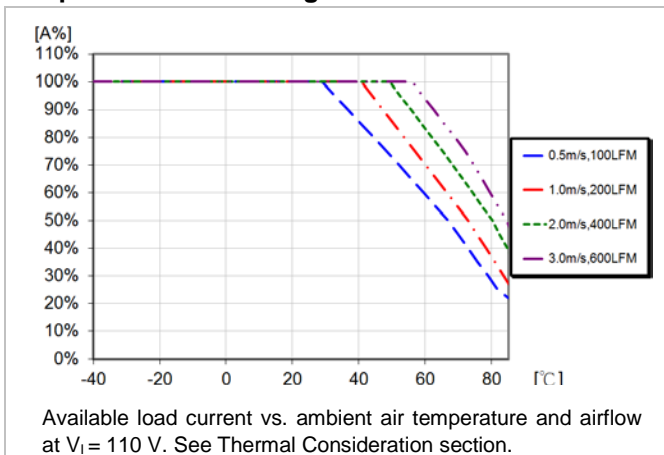
Output Current Derating



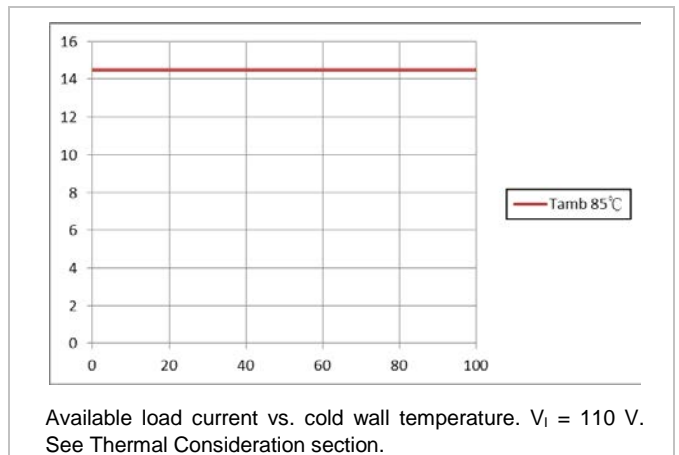
Current Limit Characteristics



Output Current Derating with 0.5" heatsink



Output Current Derating - Cold wall sealed box



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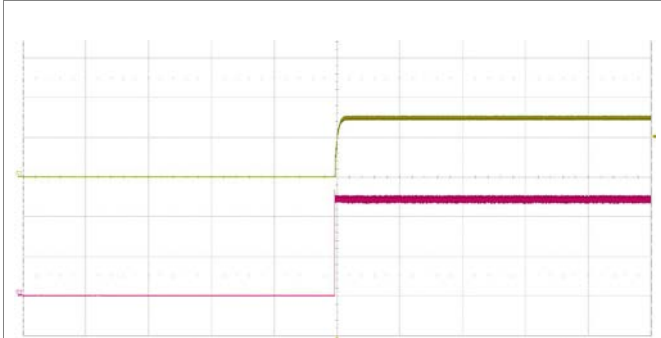
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Typical Characteristics
13.8 V, 14.5 A / 200 W

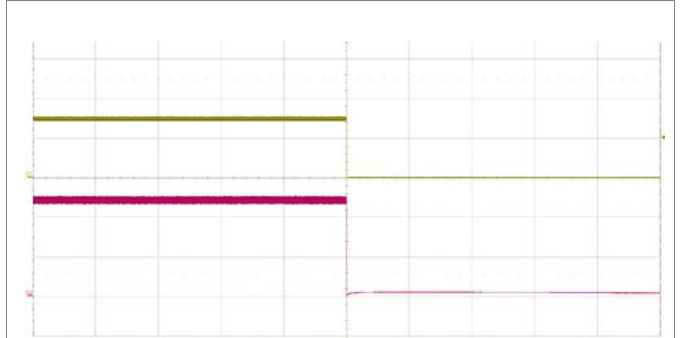
PKJ7217KWPI(P)

Start-up



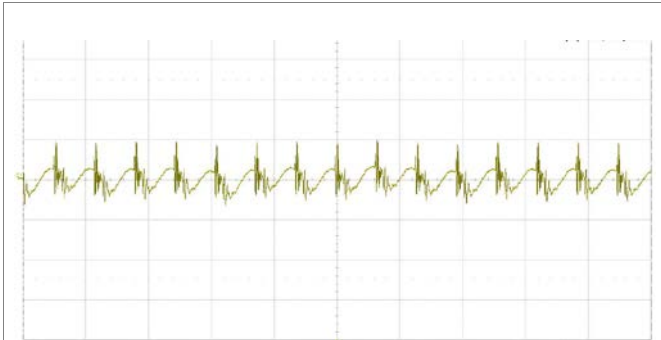
Start-up enabled by connecting V_i at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 14.5\text{ A}$ resistive load.
 Top trace: output voltage (10 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 14.5\text{ A}$ resistive load.
 Top trace: output voltage (10 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

Output Ripple & Noise



Output voltage ripple at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 14.5\text{ A}$ resistive load.
 Trace: output voltage (20 mV/div.).
 Time scale: (5 μs /div.).
 20 MHz bandwidth

Output Load Transient Response



Output voltage response to load current step-change (7.25-10.87-7.25 A) at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$.
 Top trace: output voltage (500 mV/div.).
 Bottom trace: load current (5 A/div.).
 Time scale: (2 ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

Output Voltage = 13.8 V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{ADJ_UP} = \left(\frac{8.304}{\Delta} - 50.0 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{ADJ_DOWN} = \left(\frac{11.1305}{\Delta} - 69.435 \right) \text{ k}\Omega$$

example:

o trim up the 13.8 V model by 5% to 14.55 V the required external resistor is:

$$R_{ADJ_UP} = \left(\frac{8.304}{0.05} - 50.0 \right) = 116.08 \text{ k}\Omega$$

example:

o trim down the 13.8 V model by 6% to 13.03 V the required external resistor is:

$$R_{ADJ_DOWN} = \left(\frac{11.1305}{0.06} - 69.435 \right) = 116.07 \text{ k}\Omega$$

Technical Specification

PKJ 7200W series DC-DC Converters
Input 14-160 V, Output up to 16.7 A / 200 W

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Electrical Specification
15 V, 13.33 A / 200 W

PKJ7215WPI(P)

$T_{P1} = -40$ to 115°C , $V_I = 14$ to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, max I_O , unless otherwise specified under Conditions.

Additional $C_{in} = 100$ μF , $C_{out} = 0.1$ μF ceramic Cap. + 10 μF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		14		160	V
V_{loff}	Turn-off input voltage	Decreasing input voltage		12		V
V_{lon}	Turn-on input voltage	Increasing input voltage		13		V
C_i	Internal input capacitance			10		μF
P_O	Output power		0		200	W
η	Efficiency	50% of max I_O , $V_I = 110$ V		88.5		%
		max I_O , $V_I = 110$ V		90		
P_d	Power Dissipation	max I_O		15	28	W
P_{ii}	Input idling power	$I_O = 0$ A, $V_I = 110$ V		0.3		W
P_{RC}	Input standby power	$V_I = 110$ V (turned off with RC)		0.1		W
f_s	Switching frequency	0-100 % of max I_O	170	200	230	kHz

V_{O_i}	Output voltage initial setting and accuracy	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = 15.33$ A	14.85	15	15.15	V
V_O	Output adjust range	See operating information	13.5	15	16.5	V
	Output voltage tolerance band	0-100% of max I_O	14.55		15.45	V
	Idling voltage	$I_O = 0$ A	14.55		15.45	V
	Line regulation	max I_O		30	75	mV
	Load regulation	$V_I = 110$ V, 0-100% of max I_O		60	150	mV
V_{tr}	Load transient voltage deviation	$V_I = 110$ V, Load step 50-75-50% of max I_O , $di/dt = 100$ mA/ μs		± 275	± 500	mV
t_{tr}	Load transient recovery time			250	500	μs
t_r	Ramp-up time (from 10-90% of V_{O_i})	100% of max I_O		5	10	ms
t_s	Start-up time (from V_I connection to 90% of V_{O_i})			8	15	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{O_i})	max I_O		2	5	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		13.33	A
I_{lim}	Current limit threshold	$V_I = 110$ V, $T_{P1} < \text{max } T_{P1}$	16	20	26.66	A
I_{sc}	Short circuit current	$T_{P1} = 25^{\circ}\text{C}$, see Note 1		1.95		A
C_{out}	Recommended Capacitive Load	$T_{P1} = 25^{\circ}\text{C}$	0		3000	μF
V_{Oac}	Output ripple & noise	See ripple & noise section, $V_I = 110$ V, max I_O , see Note 2		150	300	mVp-p
OVP	Over voltage protection	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, 0-100% of max I_O		18		V

Note 1: Output Current (RMS): hiccup mode

Note 2: Measured with 0.1 μF ceramic Cap. and 10 μF tantalum(or E-Cap.) cross to output.

PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

28701-BMR 717 Rev. A

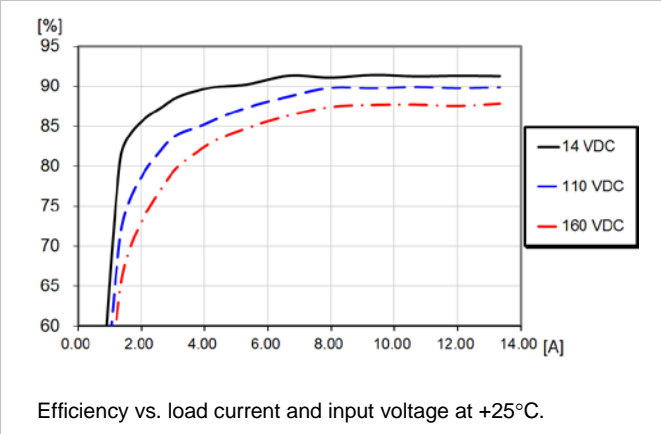
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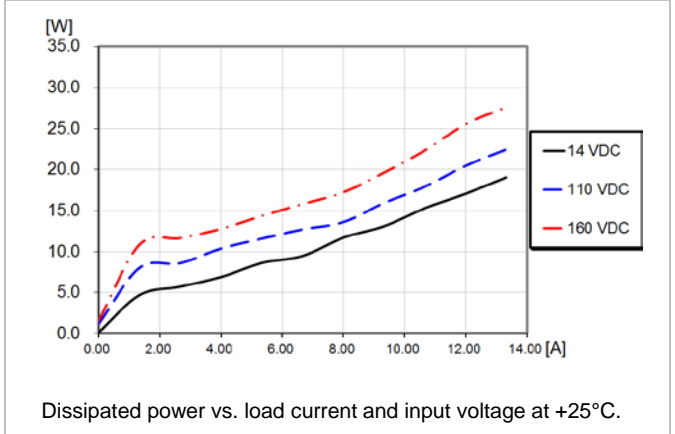
Typical Characteristics
15 V, 13.33 A / 200 W

PKJ7215WPI(P)

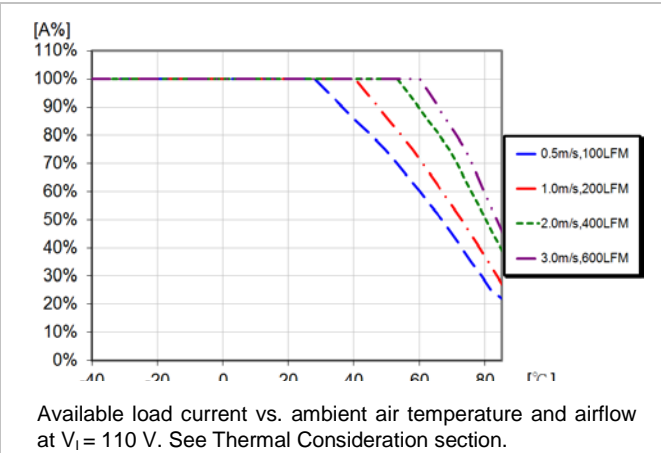
Efficiency



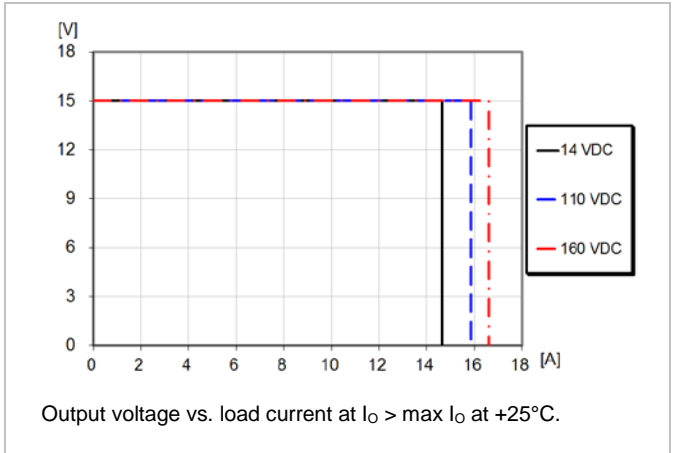
Power Dissipation



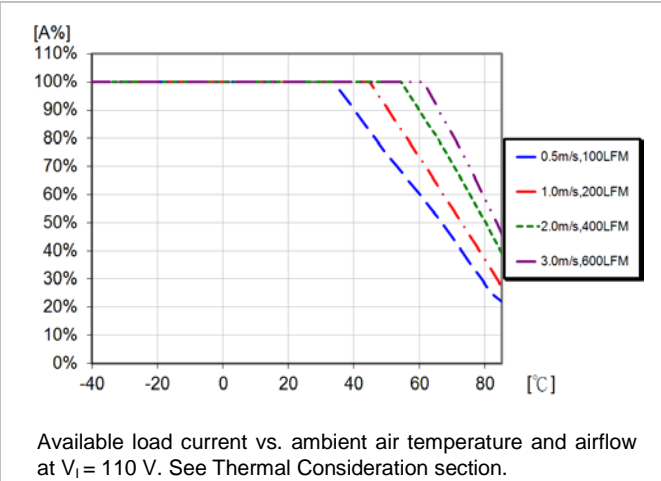
Output Current Derating



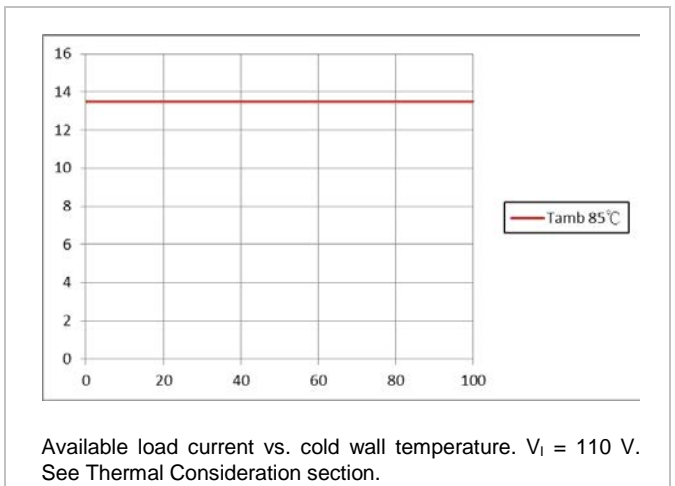
Current Limit Characteristics



Output Current Derating with 0.5" heatsink



Output Current Derating - Cold wall sealed box



PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

28701-BMR 717 Rev. A

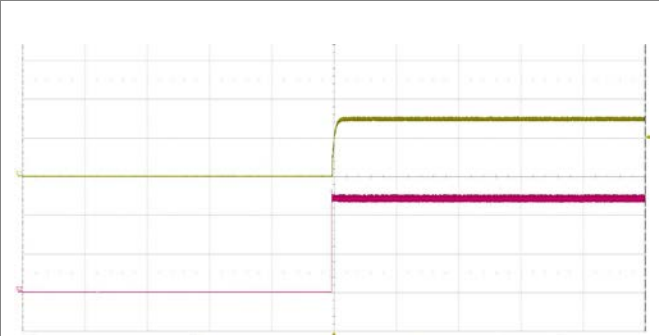
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Typical Characteristics
15 V, 13.33 A / 200 W

PKJ7215WPI(P)

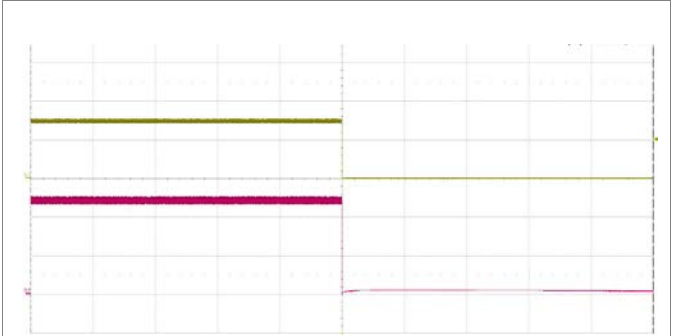
Start-up



Start-up enabled by connecting V_i at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 13.33\text{ A}$ resistive load.

Top trace: output voltage (10 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

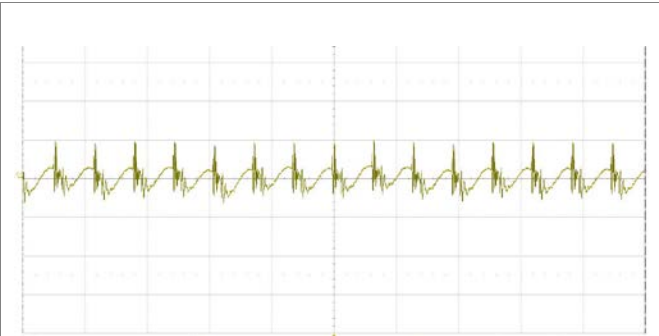
Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 13.33\text{ A}$ resistive load.

Top trace: output voltage (10 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

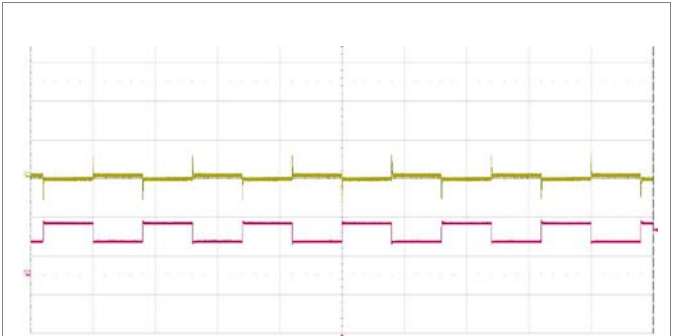
Output Ripple & Noise



Output voltage ripple at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 13.33\text{ A}$ resistive load.

Trace: output voltage (20 mV/div.).
 Time scale: (5 μs /div.).
 20 MHz bandwidth

Output Load Transient Response



Output voltage response to load current step-change (6.66-10.0-6.66 A) at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$.

Top trace: output voltage (500 mV/div.).
 Bottom trace: load current (5 A/div.).
 Time scale: (2 ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

Output Voltage = 15 V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{\text{ADJ_UP}} = \left(\frac{9.186}{\Delta} - 68 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{\text{ADJ_DOWN}} = \left(\frac{9.5738}{\Delta} - 86.760 \right) \text{ k}\Omega$$

Example:

To trim up the 15 V model by 8% to 16.2 V the required external resistor is:

$$R_{\text{ADJ_UP}} = \left(\frac{9.186}{0.08} - 68 \right) = 46.83 \text{ k}\Omega$$

Example:

To trim down the 15 V model by 7% to 13.95 V the required external resistor is:

$$R_{\text{ADJ_DOWN}} = \left(\frac{9.5738}{0.07} - 86.76 \right) = 50 \text{ k}\Omega$$

Technical Specification

PKJ 7200W series DC-DC Converters
Input 14-160 V, Output up to 16.7 A / 200 W

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Electrical Specification
24 V, 8.33 A / 200 W

PKJ7216ZWPI(P)

$T_{P1} = -40$ to 115°C , $V_I = 14$ to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = \text{max } I_O$, unless otherwise specified under Conditions.

Additional $C_{in} = 100$ μF , $C_{out} = 0.1$ μF ceramic Cap. + 10 μF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		14		160	V
V_{loff}	Turn-off input voltage	Decreasing input voltage		12		V
V_{lon}	Turn-on input voltage	Increasing input voltage		13		V
C_i	Internal input capacitance			10		μF
P_O	Output power		0		200	W
η	Efficiency	50% of max I_O , $V_I = 110$ V		88.5		%
		max I_O , $V_I = 110$ V		90		
P_d	Power Dissipation	max I_O		13	28	W
P_{ii}	Input idling power	$I_O = 0$ A, $V_I = 110$ V		2		W
P_{RC}	Input standby power	$V_I = 110$ V (turned off with RC)		0.2		W
f_s	Switching frequency	0-100 % of max I_O	170	200	230	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = 8.33$ A	23.76	24	24.24	V
V_O	Output adjust range	See operating information	21.6	24	26.4	V
	Output voltage tolerance band	0-100% of max I_O	23.28		24.72	V
	Idling voltage	$I_O = 0$ A	23.28		24.72	V
	Line regulation	max I_O		20	48	mV
	Load regulation	$V_I = 110$ V, 0-100% of max I_O		20	240	mV
V_{tr}	Load transient voltage deviation	$V_I = 110$ V, Load step 50-75-50% of max I_O , $di/dt = 100$ mA/ μs		± 275	± 500	mV
t_{tr}	Load transient recovery time			250	500	μs
t_r	Ramp-up time (from 10-90% of V_{Oi})	100% of max I_O		5	10	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8	15	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		2	5	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		8.33	A
I_{lim}	Current limit threshold	$V_I = 110$ V, $T_{P1} < \text{max } T_{P1}$	10	12	16	A
I_{sc}	Short circuit current	$T_{P1} = 25^{\circ}\text{C}$, see Note 1		1.95		A
C_{out}	Recommended Capacitive Load	$T_{P1} = 25^{\circ}\text{C}$	0		3000	μF
V_{Oac}	Output ripple & noise	See ripple & noise section, $V_I = 110$ V, max I_O , see Note 2		27	50	mVp-p
OVP	Over voltage protection	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, 0-100% of max I_O		30		V

Note 1: Output Current (RMS): hiccup mode

Note 2: Measured with 0.1 μF ceramic Cap. and 10 μF tantalum (or E-Cap.) cross to output.

PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

28701-BMR 717 Rev. A

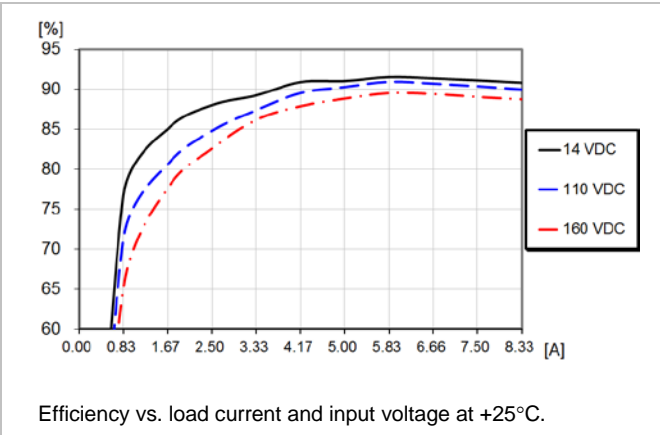
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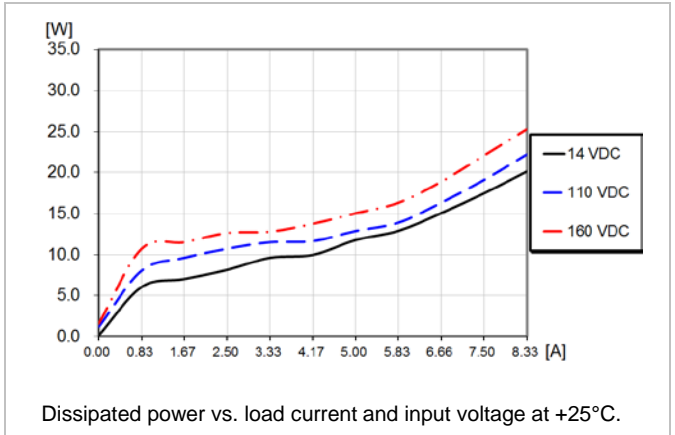
Typical Characteristics
24 V, 8.33 A / 200 W

PKJ7216ZWPI(P)

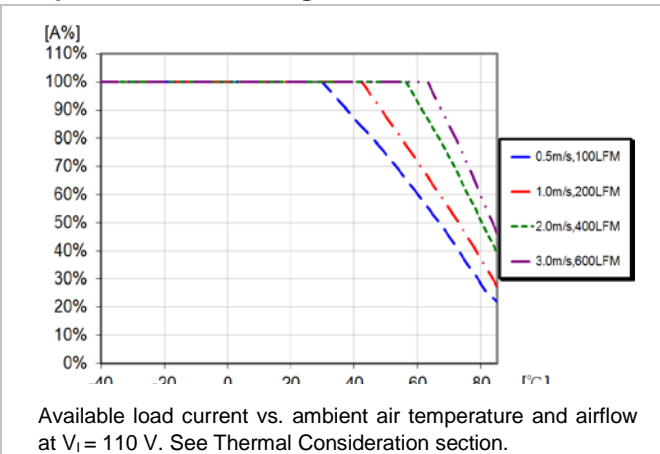
Efficiency



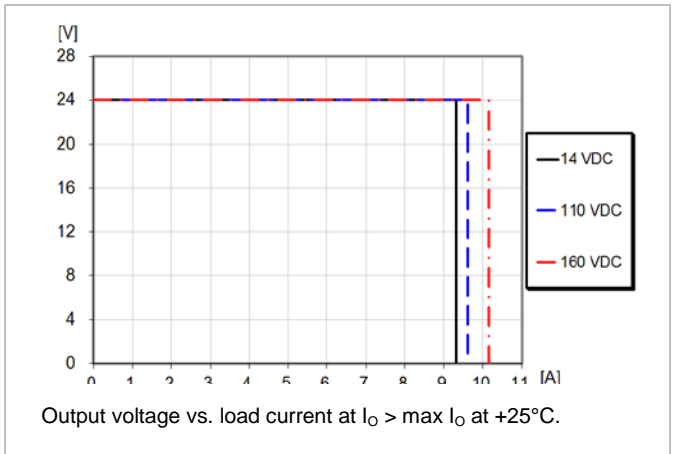
Power Dissipation



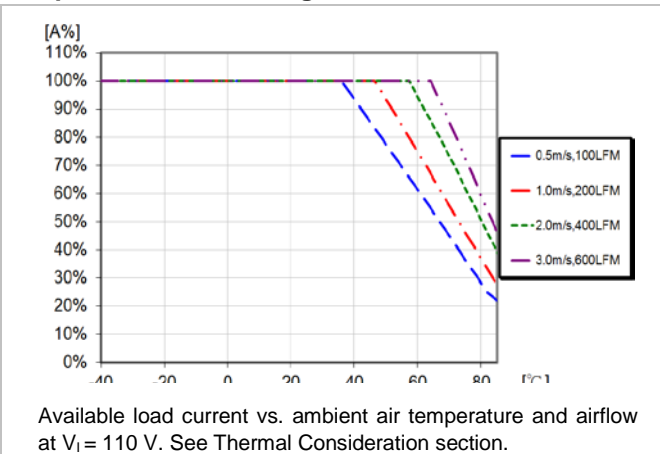
Output Current Derating



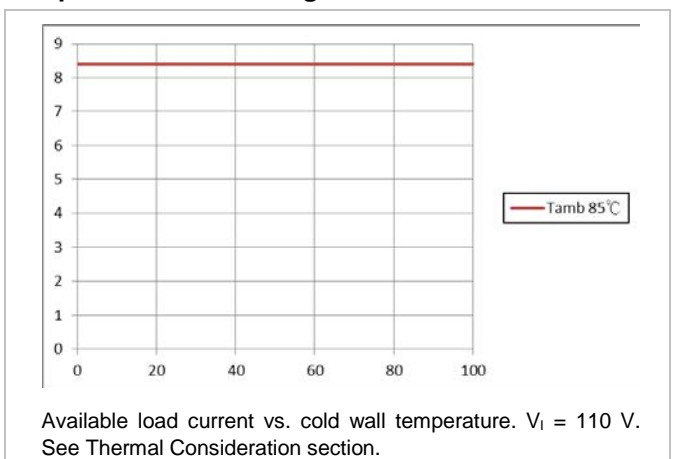
Current Limit Characteristics



Output Current Derating with 0.5" heatsink



Output Current Derating - Cold wall sealed box



PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

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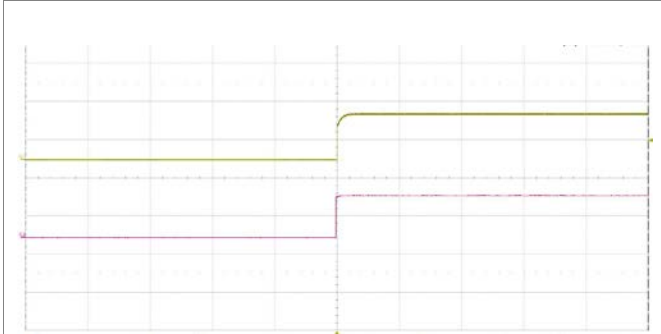
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Typical Characteristics
24 V, 8.33 A / 200 W

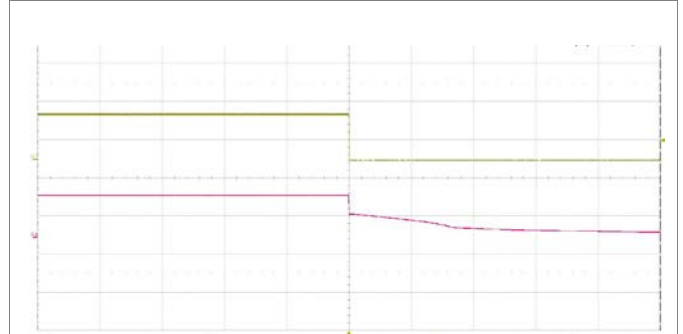
PKJ7216ZWPI(P)

Start-up



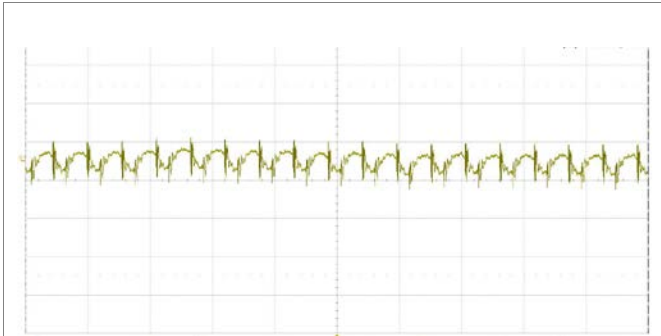
Start-up enabled by connecting V_I at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$,
 $I_O = 8.33\text{ A}$ resistive load.
 Top trace: output voltage (20 V/div.).
 Bottom trace: input voltage (100 V/div.).
 Time scale: (200 ms/div.).

Shut-down



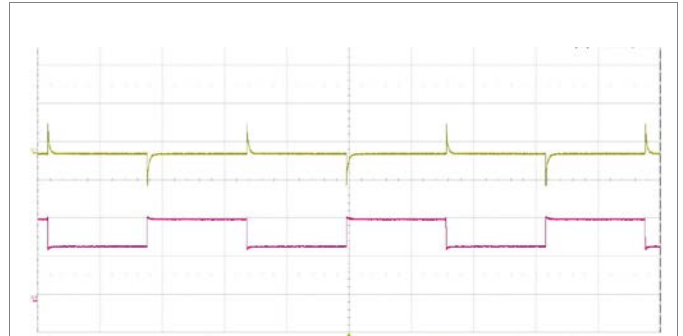
Shut-down enabled by disconnecting V_I at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$,
 $I_O = 8.33\text{ A}$ resistive load.
 Top trace: output voltage (20 V/div.).
 Bottom trace: input voltage (100 V/div.).
 Time scale: (200 ms/div.).

Output Ripple & Noise



Output voltage ripple at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$,
 $I_O = 8.33\text{ A}$ resistive load.
 Trace: output voltage (20 mV/div.).
 Time scale: (5 μs/div.).
 20 MHz bandwidth

Output Load Transient Response



Output voltage response to load current step-change (4.16-6.25-4.16 A) at:
 $T_{P1} = +25^\circ\text{C}$, $V_I = 110\text{ V}$.
 Top trace: output voltage (500 mV/div.).
 Bottom trace: load current (2 A/div.).
 Time scale: (2 ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

Output Voltage = 24 V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{\text{ADJ_UP}} = \left(\frac{14.5808}{\Delta} - 91 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{\text{ADJ_DOWN}} = \left(\frac{14.0615}{\Delta} - 119.642 \right) \text{ k}\Omega$$

Example:

To trim up the 24 V model by 8% to 25.92 V the required external resistor is:

$$R_{\text{ADJ_UP}} = \left(\frac{14.5808}{0.08} - 91 \right) = 91.26 \text{ k}\Omega$$

Example:

To trim down the 24 V model by 7% to 22.32 V the required external resistor is:

$$R_{\text{ADJ_DOWN}} = \left(\frac{14.0615}{0.07} - 119.642 \right) = 81.24 \text{ k}\Omega$$

Technical Specification

PKJ 7200W series DC-DC Converters
Input 14-160 V, Output up to 16.7 A / 200 W

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Electrical Specification
48 V, 4.16 A / 200 W

PKJ7216JWPI(P)

$T_{P1} = -40$ to 115°C , $V_I = 14$ to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = \text{max } I_O$, unless otherwise specified under Conditions.

Additional $C_{in} = 100$ μF , $C_{out} = 0.1$ μF ceramic Cap. + 10 μF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		14		160	V
V_{loff}	Turn-off input voltage	Decreasing input voltage		12		V
V_{lon}	Turn-on input voltage	Increasing input voltage		13		V
C_i	Internal input capacitance			10		μF
P_O	Output power		0		200	W
η	Efficiency	50% of max I_O , $V_I = 110$ V		88.5		%
		max I_O , $V_I = 110$ V		90		
P_d	Power Dissipation	max I_O		14	27	W
P_{ii}	Input idling power	$I_O = 0$ A, $V_I = 110$ V		0.4		W
P_{RC}	Input standby power	$V_I = 110$ V (turned off with RC)		0.2		W
f_s	Switching frequency	0-100 % of max I_O	170	200	230	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = 4.16$ A	47.52	48	48.48	V
V_O	Output adjust range	See operating information	43.2	48	52.8	V
	Output voltage tolerance band	0-100% of max I_O	46.56		49.44	V
	Idling voltage	$I_O = 0$ A	46.56		49.44	V
	Line regulation	max I_O		100	240	mV
	Load regulation	$V_I = 110$ V, 0-100% of max I_O		200	480	mV
V_{tr}	Load transient voltage deviation	$V_I = 110$ V, Load step 50-75-50% of max I_O , $di/dt = 100$ mA/ μs		± 275	± 500	mV
t_{tr}	Load transient recovery time			250	500	μs
t_r	Ramp-up time (from 10-90% of V_{Oi})	100% of max I_O		5	10	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8	15	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		2	5	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		4.16	A
I_{lim}	Current limit threshold	$V_I = 110$ V, $T_{P1} < \text{max } T_{P1}$	5	6.24	8.32	A
I_{sc}	Short circuit current	$T_{P1} = 25^{\circ}\text{C}$, see Note 1		1.95		A
C_{out}	Recommended Capacitive Load	$T_{P1} = 25^{\circ}\text{C}$	0		1000	μF
V_{Oac}	Output ripple & noise	See ripple & noise section, $V_I = 110$ V, max I_O , see Note 2		150	300	mVp-p
OVP	Over voltage protection	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, 0-100% of max I_O		58		V

Note 1: Output Current (RMS): hiccup mode

Note 2: Measured with 0.1 μF ceramic Cap. and 10 μF tantalum (or E-Cap.) cross to output.

PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

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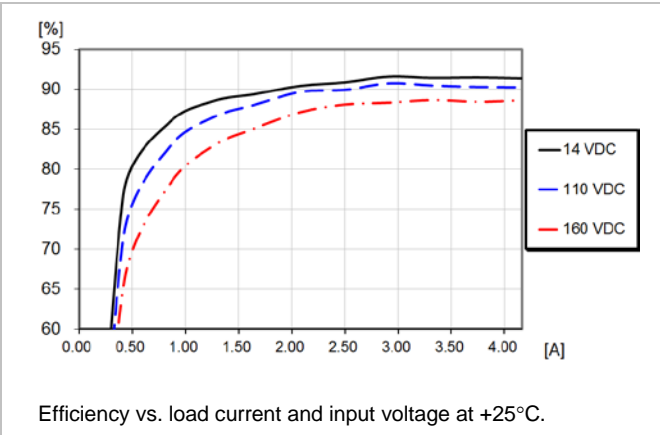
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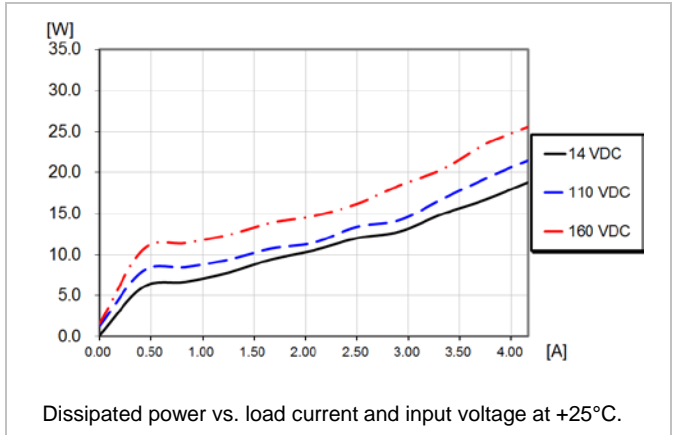
Typical Characteristics
48 V, 4.16 A / 200 W

PKJ7216JWPI(P)

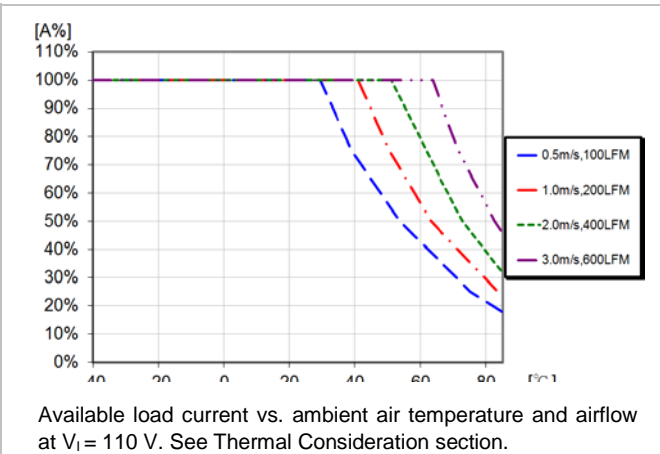
Efficiency



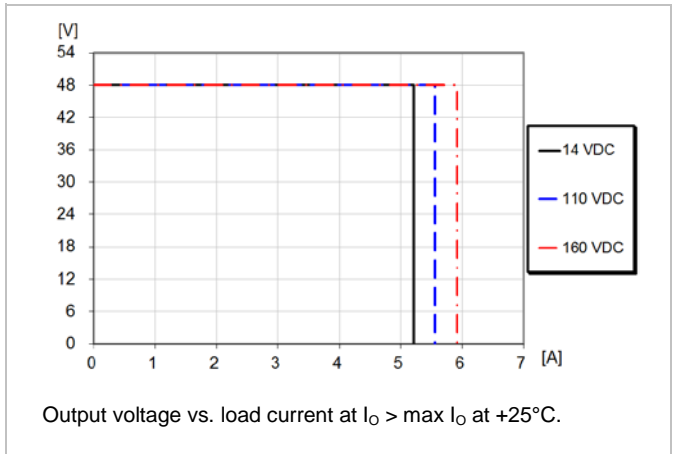
Power Dissipation



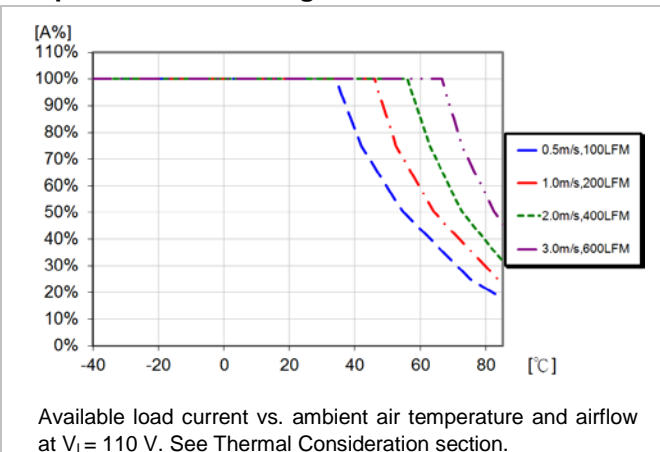
Output Current Derating



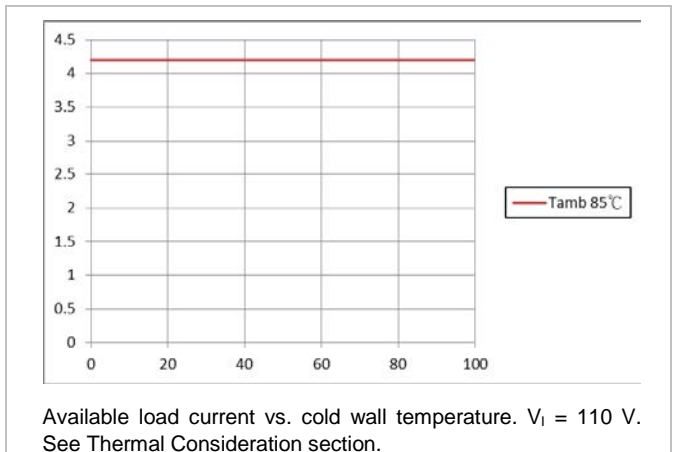
Current Limit Characteristics



Output Current Derating with 0.5" heatsink



Output Current Derating - Cold wall sealed box



PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

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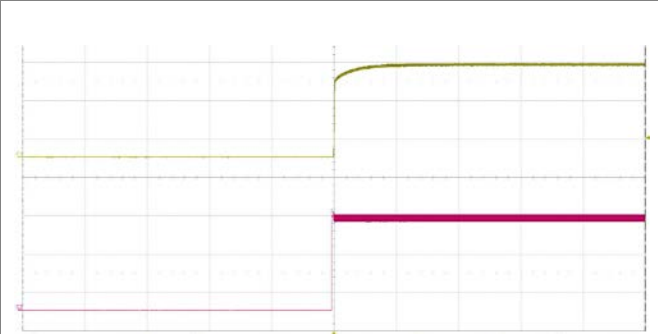
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Typical Characteristics
48 V, 4.16 A / 200 W

PKJ7216JWPI(P)

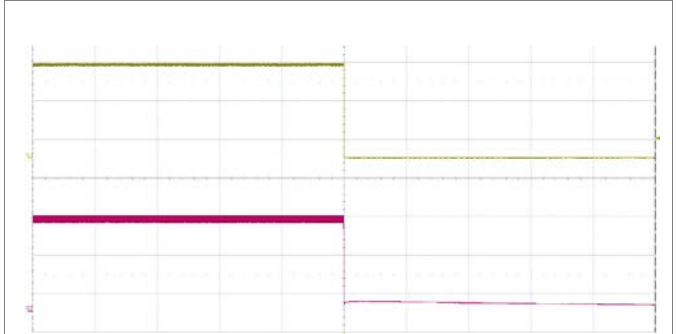
Start-up



Start-up enabled by connecting V_i at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 4.16\text{ A}$ resistive load.

Top trace: output voltage (20 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

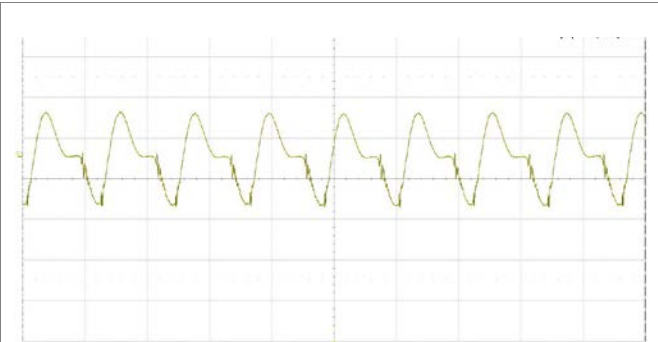
Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 4.16\text{ A}$ resistive load.

Top trace: output voltage (20 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

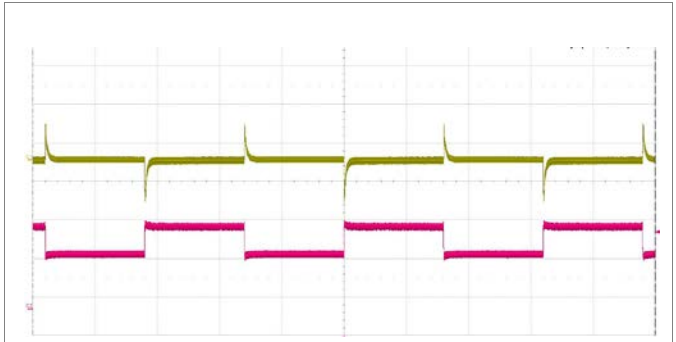
Output Ripple & Noise



Output voltage ripple at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 4.16\text{ A}$ resistive load.

Trace: output voltage (20 mV/div.).
 Time scale: (5 μs/div.).
 20 MHz bandwidth

Output Load Transient Response



Output voltage response to load current step-change (2.08-3.12-2.08 A) at:
 $T_{P1} = +25^\circ\text{C}$, $V_i = 110\text{ V}$.

Top trace: output voltage (500 mV/div.).
 Bottom trace: load current (2 A/div.).
 Time scale: (2 ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

Output Voltage = 48 V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{\text{ADJ_UP}} = \left(\frac{29.4091}{\Delta} - 180 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{\text{ADJ_DOWN}} = \left(\frac{29.2283}{\Delta} - 238.637 \right) \text{ k}\Omega$$

Example:

To trim up the 48 V model by 8% to 51.84 V the required external resistor is:

$$R_{\text{ADJ_UP}} = \left(\frac{29.4091}{0.08} - 180 \right) = 187.61 \text{ k}\Omega$$

Example:

To trim down the 48 V model by 7% to 44.64 V the required external resistor is:

$$R_{\text{ADJ_DOWN}} = \left(\frac{29.2283}{0.07} - 238.637 \right) = 178.91 \text{ k}\Omega$$

Technical Specification

PKJ 7200W series DC-DC Converters
Input 14-160 V, Output up to 16.7 A / 200 W

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Electrical Specification
54 V, 3.7 A / 200 W

PKJ7216HWPI(P)

$T_{P1} = -40$ to 115°C , $V_I = 14$ to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, I_O max, unless otherwise specified under Conditions.

Additional $C_{in} = 100$ μF , $C_{out} = 0.1$ μF ceramic Cap. + 10 μF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		14		160	V
V_{loff}	Turn-off input voltage	Decreasing input voltage		12		V
V_{lon}	Turn-on input voltage	Increasing input voltage		13		V
C_i	Internal input capacitance			10		μF
P_O	Output power		0		200	W
η	Efficiency	50% of max I_O , $V_I = 110$ V		88.5		%
		max I_O , $V_I = 110$ V		90		
P_d	Power Dissipation	max I_O		13	26	W
P_{ii}	Input idling power	$I_O = 0$ A, $V_I = 110$ V		0.3		W
P_{RC}	Input standby power	$V_I = 110$ V (turned off with RC)		0.1		W
f_s	Switching frequency	0-100 % of max I_O	170	200	230	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, $I_O = 3.7$ A	53.46	54	54.54	V
V_O	Output adjust range	See operating information	48.6	54	59.4	V
	Output voltage tolerance band	0-100% of max I_O	52.38		55.62	V
	Idling voltage	$I_O = 0$ A	52.38		55.62	V
	Line regulation	max I_O		100	270	mV
	Load regulation	$V_I = 110$ V, 0-100% of max I_O		200	540	mV
V_{tr}	Load transient voltage deviation	$V_I = 110$ V, Load step 50-75-50% of max I_O , $di/dt = 100$ mA/ μs		± 275	± 500	mV
t_{tr}	Load transient recovery time			250	500	μs
t_r	Ramp-up time (from 10-90% of V_{Oi})	100% of max I_O		5	10	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8	15	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		2	5	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		3.7	A
I_{lim}	Current limit threshold	$V_I = 110$ V, $T_{P1} < \text{max } T_{P1}$	4.44	5.55	7.4	A
I_{sc}	Short circuit current	$T_{P1} = 25^{\circ}\text{C}$, see Note 1		1.95		A
C_{out}	Recommended Capacitive Load	$T_{P1} = 25^{\circ}\text{C}$	0		1000	μF
V_{Oac}	Output ripple & noise	See ripple & noise section, $V_I = 110$ V, max I_O , see Note 2		150	300	mVp-p
OVP	Over voltage protection	$T_{P1} = +25^{\circ}\text{C}$, $V_I = 110$ V, 0-100% of max I_O		62		V

Note 1: Output Current (RMS): hiccup mode

Note 2: Measured with 0.1 μF ceramic Cap. and 10 μF tantalum (or E-Cap.) cross to output.

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 Input 14-160 V, Output up to 16.7 A / 200 W

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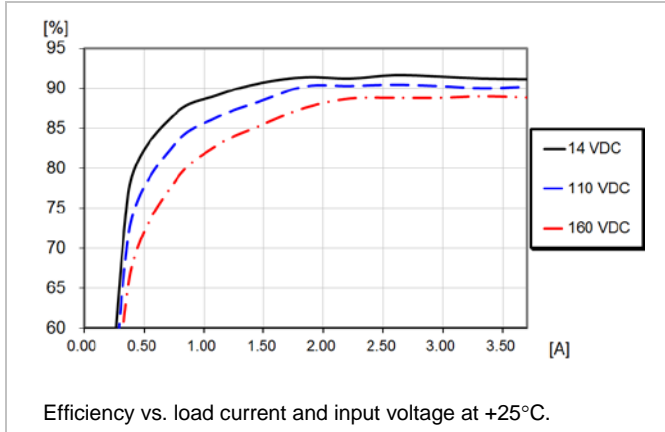
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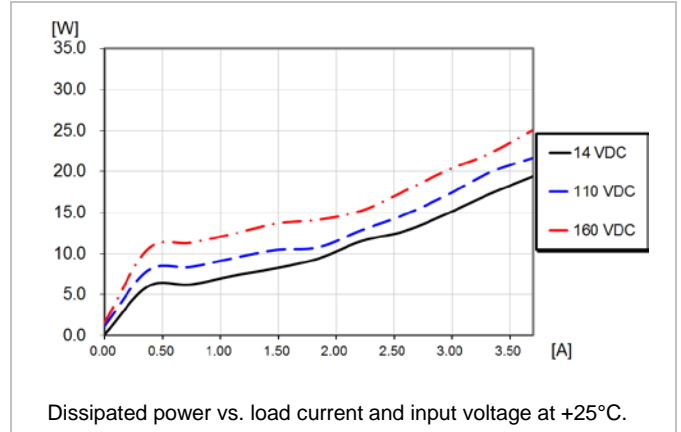
Typical Characteristics
54 V, 3.7 A / 200 W

PKJ7216HWPI(P)

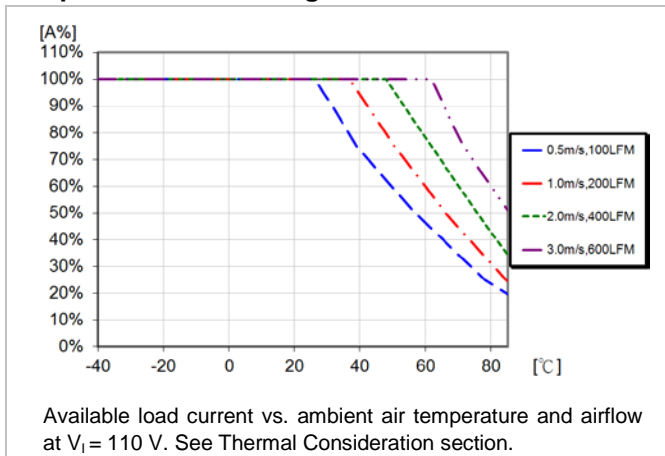
Efficiency



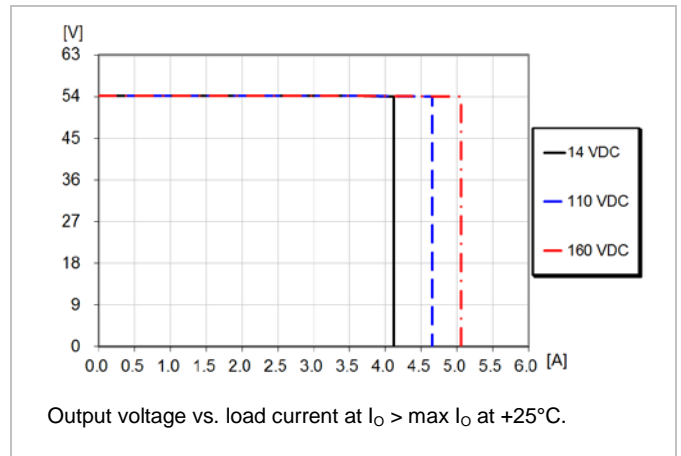
Power Dissipation



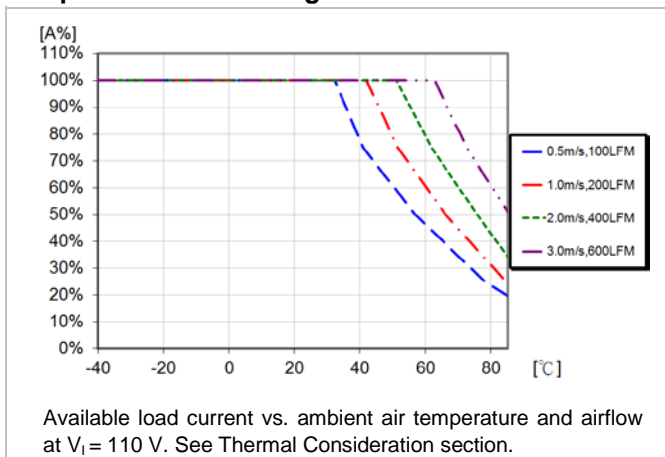
Output Current Derating



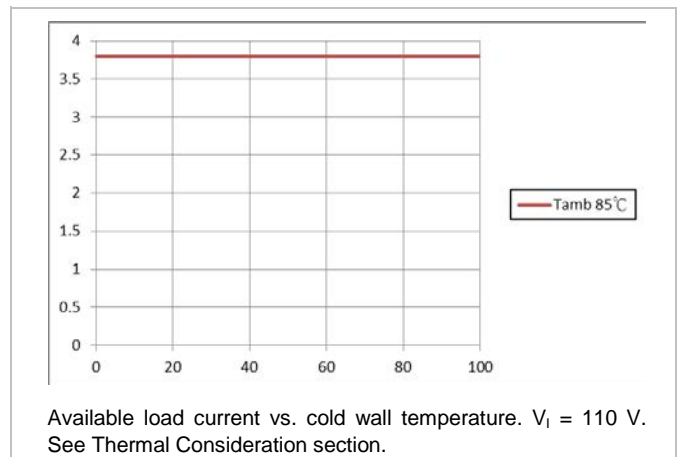
Current Limit Characteristics



Output Current Derating with 0.5" heatsink



Output Current Derating - Cold wall sealed box



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 Input 14-160 V, Output up to 16.7 A / 200 W

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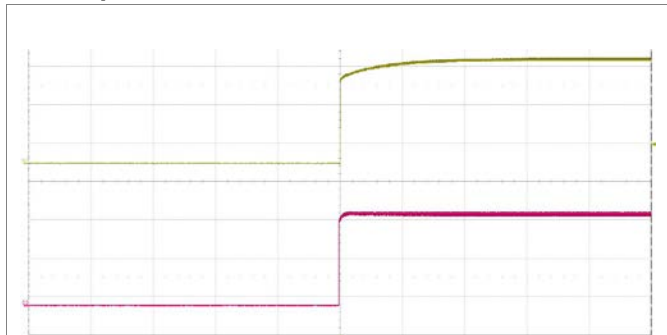
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Typical Characteristics
54 V, 3.7 A / 200 W

PKJ7216HWPI(P)

Start-up



Start-up enabled by connecting V_i at:
 $T_{P1} = +25^{\circ}\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 3.7\text{ A}$ resistive load.

Top trace: output voltage (20 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

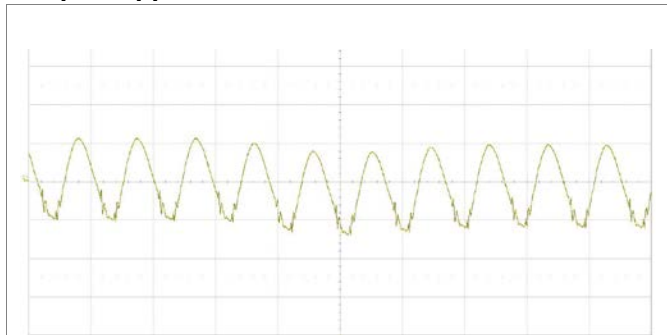
Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{P1} = +25^{\circ}\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 3.7\text{ A}$ resistive load.

Top trace: output voltage (20 V/div.).
 Bottom trace: input voltage (50 V/div.).
 Time scale: (200 ms/div.).

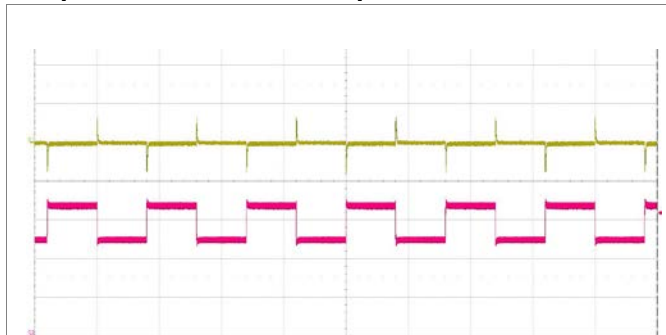
Output Ripple & Noise



Output voltage ripple at:
 $T_{P1} = +25^{\circ}\text{C}$, $V_i = 110\text{ V}$,
 $I_o = 3.7\text{ A}$ resistive load.

Trace: output voltage (20 mV/div.).
 Time scale: (5 μs /div.).
 20 MHz bandwidth

Output Load Transient Response



Output voltage response to load current step-change (1.85-2.77-1.85 A) at:
 $T_{P1} = +25^{\circ}\text{C}$, $V_i = 110\text{ V}$.

Top trace: output voltage (500 mV/div.).
 Bottom trace: load current (1 A/div.).
 Time scale: (2 ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

Output Voltage = 54 V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{\text{ADJ_UP}} = \left(\frac{32.667}{\Delta} - 270 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{\text{ADJ_DOWN}} = \left(\frac{40.5244}{\Delta} - 343.191 \right) \text{ k}\Omega$$

Example:

To trim up the 54 V model by 8% to 58.32 V the required external resistor is:

$$R_{\text{ADJ_UP}} = \left(\frac{32.667}{0.08} - 270 \right) = 138.34 \text{ k}\Omega$$

Example:

To trim down the 54 V model by 7% to 50.22 V the required external resistor is:

$$R_{\text{ADJ_DOWN}} = \left(\frac{40.5244}{0.07} - 343.191 \right) = 235.723 \text{ k}\Omega$$

Technical Specification

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 Input 14-160 V, Output up to 16.7 A / 200 W

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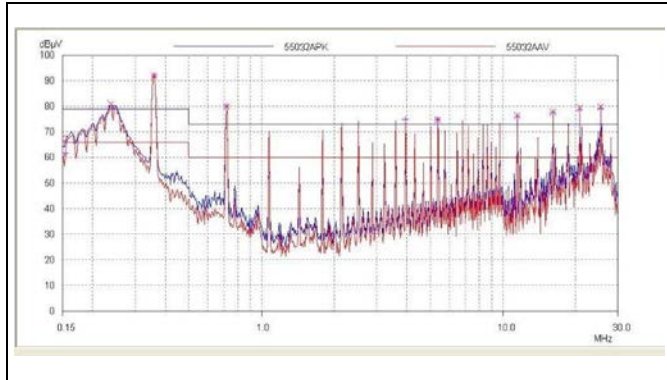
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EMC Specification

Conducted EMI measured according to EN55032, CISPR 32 and FCC part 15J (see test set-up). See Design Note 009 for further information. The fundamental switching frequency is 200 kHz for PKJ7216ZWPIP (24 V / 200 W) at $V_i = 110$ V and I_o .

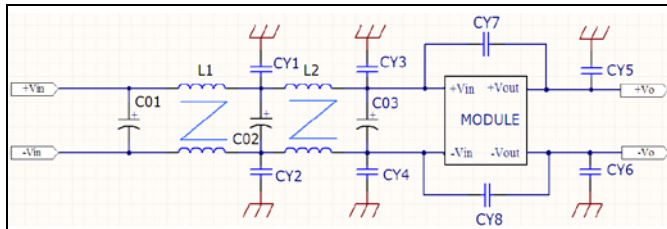
Conducted EMI Input terminal value (typ)



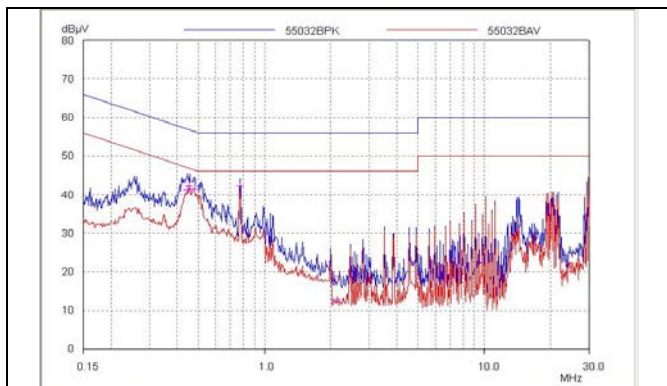
EMI without filter

Optional external filter for class B

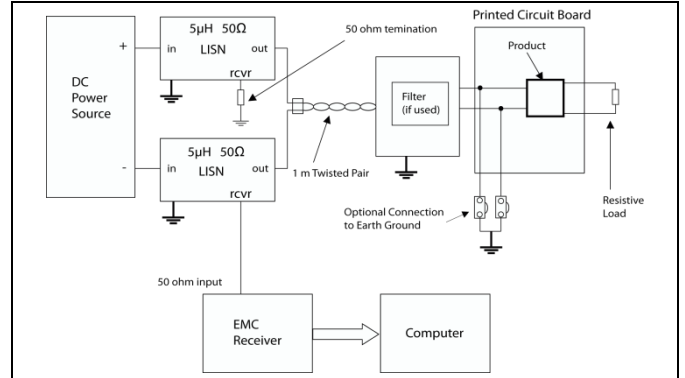
Suggested external input filter in order to meet class B in EN 55032, CISPR 32 and FCC part 15J.



Filter components:
 C01, C02, C03=10µF (EE-CAP)
 CY1, CY2, CY3, CY4=3.3nF, CY7, CY8=2.2nF(Y-CAP)
 L1 = 1.6mH, L2 = 10mH (CM CHOKE)
 CY5 & CY6 : NC



EMI with filter



Test set-up

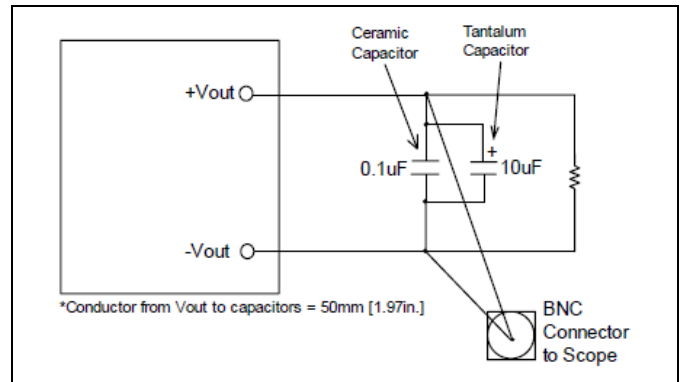
Layout recommendations

The radiated EMI performance of the product will depend on the PWB layout and ground layer design. It is also important to consider the stand-off of the product. If a ground layer is used, it should be connected to the output of the product and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PWB and improve the high frequency EMC performance.

Output ripple and noise

Output ripple and noise measured according to figure below. See Design Note 022 for detailed information.



Output ripple and noise test setup

Technical Specification

PKJ 7200W series DC-DC Converters
Input 14-160 V, Output up to 16.7 A / 200 W

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Operating Information

Input Voltage

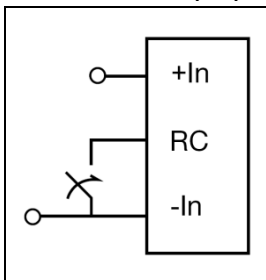
The input voltage range 14 to 160 Vdc, at input voltages exceeding 160 V, the power loss will be higher than at normal input voltage and T_{P1} must be limited to absolute max +115°C. The absolute maximum continuous input voltage is 160 Vdc.

Short duration transient disturbances can occur on the DC distribution and input of the product when a short circuit fault occurs on the equipment side of a protective device (fuse or circuit breaker). The voltage level, duration and energy of the disturbance are dependant on the particular DC distribution network characteristics and can be sufficient to damage the product unless measures are taken to suppress or absorb this energy. The transient voltage can be limited by capacitors and other energy absorbing devices like zener diodes connected across the positive and negative input conductors at a number of strategic points in the distribution network. The end-user must secure that the transient voltage will not exceed the value stated in the Absolute maximum ratings. ETSI TR 100 283 examines the parameters of DC distribution networks and provides guidelines for controlling the transient and reduce its harmful effect.

Turn-off Input Voltage

The products monitor the input voltage and will turn on and turn off at predetermined levels. The minimum hysteresis between turn on and turn off input voltage is 1.7 V.

Remote Control (RC)



The products are fitted with a remote control function referenced to the primary negative input connection (-In), with negative and positive logic options available. The RC function allows the product to be turned on/off by an external device like a semiconductor or mechanical switch. The RC pin has an internal pull up resistor to +In.

The external device must provide a minimum required sink current to guarantee a voltage not higher than maximum voltage on the RC pin (see Electrical characteristics table). When the RC pin is left open, the voltage generated on the RC pin is 3 - 5 V.

The standard product is provided with “negative logic” RC and will be on until the RC pin is connected to the -In. To turn off the product the RC pin should be left open, or connected to a voltage higher than 2.5 V referenced to -In. In situations where it is desired to have the product to power up automatically without the need for control signals or a switch, the RC pin can be wired directly to -In.

The second option is “positive logic” remote control, which can

be ordered by adding the suffix “P” to the end of the part number. When the RC pin is left open, the product starts up automatically when the input voltage is applied. Turn off is achieved by connecting the RC pin to the -In. The product will restart automatically when this connection is opened.

See Design Note 021 for detailed information.

Input and Output Impedance

The impedance of both the input source and the load will interact with the impedance of the product. It is important that the input source has low characteristic impedance. The products are designed for stable operation without external capacitors connected to the input or output. The performance in some applications can be enhanced by addition of external capacitance as described under External Decoupling Capacitors.

If the input voltage source contains significant inductance, the addition of a 330 μ F capacitor across the input of the product will ensure stable operation. The capacitor is not required when powering the product from an input source with an inductance below 10 μ H. The minimum required capacitance value depends on the output power and the input voltage. The higher output power the higher input capacitance is needed. Approximately doubled capacitance value is required for a 24 V input voltage source compared to a 48 V input voltage source.

External Decoupling Capacitors

When powering loads with significant dynamic current requirements, the voltage regulation at the point of load can be improved by addition of decoupling capacitors at the load. The most effective technique is to locate low ESR ceramic and electrolytic capacitors as close to the load as possible, using several parallel capacitors to lower the effective ESR. The ceramic capacitors will handle high-frequency dynamic load changes while the electrolytic capacitors are used to handle low frequency dynamic load changes. It is equally important to use low resistance and low inductance PWB layouts and cabling.

External decoupling capacitors will become part of the product’s control loop. The control loop is optimized for a wide range of external capacitance and the maximum recommended value that could be used without any additional analysis is found in the Electrical specification. The ESR of the capacitors is a very important parameter. Stable operation is guaranteed with a verified ESR value of >5 m Ω across the output connections. For further information please contact your local Flex Power Modules representative.

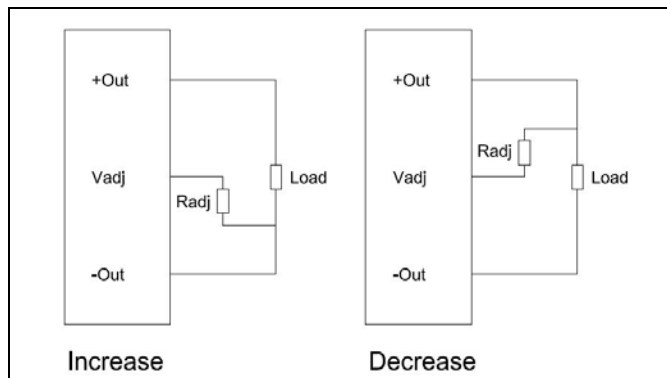
Technical Specification

PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

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Output Voltage Adjust (V_{adj})

The products have an Output Voltage Adjust pin (V_{adj}). This pin can be used to adjust the output voltage above or below Output voltage initial setting. When increasing the output voltage, the voltage at the output pins (including any remote sense compensation) must be kept below the threshold of the over voltage protection, (OVP) to prevent the product from shutting down. At increased output voltages the maximum power rating of the product remains the same, and the max output current must be decreased correspondingly. To increase the voltage the resistor should be connected between the V_{adj} pin and –Out pin. The resistor value of the Output voltage adjust function is according to information given under the Output section for the respective product. To decrease the output voltage, the resistor should be connected between the V_{adj} pin and +Out pin.



Over Temperature Protection (OTP)

The products are protected from thermal overload by an internal over temperature shutdown circuit. When T_{P1} as defined in thermal consideration section exceeds 115°C the product will shut down. The product will make continuous attempts to start up (non-latching mode) and resume normal operation automatically when the temperature has dropped >10°C below the temperature threshold.

Over Voltage Protection (OVP)

The products have output over voltage protection that will shut down the product in over voltage conditions. The product will make continuous attempts to start up (non-latching mode) and resume normal operation automatically after removal of the over voltage condition.

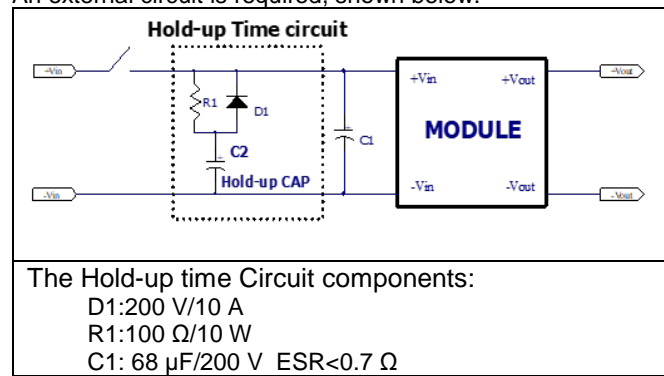
Over Current Protection (OCP)

The products include current limiting circuitry for protection at continuous overload. The output voltage will decrease towards zero for output currents in excess of max output current (max I_o). The product will resume normal operation after removal of the overload. The load distribution should be designed for the maximum output short circuit current specified.

The Hold-up time Circuit

Hold up time is defined as the duration of time that DC/DC converter output will remain active following a loss of input power. To meet power supply interruptions, voltage bleeding resistors and reverse polarity protection diodes should be included to provide a discharge path for the capacitors, avoiding injury from stored charge, and protecting the capacitors from damage at turn-on and turn-off.

An external circuit is required, shown below.



The **resistor (R1)** is used to limit in-rush current during hold-up capacitor charging. The **capacity of C2** decides the hold-up time during interruption of input power. When choosing a hold-up time capacitor(C2), it is recommended to use capacitors with low Equivalent Series Resistance (ESR) as well as high rated for high ripple current. (When higher voltage is involved, it will be likely necessary to use two or more capacitors in series and/or series-parallel configurations to achieve the required total capacitance)

The relationship is showed in below table.

MODEL	V _{in} / Capacitance	24V	36V	48V	72V	96V	110V
		in	in	in	in	in	in
PKJ7213W PKJ7217KW	10ms	2200 μF	1000 μF	750 μF	630 μF	510 μF	470 μF
	30ms	7000 μF	2800 μF	2000 μF	1800 μF	1500 μF	1200 μF
	50ms	12000 μF	5600 μF	4000 μF	3000 μF	2200 μF	2000 μF
PKJ7215W PKJ7216ZW PKJ7216JW PKJ7216HW	10ms	2200 μF	1000 μF	750 μF	630 μF	510 μF	470 μF
	30ms	7000 μF	2800 μF	2000 μF	1800 μF	1500 μF	1200 μF
	50ms	12500 μF	6000 μF	4300 μF	3300 μF	2500 μF	2200 μF

Technical Specification

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 Input 14-160 V, Output up to 16.7 A / 200 W

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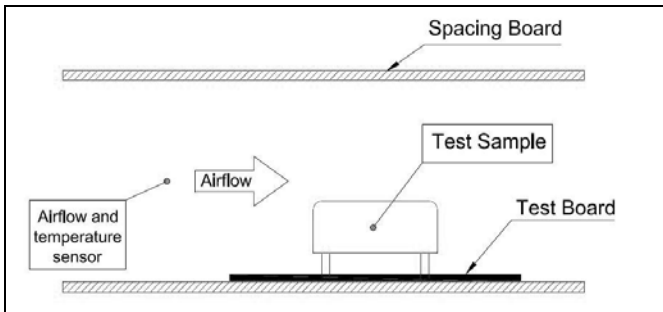
Thermal Consideration

General

The products are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.

For products mounted on a PWB without a heat sink attached, cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependent on the airflow across the product. Increased airflow enhances the cooling of the product. The Output Current Derating graph found in the Output section for each model provides the available output current vs. ambient air temperature and air velocity at $V_i = 110\text{ V}$.

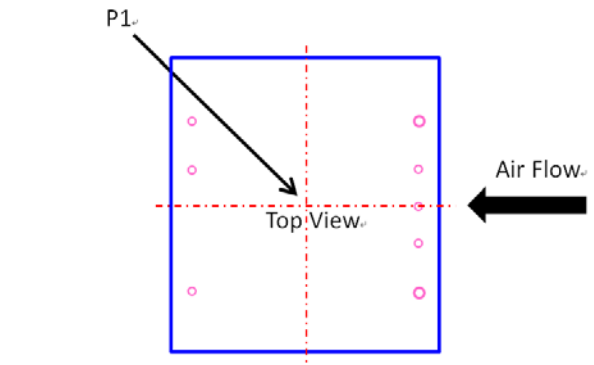
The product is tested on a 107 x 45 mm, 70 μm (2 oz), 1-layer test board in a wind box with 370 x 220 mm.



Definition of product operating temperature

The product operating temperatures is used to monitor the temperature of the product, and proper thermal conditions can be verified by measuring the temperature at positions P1. The temperature at this position (T_{P1}) should not exceed the maximum temperatures in the table below. Temperature above maximum T_{P1} , measured at the reference point P1 are not allowed and may cause permanent damage.

Position	Description	Max Temp.
P1	Reference point	$T_{P1}=115^\circ\text{ C}$



Reference point on PKJ721XW variants

Technical Specification

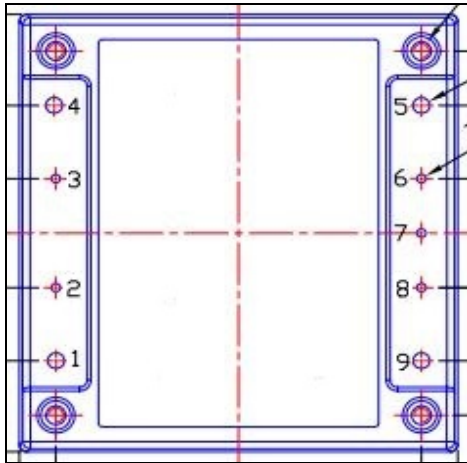
PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

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Connections



Pin	Designation	Function
1	+In	Positive input
2	RC	Remote control
3	Case	Baseplate
4	-In	Negative input
5	-Out	Negative output
6	-Sense	Negative remote sense
7	V _{adj}	Output voltage adjust
8	+Sense	Positive remote sense
9	+Out	Positive output

Technical Specification

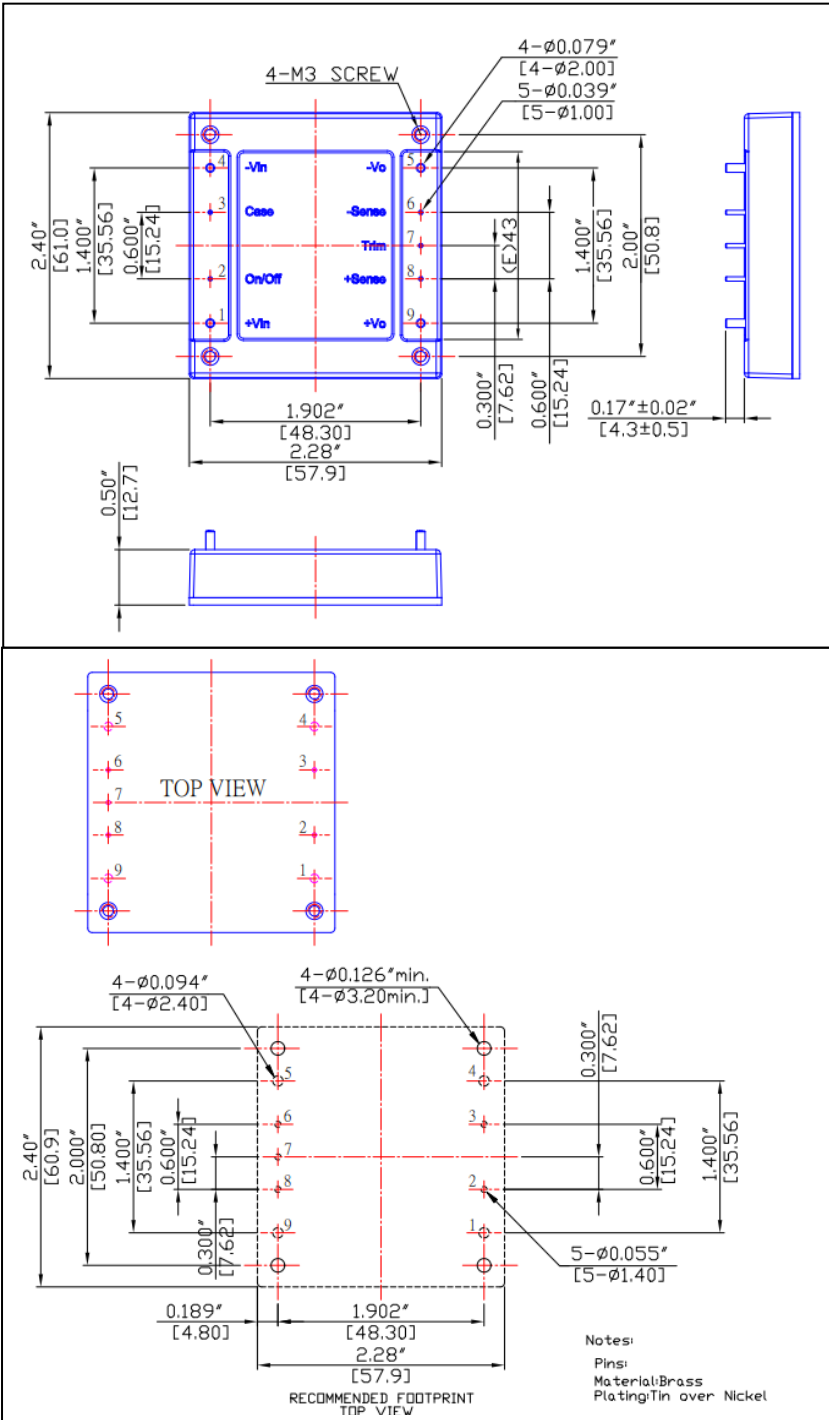
PJK 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

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Mechanical Information



Notes:

1. All dimensions in inches (mm)
2. Tolerance .xx = \pm 0.02"
xxx= \pm 0.010"
3. The screw locked torque:
MAX 4.0kgf-cm/0.39N-m
4. Weight: 100g typ.

Technical Specification

PKJ 7200W series DC-DC Converters
 Input 14-160 V, Output up to 16.7 A / 200 W

28701-BMR 717 Rev. A

July 2021

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Soldering Information - Hole Mounting

The hole mounted product is intended for plated through hole mounting by wave or manual soldering. The pin temperature is specified to maximum to 270°C for maximum 10 seconds.

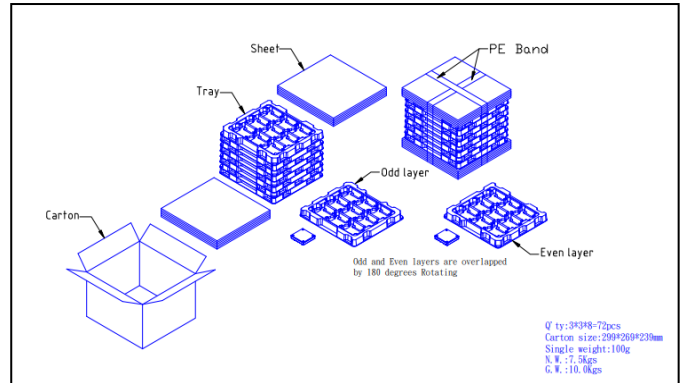
A maximum preheat rate of 4°C/s and maximum preheat temperature of 150°C is suggested. When soldering by hand, care should be taken to avoid direct contact between the hot soldering iron tip and the pins for more than a few seconds in order to prevent overheating.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product or between the product and the host board. The cleaning residues may affect long time reliability and isolation voltage.

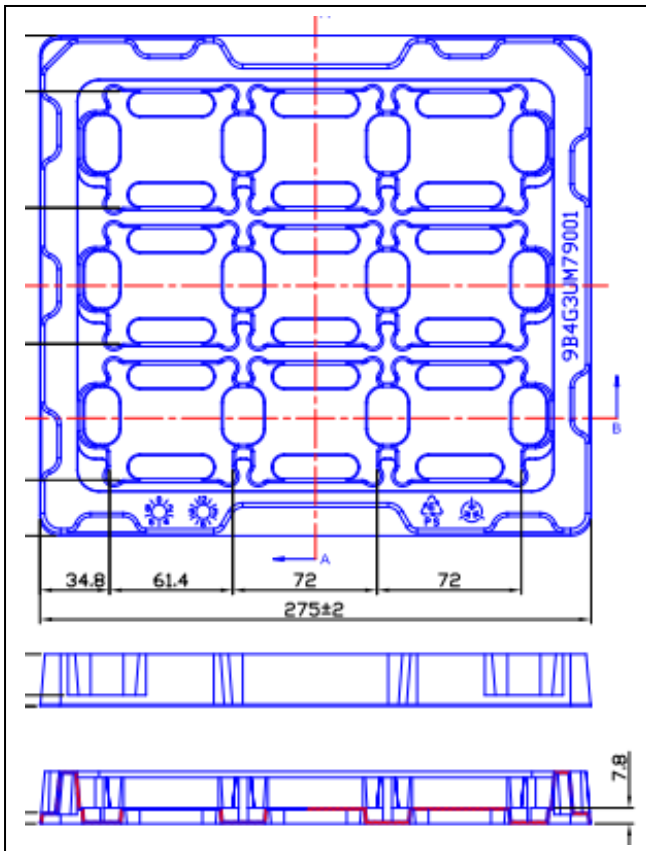
Delivery Package Information

The products are delivered in antistatic clamshell trays

Package



Tray Specifications	
Material	Antistatic PS
Surface resistance	$10^7 < \text{Ohm/square} < 10^{11}$
Bakability	This tray is not bake-able
Tray thickness	26.0 mm [1.0236 inch]
Box capacity	72 products (8 full trays/box)
Tray weight	60 g empty, 960 g full tray



Technical Specification

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Product Qualification Specification

Characteristics			
External visual inspection	IPC-A-610		
Change of temperature (Temperature cycling)	IEC 60068-2-14 Na	Temperature range Number of cycles Dwell/transfer time	-55 to 105°C 20 30 min/3 min
Cold (in operation)	IEC 60068-2-1 Ad	Temperature T _A Duration	-45°C 72 h
Damp heat	IEC 60068-2-30	Temperature Humidity Duration	45°C 95 % RH 72 hours
Dry heat	IEC 60068-2-2 Bd	Temperature Duration	125°C 1000 h
Electrostatic discharge susceptibility	IEC 61340-3-1, JESD 22-A114	Human body model (HBM)	Class 2, 2000 V
Immersion in cleaning solvents	IEC 60068-2-45 XA, method 2	Water Glycol ether {Isopropyl alcohol}	55°C 35°C {35°C}
Mechanical shock	IEC 60068-2-27 Ea	Peak acceleration Duration	200 g 6 ms
Moisture reflow sensitivity ¹	J-STD-020E	Level 1 (SnPb-eutectic) Level 3 (Pb Free)	225°C 260°C
Operational life test	MIL-STD-202G, method 108A	Duration	1000 h
Resistance to soldering heat ²	IEC 60068-2-20 Tb, method 1A	Solder temperature Duration	270°C 10-13 s
Robustness of terminations	IEC 60068-2-21 Test Ua1	Through hole mount products	All leads
Solderability	IEC 60068-2-20 test Ta ¹	Preconditioning Temperature, SnPb Eutectic Temperature, Pb-free	235°C 245°C
Vibration, broad band random	IEC 61373	Frequency RMS acceleration Duration	5 to 150 Hz 5 grms 5 hrs in each direction

Notes

¹ Only for products intended for wave soldering (plated through hole products)