

115V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	BV _{DSS} @ T _{J Max}	R _{DS(ON)} Max	I _D Max T _A = +25°C
		65mΩ @ V _{GS} = 4.5V	4.4A
115V	120V	70mΩ @ V _{GS} = 2.5V	4.3A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

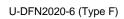
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Features and Benefits

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- ESD Protected
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

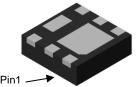
Mechanical Data

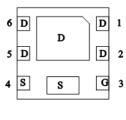
- Package: U-DFN2020-6
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)

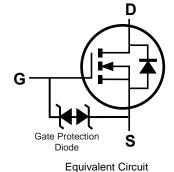












Top View

Bottom View

Pin Out Bottom View

Ordering Information (Note 4)

Part Number	Packago	Packing		
Fait Number	Package	Qty.	Carrier	
DMT12H060LFDF-7	U-DFN2020-6 (Type F)	3,000	Reel	
DMT12H060LFDF-13	U-DFN2020-6 (Type F)	10,000	Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information



26 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 1 = 2021)

W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	0	1	2	3	4	5	6	7	8	9	0	1

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	X	Υ	Z



26 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н		J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		VDSS	115	V
Gate-Source Voltage		V _{GSS}	±8	V
Continuous Drain Current, V _{GS} = 4.5V (Note 6) $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		lo	4.4 3.6	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	20	Α
Maximum Body Diode Continuous Current		Is	4.4	Α
Pulsed Body Diode Continuous Current (10µs Pulse, Duty C	Sycle = 1%)	Ism	20	Α
Avalanche Current, L = 0.3mH (Note 9)		las	12	Α
Avalanche Energy, L = 0.3mH (Note 9)	E _{AS}	21.6	mJ	
ESD Capability (Note 8)	HBM	1C	Class	
ESD Capability (Note o)		CDM	C4	Class

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	D-	1.1	W
Total Power Dissipation (Note 5)	$T_A = +70$ °C	P _D	0.7	VV
Thermal Resistance, Junction to Ambient (Note 5)		RθJA	110	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C		2.0	W
Total Power Dissipation (Note 6)	$T_A = +70$ °C	PD	1.3	VV
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	60	°C/W	
Thermal Resistance, Junction to Case (Note 6)	R _θ JC	8	C/VV	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	115	_		V	$V_{GS} = 0V$, $I_D = 10mA$
Zero Gate Voltage Drain Current	IDSS	_		1	μΑ	V _{DS} = 92V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
		_	42	65		$V_{GS} = 4.5V, I_{D} = 3A$
Static Drain-Source On-Resistance	RDS(ON)	_	48	70	mΩ	$V_{GS} = 2.5V, I_{D} = 2A$
		_	85	130		$V_{GS} = 1.5V, I_D = 10mA$
Diode Forward Voltage	VsD	_	0.63	1.0	V	Vgs = 0V, Is = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	475	_	pF	., 50,4,14, 0,4
Output Capacitance	Coss	_	115		pF	V _{DS} = 50V, V _{GS} = 0V, -f = 1MHz
Reverse Transfer Capacitance	Crss	_	7	_	pF	1 = 1101112
Gate Resistance	Rg	_	4.5		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	_	7.8	_	nC	., 50// 1 04
Gate-Source Charge	Qgs	_	0.5	_	nC	V _{DS} = 50V, I _D = 3A, V _{GS} = 4.5V
Gate-Drain Charge	Qgd	_	3.0	_	nC	VGS = 4.5V
Turn-On Delay Time	tD(ON)	_	5.5	_	ns	
Turn-On Rise Time	tR		2	_	ns	$V_{DS} = 50V, R_{L} = 16.7\Omega$
Turn-Off Delay Time	t _{D(OFF)}	_	27	_	ns	$V_{GS} = 4.5V$, $R_{GEN} = 3\Omega$
Turn-Off Fall Time	tF	_	8	_	ns]
Reverse Recovery Time	trr	_	24	_	ns	1 2A di/dt 200A/v-
Reverse Recovery Charge	Q _{RR}	_	30		nC	I _F = 3A, di/dt = 300A/μs

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

- 8. Guaranteed by design. Not subject to product testing.
- 9. Single pulse avalanche rating limited by junction temperature $T_{J(max)}$ = +150°C.



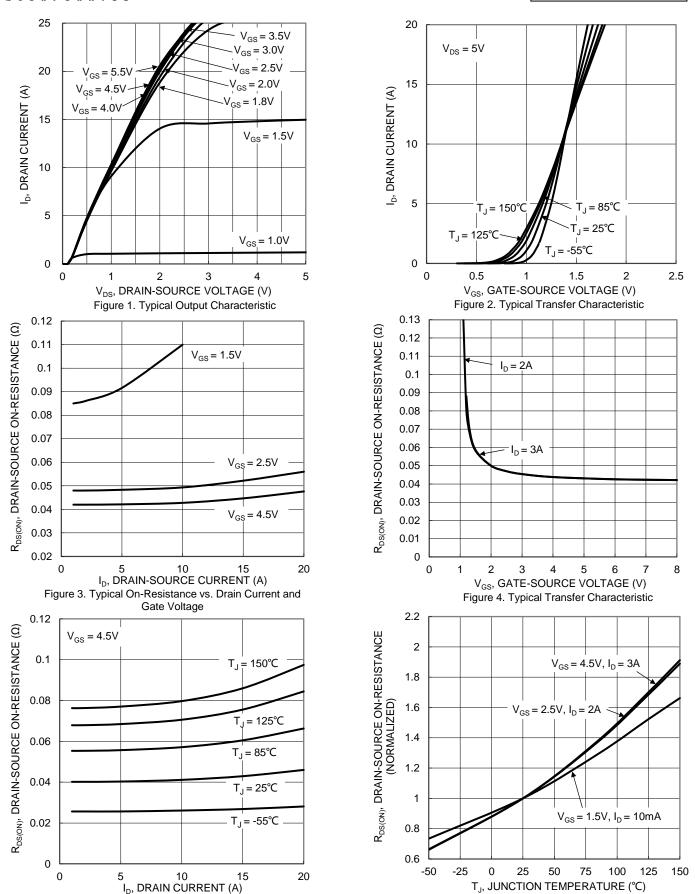


Figure 5. Typical On-Resistance vs. Drain Current

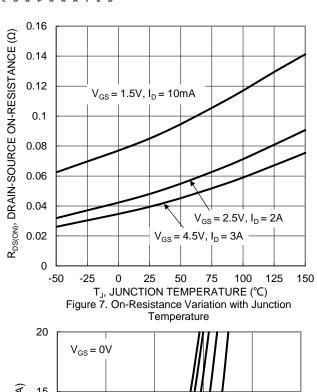
and Junction Temperature

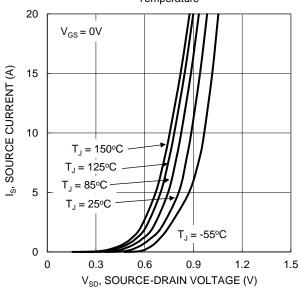
Figure 6. On-Resistance Variation with Junction

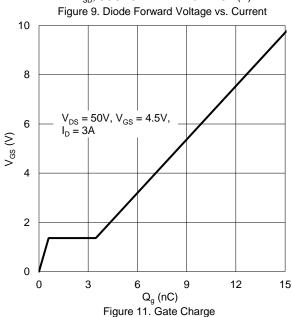
Temperature

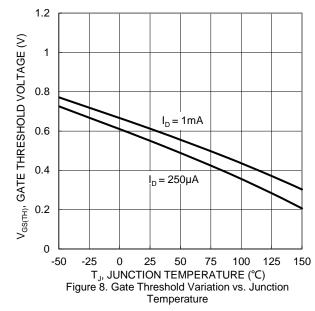


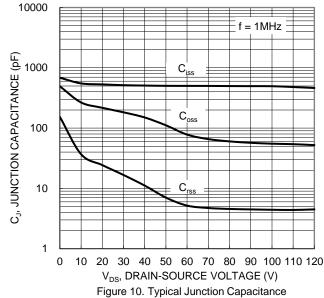


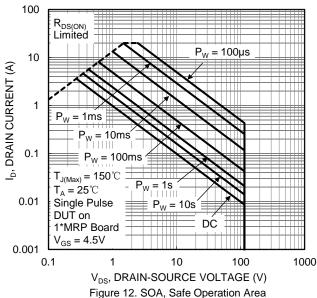














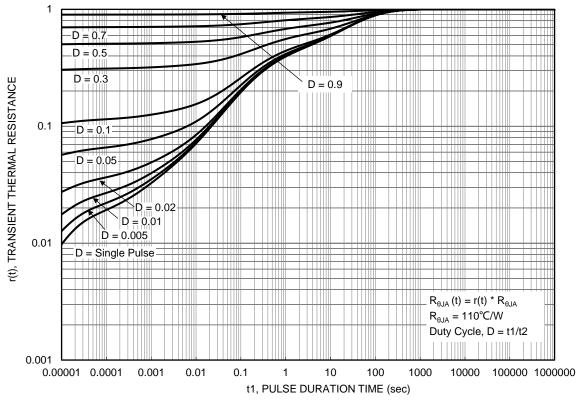


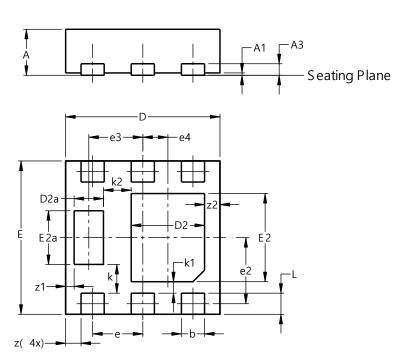
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

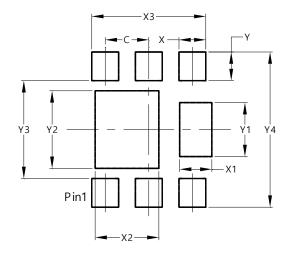


	U-DFN2020-6							
	(Type F)							
Dim	Min							
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е		0.65 BS	С					
e2	().863 BS	SC					
е3		0.70 BS	С					
e4	().325 BS	SC					
k		0.37 BS	С					
k1		0.15 BS	С					
k2		0.36 BS						
L	0.225	0.325	0.275					
Z	0.20 BSC							
z 1	().110 BS	SC					
z2		0.20 BS	С					
All C	imens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
Х3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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