



# PJQ2461

## 60V P-Channel Enhancement Mode MOSFET

Voltage **-60 V** Current **-2.4 A**

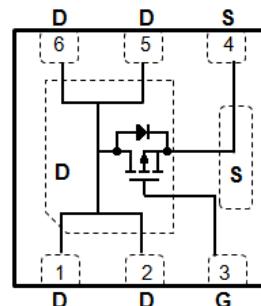
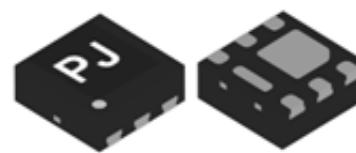
### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ -10V$ ,  $I_D @ -2A < 190m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ -4.5V$ ,  $I_D @ -1.5A < 240m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

DFN2020B-6L



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current (Note 4)	$I_D$	-2.4	A
		-1.9	
Pulsed Drain Current (Note 1)	$I_{DM}$	-9.6	W
Power Dissipation	$P_D$	2	
		1.3	
Single Pulse Avalanche Energy (Note 6)	$E_{AS}$	32	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal Resistance - Junction to Ambient (Note 4,5)	$R_{\theta JA}$	62.5	°C/W



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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.88	-2.5	
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-2\text{A}$	-	140	190	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-1.5\text{A}$	-	190	240	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	$\text{nA}$
<b>Dynamic</b> (Note 7)						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-2\text{A}, V_{\text{GS}}=-10\text{V}$ (Note 1,2)	-	8.3	-	$\text{nC}$
Gate-Source Charge	$Q_{gs}$		-	1.8	-	
Gate-Drain Charge	$Q_{gd}$		-	1.6	-	
Input Capacitance	$C_{iss}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHZ}$	-	430	-	$\text{pF}$
Output Capacitance	$C_{oss}$		-	33	-	
Reverse Transfer Capacitance	$C_{rss}$		-	29	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{\text{DD}}=-30\text{V}, I_{\text{D}}=-1\text{A}, V_{\text{GS}}=-10\text{V}, R_G=6\Omega$ (Note 1,2)	-	5.1	-	$\text{ns}$
Turn-On Rise Time	$t_r$		-	20	-	
Turn-Off Delay Time	$t_{d(off)}$		-	36	-	
Turn-Off Fall Time	$t_f$		-	11	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-1.5	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_s=-1\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.78	-1	V

### NOTES :

1. Pulse width  $\leq 300\text{us}$ , Duty cycle  $\leq 2\%$ .
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature  $T_{J(\text{MAX})}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J = 25^\circ\text{C}$ .
4. The maximum current rating is package limited.
5.  $R_{\text{eJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. The test condition is  $L=1\text{mH}, I_{AS}=-8\text{A}, V_{DD}=-25\text{V}, V_{GS}=-10\text{V}$
7. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

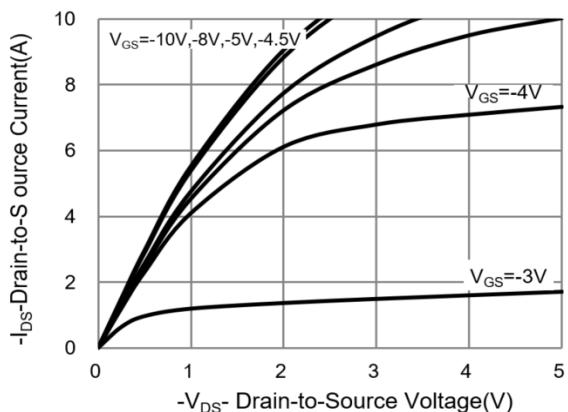


Fig.1 On-Region Characteristics

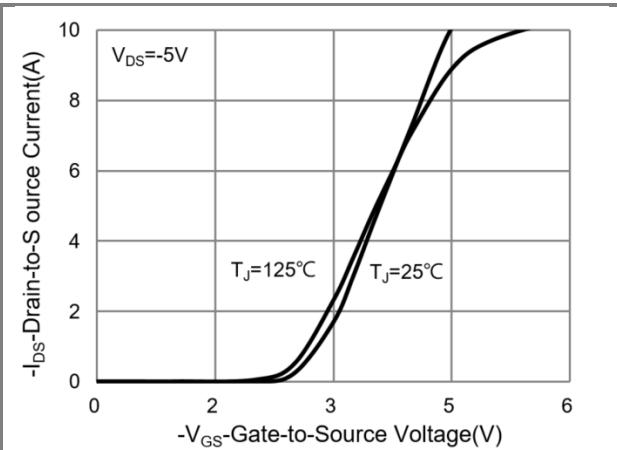


Fig.2 Transfer Characteristics

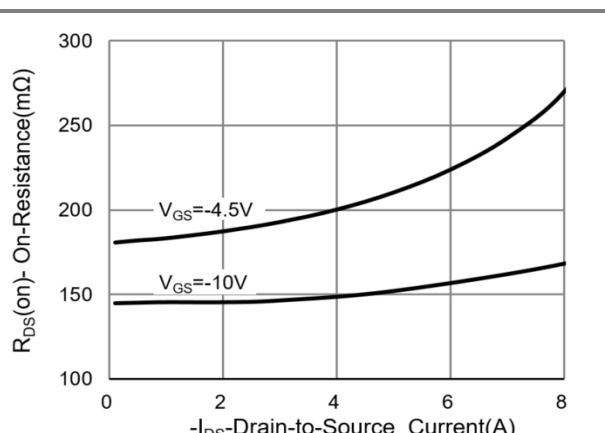


Fig.3 On-Resistance vs. Drain Current

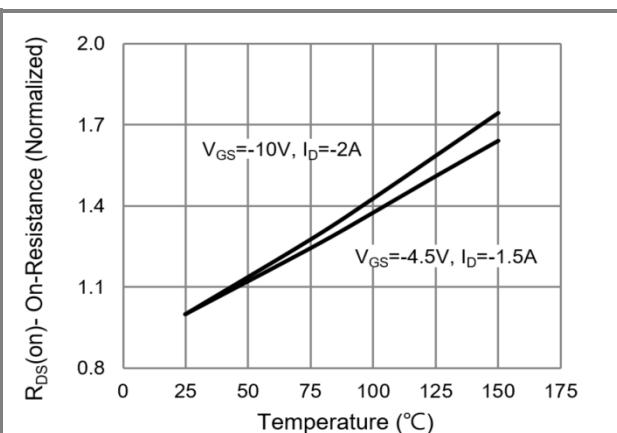


Fig.4 On-Resistance vs. Junction temperature

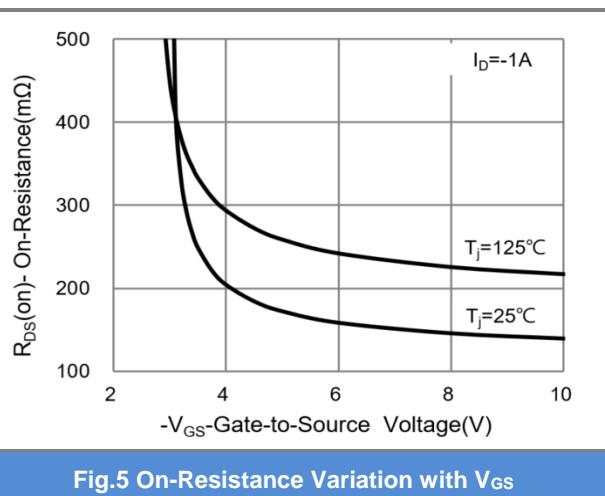


Fig.5 On-Resistance Variation with V<sub>GS</sub>

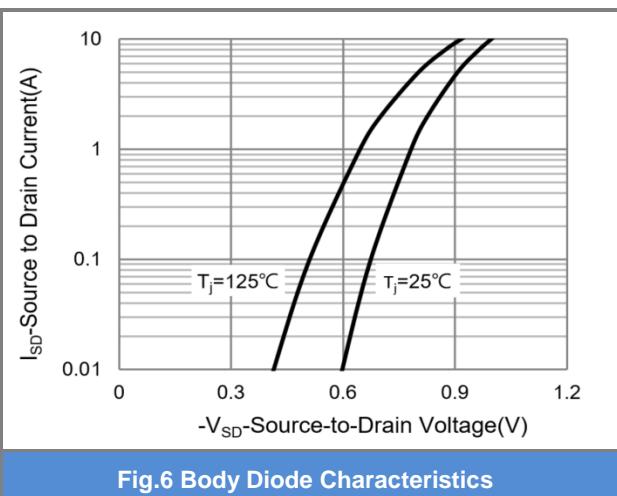


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

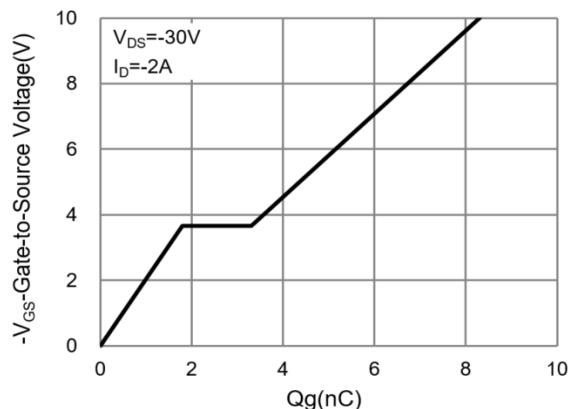


Fig.7 Gate-Charge Characteristics

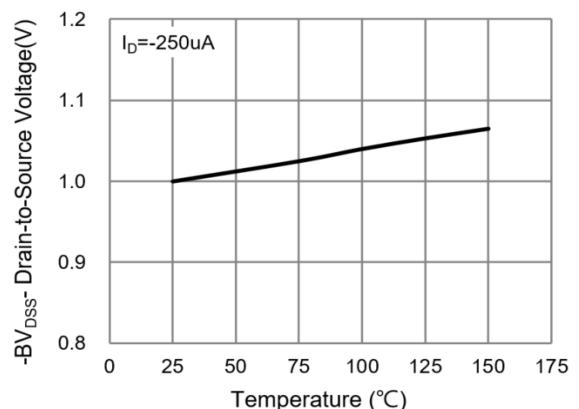


Fig.8 Breakdown Voltage Variation vs. Temperature

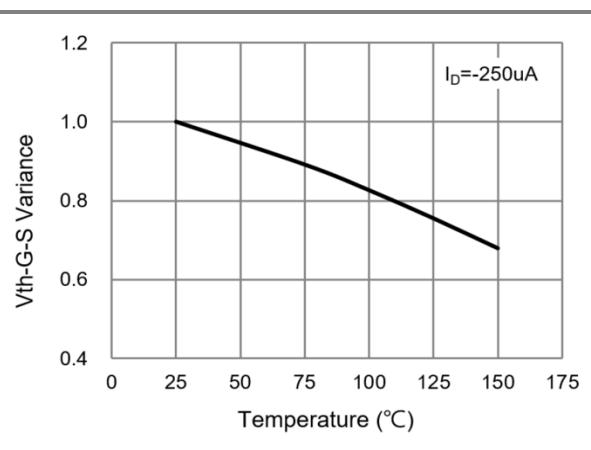


Fig.9 Threshold Voltage Variation with Temperature

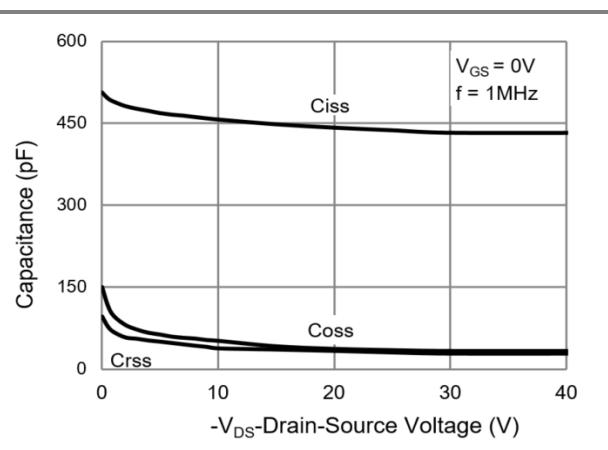


Fig.10 Capacitance vs. Drain-Source Voltage

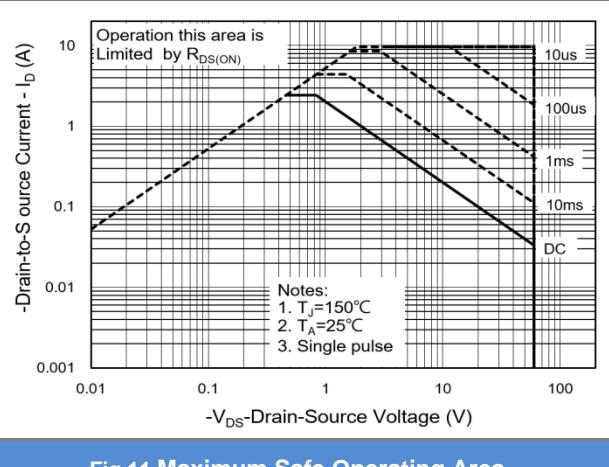


Fig.11 Maximum Safe Operating Area.

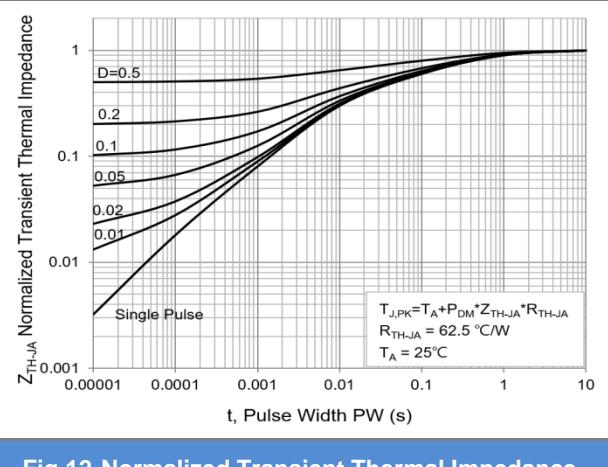


Fig.12 Normalized Transient Thermal Impedance

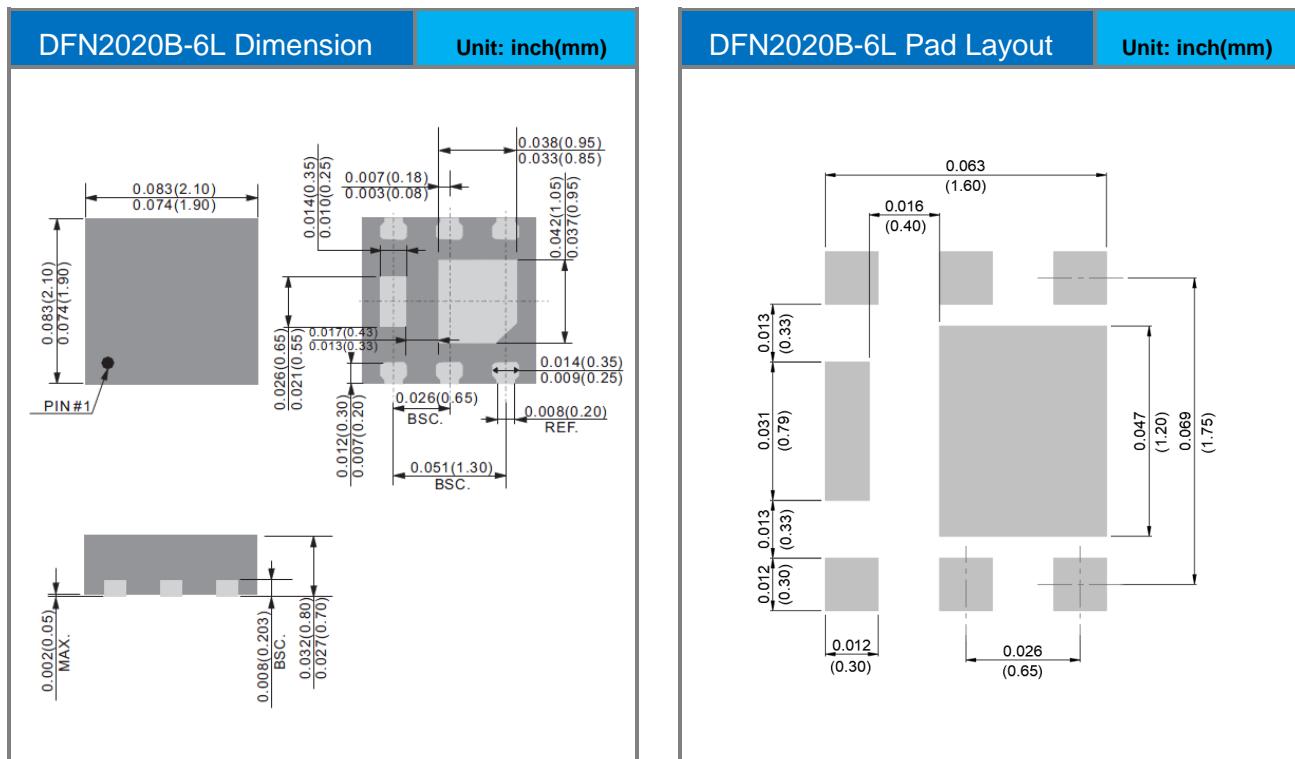


## PJQ2461

### Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2461_R1_00001	DFN2020B-6L	3K pcs / 7" reel	461	Halogen free RoHS compliant

### Packaging Information & Mounting Pad Layout





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