



#### SMS252010 Series Ultra-high current SMD power inductors





#### ◆特征:

- 低直流电湿和超大电流的薄型设计
- 磁屏豪型抗电磁干扰强适用于高密度安装
- 高可靠性,通过采用一体成型结构享有卓越 的抗震动性能
- 极低的直流电阻和超低的交流损耗,适用于高 开关频率
- 符合 RoHS,无卤和 REACH

#### ◆用途:

- 笔记本电脑 CPU 用直流/直流变换器
- 手机、平板电脑、HDD、DVC、PDA、5G 模块
- 服务器、基站等
- 各种 DC-DC 转换功率模块

#### ◆环境:

工作温度: -40℃ 至+125℃
 (包括线圈自身温升)

#### ◆试验设备:

• 电感值: WK3260B 或同等仪器

SMS

- 电流: WK3260B+WK3265B 或同等仪器
- 直流电阻. ← iroma 16502 或同等仪器

#### ◆产品型号:

## Features:

- Low RDC and ultra-high current thin design
- Magnetic shielding type, strong anti- electromagnetic
   Interference, suitable for nigh- density installation
- High-reliability, High vibration resistance as result of newly developed integral construction
- Extremely low DCR and ultra low AC losses for high switching frequencies
- Rons, Halogen Free and REACH Compliance

#### **Applications:**

- DC/DC converter for CPU in Novebook PC
- Phones, tablets, HDDs. DVCs, PDAs,5G modules
- Server, base station, etc.
- Various DC-DC conversion power modules

#### **Environmental Data:**

• Operating Temperature: -40°C to +125°C (Including coils self-temperature rise)

### Test Squipment:

- L: WK3260B LCR meter or equivalent
- Isat & Irms: WK3260B+WK3265Bor equivalent

T

(5)

DCR:Chroma 16502 or equivalent

#### Product Identification

M

	1				
	<b>1</b>				
	类型 Type	是小			
SMS	成型贴片功 Molding SMD Power Inducto				
4)					

公差 Inductance Tolerance

J:±5%,K: ±10%, L: ±15% M: ±20%,P: ±25%, N: ±30%

2					
外形尺寸(L×Wart) (mm)					
External Dimensions (L×W×H)					
(mm)					
252010	2.5×2.0×1.0				
(6)	-				

<u>R47</u>

(3)

	-777/-	
Z Y	(HE)	包装 Packing
>	В	散装Bulk Package
	Т	编带Tape & Reel

(3)	
Inductance	
0.47 uH	

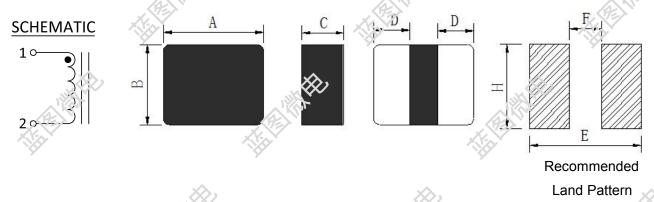
252010





# ◆外观尺寸:

# Shape and Omensions (dimensions are in om):



Part No	4 11/14		ITEM				14/14
T art No	A	A B		D	E	F	H
SMS252010	2.50±0.20	2.00±0.20	1.00 Max	0.90Тур	2.80 Typ	1.00 Typ	2.10 Typ

# ◆规格特性:

#### Specifications:

• SMS252(20) Series Electrical Characteristics (Electrical specifications at 25℃)

Post No.	Induct 1MHz		DCR (mΩ)		Saturation Current	Heat Rating Current
Part No	L(µH) '@04	Tol	Typical	Max	(A) Typical	(A) Typica
SMS252010-R33M	0.33	±20%	13.0	19.0	7.20	6.20
SMS252010-R47M	0.47	±20%	15.0	22.0	6.50	5.60
SMS252010-R68M	0.68	±20%	23.0	27.0	5.50	5.00
SMS252010-1R0M	1.0	±20%	25.0	30.0	4.80	4.10
SMS252010-1R5M	1.5	±20%	45.0	55.0	3.90	3.00
SMS252010-2R2M	2.2	±20%	62.0	70.0	3.00	2.10
SMS252010-3R3M	3.3	±20%	86.0	100.0	2.50	2.10
SMS252010-4R7M	4.7	±20%	160.0	180.0	2.00	1.60

- Saturation Current: DC current at which inductance drops 30% from its value without current.
- Heat Rating Current: the actual value of DC current when the temperature rise is ΔT 40 °C (Ta=25 °C)
- Rated DC Current: The less value which is Isat or Irms.
- Special remind:Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

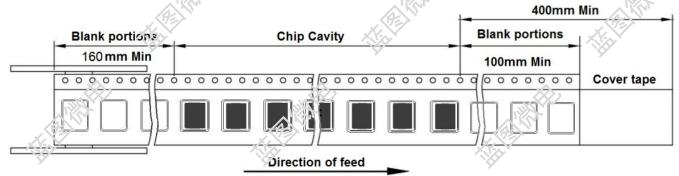




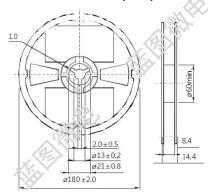


# Packaging:

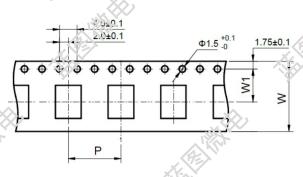
• Tape and Reel Specifica@ons: (Dimensions are in mm)



#### Reel dimensions (mm)

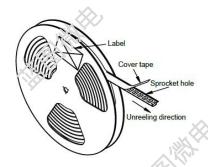


#### ◆Tape Dimension (mm)

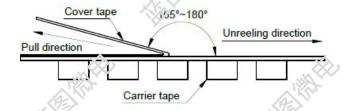


Dort No.	Тар	Tape Dimension		REEL	Inside	Outside
Part No.	W	Р	W1	(PC3)	Box(PCS)	Carton(PCS)
SMS252010	8.0	4.0	3.5	3000	30,000	120,000

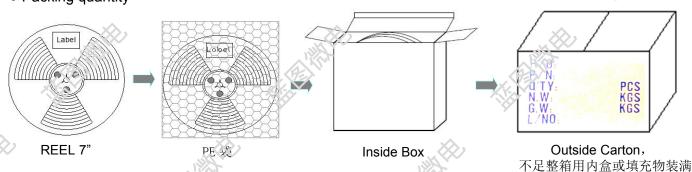
#### • Cover tape peel off condition



- a) Cover tape peel force shall be 10 to 120g
- b) Noodle strip peeling angle 165° to 180°



#### Packing quantity







# Reliability Testing:

B	
Requirements	Test Methods and Remarks
Define: A: sectional area of terminal A ≤ 8mm2 force ≥ 5N time: 30sec 8mm2 <a 0.12mm="" 10n="" 10sec="" 2.solder="" 20mm2="" 20mm2<a="" 20n="" 3.meet="" above="" any<="" force="" paste="" requirements="" td="" the="" thickness:="" time:="" without="" ≤="" ≥=""><td>Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.</td></a>	Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.
0.50Applied force:5N Duration:  10sec2.Terminal diameter(d) mm0.50 < d ≤ 0.59Applied force:10N Duration:  10sec3.Terminal diameter(d) mm0.80 < d ≤ 1.25Applied force:20N Duration:  10sec4.Terminal diameter(a) mmD > 1.25Applied force:40N Puration:  10sec5.Meet the above requirements	Pull Force: the force shall be applied gradually to the terminal and thenmaintained for 10 seconds.
	1.Solder the inductor to the test jig (glass epoxy board 2.shown in Using a leadfree solder. Then apply a force in the direction shown 3.Flexure: 2mm. 4.Pressurizing Speed: 0.5mm/sec. 5.Keep time: 30 sec.
inappearance.  2.No short and no open.  1.No visible mechanical damage.  2.Wetting shall exceed 75% coverage for  3.Terminals must have 95% minimum solder	1. Drop the packaged products from 1m high in 1 angle, 3 ridges and 6surfaces, twice in each direction.  1. Solder temperture 240±2°C 2. Duration: 3 sec. 3. Solder: Sn/3.9Ag/0.5Cu. 4. Flux: 25% Resin and 75% ethanol in weight
	Define: A: sectional area of terminal A ≤ 8mm2 force ≥ 5N time: 30sec 8mm2 <a 0.12mm="" 0.35<d≤="" 0.50applied="" 1.25applied="" 1.terminal="" 10n="" 10sec="" 10sec2.terminal="" 10sec3.terminal="" 10sec4.terminal="" 16n="" 2.solder="" 20mm2="" 20mm2<a="" 20n="" 3.meet="" 5n="" above="" any="" diameter(d)="" diameter(f)="" duration:="" force="" force:="" loose="" mm="" mm0.50<d≤="" mm0.80<d≤="" mmd="" paste="" requirements="" terminal="" the="" thickness:="" time:="" without="" ≤="" ≥=""> 1.25Applied force: 40N Ouration: 10sec5.Meet the above requirements without any loose terminal. 1.No visible mechanical damage.  1.No visible mechanical damage.  2.No short and no open.</a>





Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	i.Solder the inductor to the testing its (glass epoxy
	լ 2. inductance change: Within ±10%.	boardshown in ) using leadfree solder.
	3.Q factor change: Within ±20%.	2.The inductor shall be subjected to a simple
<i>X</i> >,	0.112	harmonic motion having total amplitude of 1.5mm,
THE PARTY OF THE P	Cu pad Solder mask	the frequency being varieduniformly between the
A A		approximate limits of 10 and 55 Hz.
Vibration	72 72 72 72	3.The frequency range from 10 to 55 Hz and
Reference documents:		return to 10 Hz shallbe traversed in approximately
GB/T 2423.10-2019	Jass Epoxy Board	1 minute. This motion shall be applied for a period
振動試验	1444	of 2 hours in each 3mutually perpendicular
	* PEN	directions(total of 6 hours).
	A STATE OF THE PARTY OF THE PAR	Freq 1
		55Hz
The state of the s	THE PERSON NAMED IN COLUMN TO PE	10Hz
X HEY	, (P)	0 1Min Time
	1.No visible mechanical damage.	1.Start at ( 85-125℃) for T time, rush to
	2. Inductance change: Within ±10%.(M	ln-Zn: $(-55\sim40$ $^{\circ}\mathrm{C}$ ) for T time as one cycle, go through100
<b>)</b>	Within ≦ვნ% )	cycles.
The area of Obe only	3.Q factor change: Within ±20%.	a::Transforming interval: Max. 20 sec
Thermal Shock	XXXX	3.Tested cycle: 100 cycles.
Reference documents:		4.The chip shall be stabilized at normal condition
GB/T 2423.22-2012		for 1~2 hours
Method Na	· State	125°C/85°C 30 min. 30 min.
冷热冲击试验	A KINT	Ambient
	A. C.	Temperature 30 min.
<b>Y</b>	.**	-550/-40°C 20sec. (max.)
	<i>≫</i> ,	×.
	1.No visible mechanical damage.	1.7emperature:M(-55~-40±2℃)
	2. Inductance change: Within ±10%.(Mn-Zr	7. 1/2.
	Within ≦30%)	3.The chip shall be stabilized at normal condition for
Low temperature Storage	3.Q factor change: Within ±20%.	1~2 hoursbefore measuring.
Reference documents:	(A)	× 1
GB/T 2423.1-2008	- Leki	Room Temp
Method Aົລ	X DEX	0 96H / Test / 97H 98H Time
低温储存试验		XIIII
		M°C Low temperature
1		

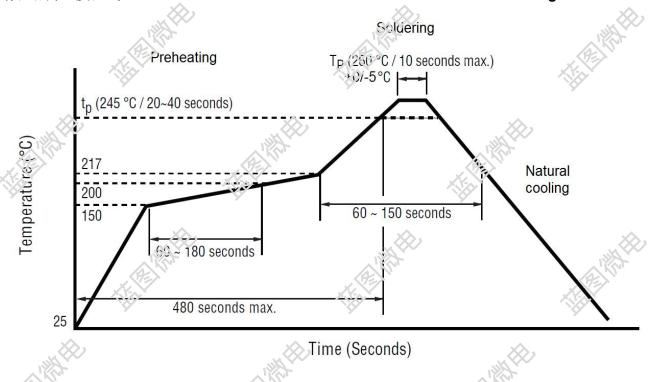




Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	∴Temperature:N(125~85±2°C).
High temperature	2. (relictance change: Within ±10%.(Mn-Zb:	2.Duration: 96±2 hours
Storage	   \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\	3.The chip shall be stabilized at normal condition
	3.Q factor change: Within ±20%.	for 1~2 hoursbefore measuring.
GB/T 2423.2-2003	×.	Temp High temperature
Method Bb	LINE COLOR	N°C
A.	DE KA	Room
高温储存试验	N. T.	Temp
<b>y</b>	Y	0 96H 97H 98H Time
	1.No visible mechanical damage.	1.Temperature: 60±2°C
	2. Inductance change: Within ±10%.(Mn-Zn:	2.Humidity: 90% to 95% RH.
Damp Heat	Within ≦30%)	3.Duration: 96±2 hours.
(Steady States)	강.	4.The chip shall be stabilized at normal condition
Reference documents:	Y	for 1~2 hoursbefore measuring.
GB/T 2423.3-2016	<i>∽</i>	Temp
恒定湿热试验	CAT Plus	93%RH High temperature High temperature
	A KIN	Room Conditions
***	XI:	Test
Ÿ	***	90H 97H 90H 1111E
Heat endurance of	1.No significant defects in appearance.	1.Refer to the above reflow curve and go through
Reflow soldering	2. △ L/L ≦ 10% (Mn-Zn: △ L/L ≦ 30% )	the reflow for twice.
Reference documents:	3. △ Q/(2 ≥ 30% (SMD series only)	∷ੇਮਿe peak temperature : 260+0/-5℃
GJB 360B-2009	4, △ UCR/DCR≦10%	
回流焊耐热性试验	No see a defermention on absorber in	
Desistant to a New t	No case deformation or change in	To dip parts into IPA solvent for 5±0.5Min,then
Resistance to solvent	appearance or obliteration of marking	drying them at room temp for 5Min,at last ,to
test Reference documents:	S. KIT	brushing making 10 limes.
IEC 68-2-45:1993	XI.	XY.XE
耐溶剂性试验	***	****
에 1년 기기 그 (시2)	. ^	<u> </u>
Overload test	1.During the test no smoke, no peculiar,	W W W
Reference documents:	smeil no fire	
JIS C5311-6.13		Apply twice as rated current for 5 minutes.
过负荷试验	.4	
	_	
voltage resistance test	1.During the test no breakdown	- N
. 1884	2.The characteristic is normal after test	- Company of the Comp
MIL-STD-202G Method	17=/	1. For parts with wo coils
301		2. DC1000V, Corrent: 1mA, Time: 1Min.
绝缘耐压测试		Refer to catalogue of specific products
	XX,	$\times$

#### **★推荐回流焊温度曲线**

#### Recommended reflow soldering curve:



The recommended reflow conditions as above graph, is set according to our soldering equipment. DUE to various manufactures may have different reflow soldering equipment, products, process conditions, set methods. And so on, when setting the reflow conditions, Please adjust and confirm according to users' environment/equipment.





#### 使用注意事项

# REMINDERS FOR USING THESE PRODUCTS



● 保存时间为12 个月以内, 保存条件(温度5~40°C以下、湿度35~ 66%RH 以下), 需充分注意 若超过保存时间,端一点极的可焊性将可能老化。

The storage period is within 12 months. Be sure to follow the storage conditions (temperature: 5~40°C, humidity: 35 to 65% RH or less). If the storage period elapses, the soldering of the terminal electrodes may deteriorate.

● 请勿在气体腐蚀环境(盐、酸、碱等)下使用和保存。

Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).

- 手上的油脂会导致可焊性降低,应避免用手直接接触端子。
  - Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering Always ensure optimum conditions for soldering.
- 请小心轻拿轻放,避免由于产品的跌落或取出不当而导致的损坏
  - Please always handle products carefully to prevent any damage caused bydropping down or inappropriate removing.
- 端子过度弯齿会导致断线,请不要过度弯曲端子。
  - Don't bend the terminals with excessive stress in case of any wire fracture.
- 不受清洗产品, 如需要清洗时请联系我司
  - Don't rinse coils by yourself and please contact SXN if necessary.
- 请勿将本产品靠近磁铁或带有磁力的物体
  - Don't expose the products to magnets or magnetic fields
- 在实施焊接前,请务必进行预热。预热温度与焊接温度及芯片温度的温度差要在150°C 以内。
  - Before soldering, be sore to preheat components. The preheating temperature should be set so that the temperature difference between the solder temperature and chip temperature does not exceed 150°C.
- 安装后的焊接修正应在规格书规定的条件范围内。若加热过度可能导致短路、性能降低、寿命减少。
  Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespec shortening may occur.
- 装置会因通电而自我发热(温度上升),因此在热设计方面需留有充分余地。
  Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
- 非磁屏蔽型在基板设计时需注意配置线圈,受到电磁干扰可能会导致误动作。
  - Carefully lay out the cold for the circuit board design of the non-magnetic shield type. A malfunction may occur due to magnetic interference.