

MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

S8550-MS

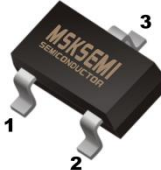

Product specification

TRANSISTOR (PNP)

FEATURES

- Complimentary to S8050-MS
- Collector current: $I_C=0.5A$

Reference News

PACKAGE OUTLINE	MARKING
 <p>1. BASE 2. EMITTER 3. COLLECTOR</p>	
SOT-23	

MAXIMUM RATINGS (Ta=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-25	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current -Continuous	-0.5	A
P_C	Collector Dissipation	0.3	W
T_j	Junction Temperature	150	°C
T_{stg}	Storage Temperature	-55-150	°C

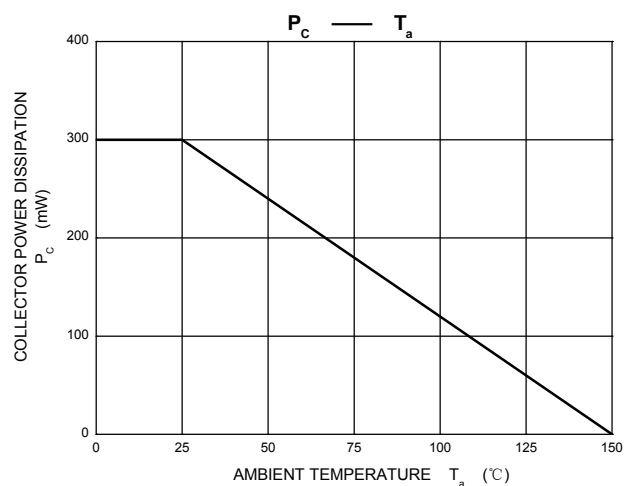
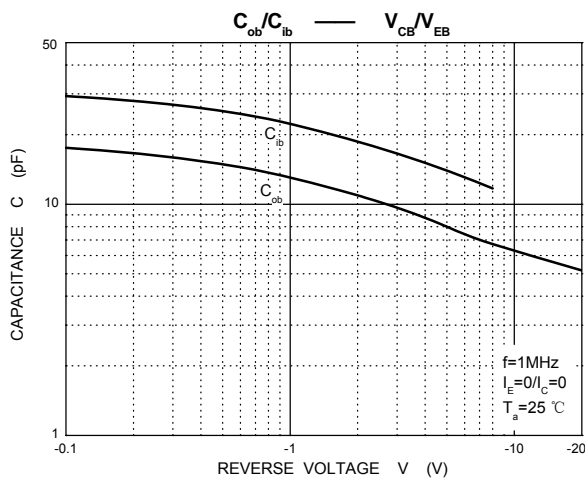
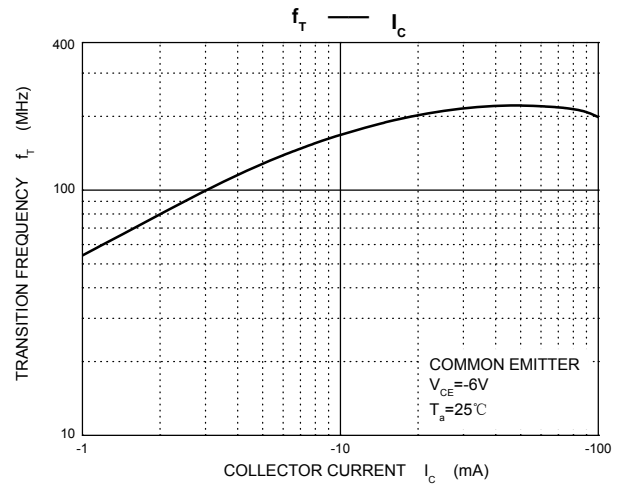
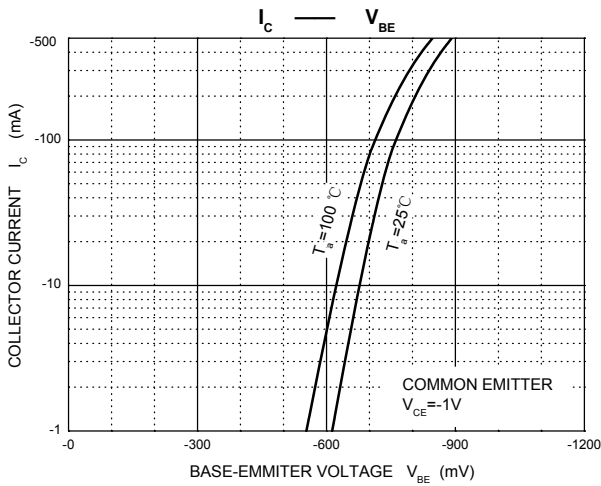
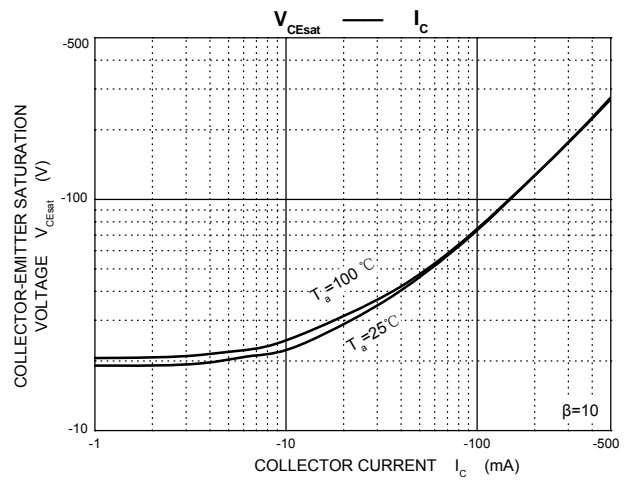
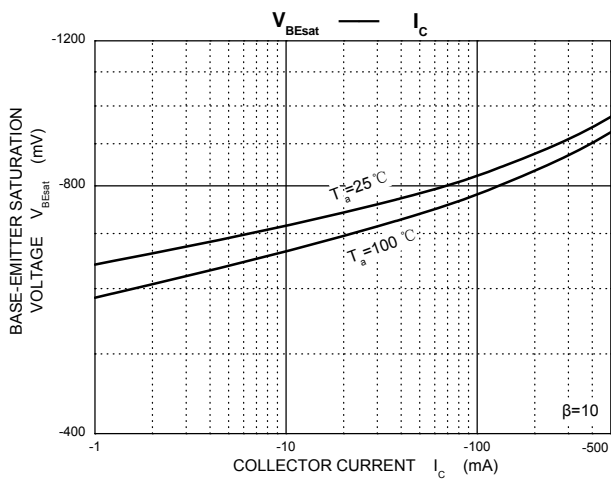
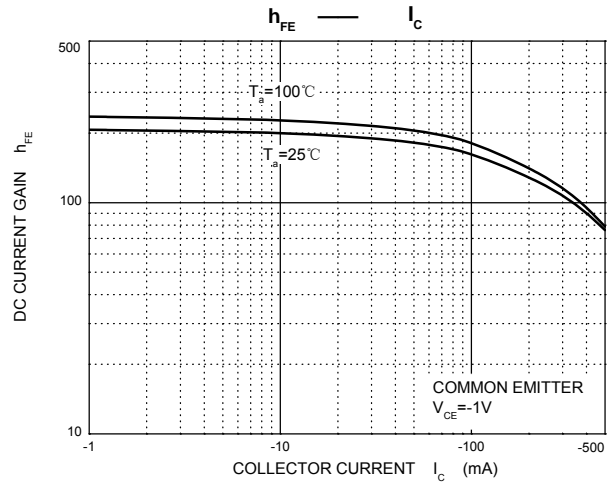
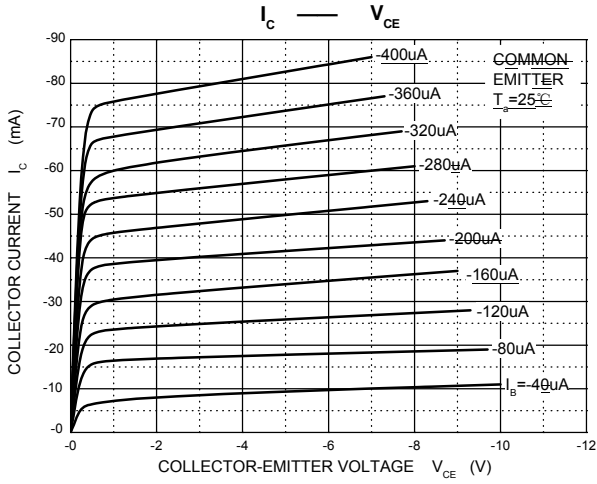
ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = -100\mu A, I_E = 0$	-40		V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = -1mA, I_B = 0$	-25		V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = -100\mu A, I_C = 0$	-5		V
Collector cut-off current	I_{CBO}	$V_{CB} = -40V, I_E = 0$		-0.1	μA
Collector cut-off current	I_{CEO}	$V_{CE} = -20V, I_B = 0$		-0.1	μA
Emitter cut-off current	I_{EBO}	$V_{EB} = -3V, I_C = 0$		-0.1	μA
DC current gain	$h_{FE(1)}$	$V_{CE} = -1V, I_C = -50mA$	120	400	
	$h_{FE(2)}$	$V_{CE} = -1V, I_C = -500mA$	50		
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -500mA, I_B = -50mA$		-0.6	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = -500mA, I_B = -50mA$		-1.2	V
Transition frequency	f_T	$V_{CE} = -6V, I_C = -20mA$ $f = 30MHz$	150		MHz

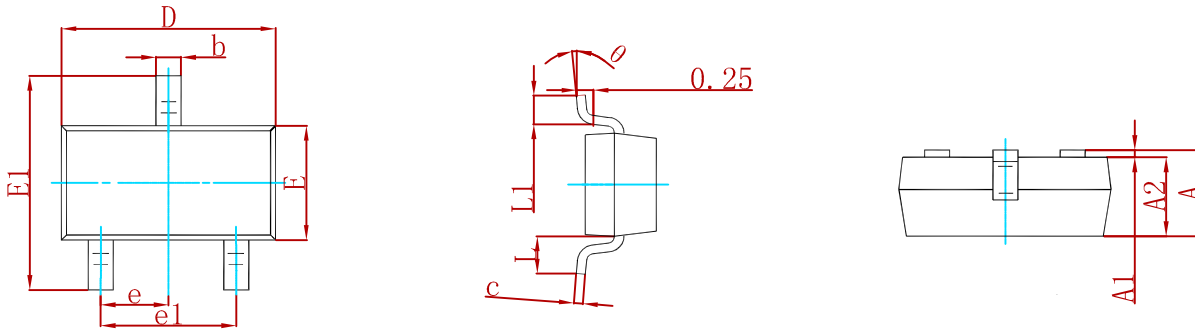
CLASSIFICATION OF $h_{FE(1)}$

Rank	L	H
Range	120-200	200-350

Typical Characteristics

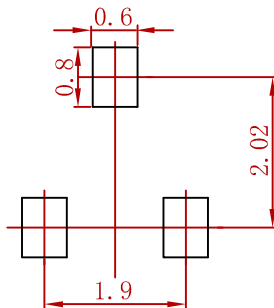


PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:
 1. Controlling dimension: in millimeters.
 2. General tolerance: ± 0.05mm.
 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
S8550-MS	SOT-23	3000

Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.