Vishay Semiconductors





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DPAK (TO-252AA)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	5 A				
V _R	600 V				
V _F at I _F	1.2 V				
t _{rr} (typ.)	18 ns				
T _J max.	175 °C				
Package	DPAK (TO-252AA)				
Circuit configuration	Single				

FEATURES

- Hyperfast recovery time, reduced Q_{rr} and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM/CCM operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 150 °C	5	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	70	A
Peak repetitive forward current	I _{FM}	$T_{C} = 150 \ ^{\circ}C, f = 20 \ \text{kHz}, d = 50 \ \%$	10	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	N
Forward voltage	V	I _F = 5 A	-	1.54	1.85	V
Forward voltage	V _F	I _F = 5 A, T _J = 150 °C	-	1.20	1.40	
		V _R = V _R rated	-	-	5	
Reverse leakage current	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	130	μA
Junction capacitance	CT	V _R = 600 V	-	3.5	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH

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RoHS COMPLIANT



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 10$	00 A/µs, V _R = 30 V	-	18	-	
Reverse recovery time	+	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	22	-	
Reverse recovery time t _{rr}	۲r	T _J = 25 °C		-	25	-	ns
		T _J = 125 °C	I _F = 5 A dI _F /dt = 200 A/µs V _R = 390 V	-	35	-	
Pools receivers ourrent	1	T _J = 25 °C		-	3.9	-	А
Peak recovery current I _{RRM}	IRRM	T _J = 125 °C		-	5.1	-	A .
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	51	-	nC
		T _J = 125 °C		-	93	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	3	°C/W
Approvimete weight				0.3		g
Approximate weight				0.01		oz.
Marking device		Case style DPAK (TO-252AA)		5EWH	06FNH	

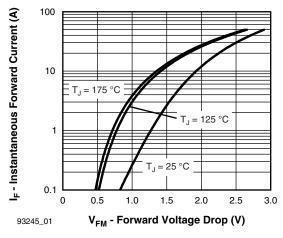


Fig. 1 - Typical Forward Voltage Drop Characteristics

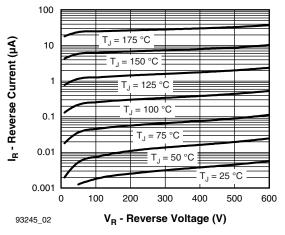
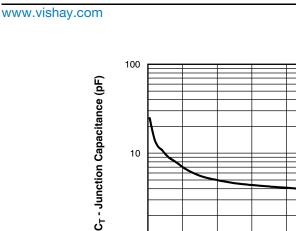


Fig. 2 - Typical Values of Reverse Current vs. **Reverse Voltage**

VS-5EWH06FNHM3

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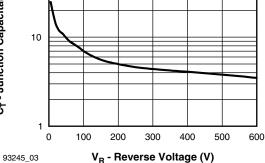


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

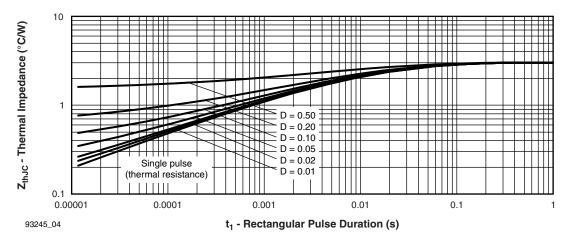
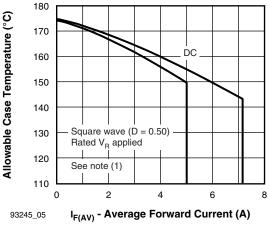
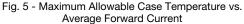
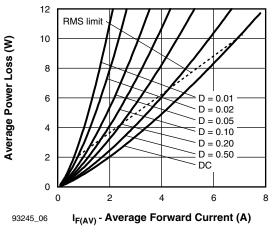


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics









Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

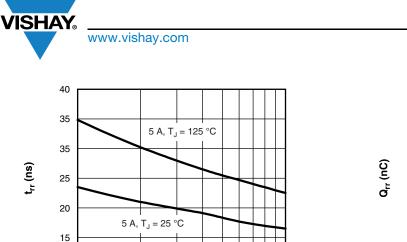
 $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R

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dl_F/dt (A/µs)

Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

10

93245 07

100

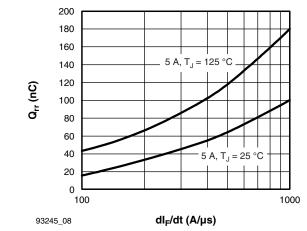
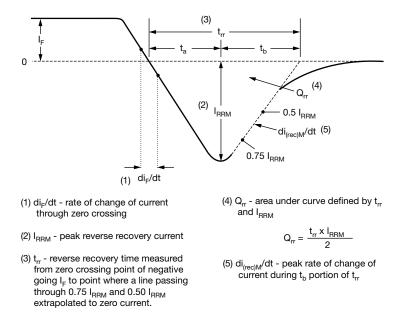


Fig. 8 - Typical Stored Charge vs. dl_F/dt



1000

Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

								-		
Device code	vs-	5	E	w	н	06	FN	TRL	н	М3
		2	3	4	5	6	7	8	9	10
	1	- Vi	shay Sen	nicondu	ctors pro	oduct				
	2	- Ci	urrent rati	ing (5 =	5 A)					
	3	- Ci	rcuit conf	iguratio	n:					
		E	= single o	diode						
	4	- Pa	ackage id	entifier:						
		W	= D-PAK	(
	5	- H	= hyperfa	ast recov	/ery					
	6	- Vo	oltage rat	ing (06 =	= 600 V))				
	7	- FI	1 = TO-2	52AA						
	8	- •	None = tu	ube						
		•	TR = tape	e and re	el					
		•	TRL = tap	be and r	eel (left	oriented	d)			
		•	TRR = ta	pe and r	eel (righ	nt orient	ed)			
	9	- H	= AEC-Q	101 qua	alified					
	10	- Er	vironme	ntal digit						
		N/	2 – haloa	on froo		oomolia	nt and	tormina	tions los	d (Dh)

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-5EWH06FNHM3	75	Antistatic plastic tube				
VS-5EWH06FNTRHM3	2000	13" diameter reel				
VS-5EWH06FNTRLHM3	3000	13" diameter reel				
VS-5EWH06FNTRRHM3	3000	13" diameter reel				

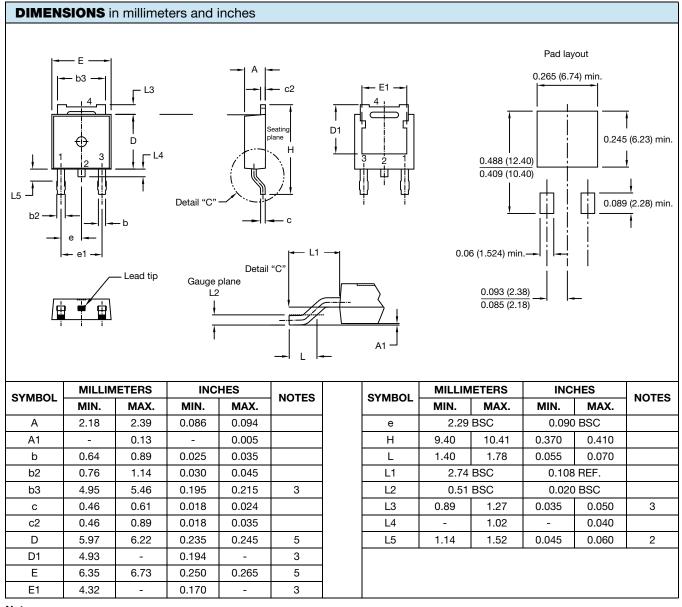
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95176				
Packaging information	www.vishay.com/doc?95518				
SPICE model	www.vishay.com/doc?95186				

Outline Dimensions



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Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA, except for D1 dimension



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