Crystal Oscillator (SPXO) • Package size (2.5 mm × 2.0 mm × 0.74 mm)

- · Fundamental mode SPXO
- · Output: LVPECL

· Reference weight Typ.12 mg

[1] Product Number / Product Name / Marking

(1-1) Product Number / Ordering Code

X1G0058810003xx

Last 2 digits code(xx) defines Quantity.

The standard is "15", 2 000 pcs/Reel.

(1-2) Product Name / Model Name

SG2520EGN 156.250000MHz DJGPZA

[2] Operating Range

Parameter	Symbol	ę	Specification	S	Unit	Conditions
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Supply voltage	V _{CC}	2.375	2.5	2.625	V	-
	GND	0	-	0	V	-
Input voltage	V _{IN}	0	-	V _{CC}	V	-
Operating temperature range	T_use	-40	-	+85	°C	-
ECL load condition	L_ECL	-	50	-	Ω	Terminated to V_{CC} - 2.0 V

[3] Frequency Characteristics

(Unless stated otherwise [2] Operating Range)

Parameter	Symbol		Specifications	3	Unit	Conditions
Falameter	Symbol	Min.	Тур.	Max.		
Output frequency	fo	-	156.250000	-	MHz	-
Frequency tolerance *1	f_tol	-50	-	+50	×10 ⁻⁶	T_use

*1 Frequency tolerance includes Initial frequency tolerance, Frequency / temperature characteristics, Frequency / voltage coefficient and aging (10 years, +25 °C).

[4] Electrical Characteristics

(Unless stated otherwise [2] Operating Range)

Deremeter	Symbol	9	Specification	าร	Unit	Conditions
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Start-up time	t_str	-	-	10	ms	t = 0 at 90 % Vcc
Current consumption	I _{CC}	-	-	60	mA	
Disable current	I_dis	-	-	35	mA	OE = GND
	V _{OH}	V _{CC} - 1.1	-	-	V	DC level
Output voltage	V _{OL}	-	-	V _{CC} - 1.5	V	DC level
Differential swing	V _{SW}	800	-	2 000	mV	Differential output peak to peak voltage
Rise time	tr	-	-	0.35	ns	20 % to 80 % of (V $_{\rm OH}$ - V $_{\rm OL})$
Fall time	tf	-	-	0.35	ns	80 % to 20 % of (V_{OH} - V_{OL})
Symmetry	SYM	45	50	55	%	At output crossing point
	V _{IH}	70 % Vcc	-	-	V	OE terminal
Input voltage	V _{IL}	-	-	30 % Vcc	V	OE terminal
Output disable time (OE)	tstp_oe	-	-	100	ns	OE terminal HIGH \rightarrow LOW
Output enable time (OE)	tsta_oe	-	-	500	ns	OE terminal LOW \rightarrow HIGH
Phase jitter	t _{PJ}	-	-	70	fs	Offset freq.: 12 kHz to 20 MHz

[For other general specifications, please refer to the attached Full Data Sheet below]

SG2520EGN

2.5 × 2.0 mm

Low Phase Jitter Crystal Oscillator: SG2016EGN/SG2520EGN

Features

- Crystal oscillator (SPXO)
 - Frequency range (fo): 25 MHz to 50
- Output:
 - Supply voltage: 2.5
- Operating temperature:
- Low phase jitter:

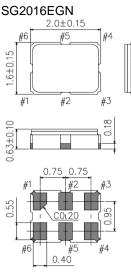
Applications

- Network equipment (Router, Switch, Optical module, etc.)
- Data center
- Test and Measurement Equipment, Factory Automation
- High Speed Converters like ADC and DAC

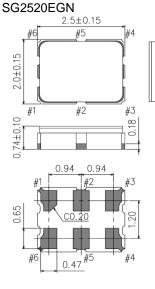
Description

5G will increase network communication traffic exponentially. A 5G communication network requires high-speed and wide-bandwidth, while minimizing noise. This will be achieved with a high frequency, low jitter reference clock for the communication equipment where small form factor optical modules will be used. The SG2016EGN/SG2520EGN is the next generation to the very popular SG3225EEN family offering the same combination of features with a wide range of available frequencies, low jitter, and improved frequency tolerance performance due to using an in-house designed IC with temperature compensation in a smaller package.

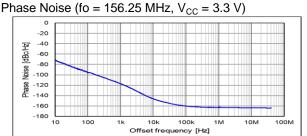




Pin	Connection						
1	OE/ST						
2	N.C. (Open or V _{CC})						
3	GND						
4	OUT						
5	ŌŪŦ						
6	V _{CC}						



Typical Performance

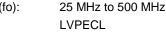


Phase Jitter (12 kHz to 20 MHz): 44 fs Typ.

RoH

SG2016EGN

2.0 × 1.6 mm



- 2.5 V Typ. / 3.3 V Typ.
- e: -40 °C to +105 °C
 - 44 fs Typ. (fo = 156.25 MHz)

[1] Product Number / Product Name

(1-1) Product Number SG2016EGN: X1G006131xxxx15 SG2520EGN: X1G005881xxxx15

(Please contact Epson for details)

(1-2) Product Name (Standard Form) <u>SG2016 EGN</u> <u>156.250000MHz</u>

3

1 2

 Model
 Output (E: LVPECL)
 Frequency
 Supply voltage
 Frequency tolerance
 Operating temperature
 Function
 Output disable status (Z: High impedance)

(9)Internal identification code (A: default)

<u>C</u> <u>J</u> <u>G</u> <u>P</u> <u>Z</u> <u>A</u> 4 5 6 7 8 9

Supply voltage							
D 2.5 V Typ.							
С 3.3 V Тур.							
<u> </u>							
5 Frequency tolerance							

9	©						
D	±25 × 10 ⁻⁶						
J	±50 × 10 ⁻⁶						

60	perating temperature					
G -40 °C to +85 °C						
Н	-40 °C to +105 °C					

⑦ Function					
Ρ	Output Enable				
S	Standby				

[2] Absolute Maximum Ratings

Parameter	Symbol		Specification		Unit	Conditions
Farameter	Symbol	Min.	Тур.	Max.	Offic	
Maximum supply voltage	V _{cc}	-0.3	-	4.0	V	
Input voltage	Vin	-0.3	-	V _{CC} + 0.5	V	OE/ST terminal
Storage temperature range	T_stg	-55	-	+125	°C	

[3] Operating Range

Parameter	Symbol		Specification		Unit	Conditions
Falametei	Symbol	Min.	Тур.	Max.	Offic	Conditions
Supply voltage	V _{CC}	2.375	2.5	2.625	V	Suffix: D
Supply voltage	V CC	3.135	3.3	3.465	V	Suffix: C
Supply voltage	GND	0.0	0.0	0.0	V	
Operating temperature range	T_use	-40	+25	+85	°C	Suffix: G
Operating temperature range	I_use	-40	+25	+105	°C	Suffix: H
ECL load condition	L_ECL		50		Ω	Terminated to V_{CC} - 2.0 V

* Power supply startup time (0 $\%V_{CC} \rightarrow 90 \%V_{CC}$) should be more than 150 µs

* A 0.1 µF and a 10 µF bypass capacitor should be connected between V_{CC} and GND pins located close to the device

SEIKO EPSON CORPORATION

[4] Frequency Characteristics

4] Frequency Characteristics	Frequency Characteristics (Unless stated otherwise [3] Operating Ran							
Parameter	Symbol		Specification			Conditions		
Falailletei	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Output frequency *1	fo	25	-	500	MHz			
Frequency tolerance *2	f tol	-25	-	+25	×10 ⁻⁶	Suffix: D		
	f_tol	-50	-	+50	×10 ⁻⁶	Suffix: J		

*1 Please contact Epson for available frequencies

*2 Frequency tolerance includes Initial frequency tolerance, Frequency / temperature characteristics, Frequency / voltage coefficient and aging (10 years, +25 °C).

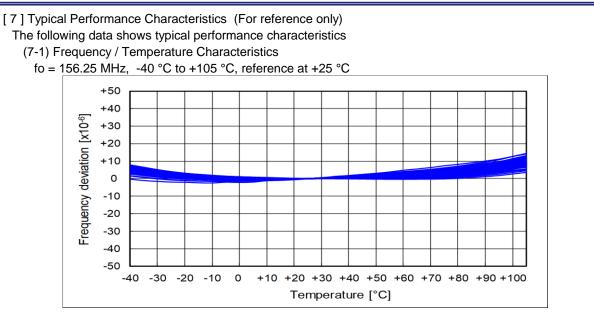
*Aging is estimated from environmental reliability tests; expected amount of the frequency variation. This does not intend to guarantee the product-life cycle.

Parameter	Cumphial	Specification			1.1		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Startup time	t_str	-	-	10	ms	t = 0 at 90 % V _{CC}	
Current consumption	I _{CC}	-	-	60	mA		
Disable current	I_dis	-	-	35	mA	OE = GND	
Stand-by current	L etd	-	-	30	μA	ST = GND, T_use Max. = +85 ℃	
Stand-by current	I_std	-	-	60	μA	<u>S</u> T = GND, T_use Max. = +105 °	
Rise time / Fall time	tr / tf	-	-	0.35	ns	20 % - 80 % of (V _{OH} - V _{OL})	
Symmetry	SYM	45	50	55	%	At output crossing point	
	V _{OH}	V _{CC} - 1.1	-	-	V	DC level	
Output voltage	V _{OL}	-	-	V _{CC} - 1.5	V	DC level	
Differential swing	V _{SW}	800	-	2 000	mV		
Input voltage	V _{IH}	70 % V _{CC}	-	-	V	OE/ST terminal	
input voltage	VIL	-	-	30 % V _{CC}	V		
Output disable time (OE)	tstp_oe	-	-	100	ns	Measured from the time OE pin crosses 30 % V_{CC}	
Output disable time (ST)	tstp_st	-	-	100	ns	Measured from the time \overline{ST} pin crosses 30 % V _{CC}	
Output enable time (OE)	tsta_oe	-	-	500	ns	Measured from the time OE pin crosses 70 % V _{CC}	
Output enable time (ST)	tsta_st	-	-	10	ms	Measured from the time ST pin crosses 70 % V _{CC}	
		-	-	250	fs	fo < 100 MHz	
Phase jitter		-	-	90	fs	100 MHz ≤ fo ≤ 156 MHz	
Offset frequency fo < 50 MHz: 12 kHz to 5 MHz	t _{PJ}	-	-	70	fs	156 MHz < fo ≤ 212 MHz	
10 < 50 MHz. 12 kHz to 5 MHz fo ≥ 50 MHz: 12 kHz to 20 MHz		-	-	60	fs	212 MHz < fo ≤ 391 MHz	
		-	-	50	fs	fo > 391 MHz	

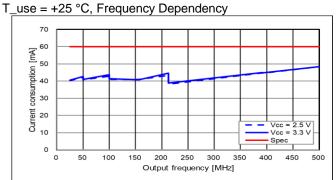
[6] Thermal resistance (For reference only)

Parameter	Symbol	Specification			Unit	Conditions
		Min.	Тур.	Max.	Onit	Conditions
Junction temperature	Tj	-	-	+140	°C	
Junction to case	Ѳјс	-	114	-	°C/W	SG2016EGN
		-	122	-	°C/W	SG2520EGN
Junction to ambient θia	Aio	-	243	-	°C/W	SG2016EGN
	θја	-	155	-	°C/W	SG2520EGN

n = 50 pcs

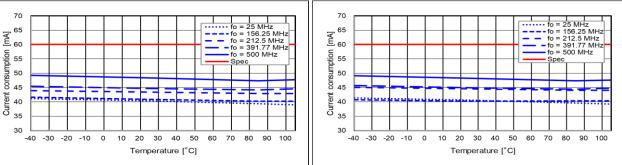


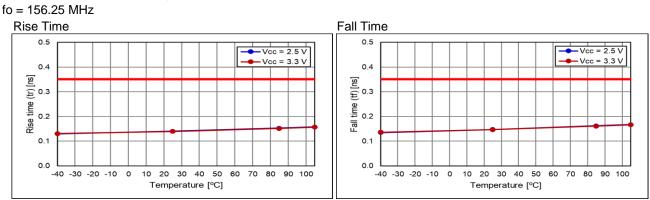
(7-2) Current Consumption





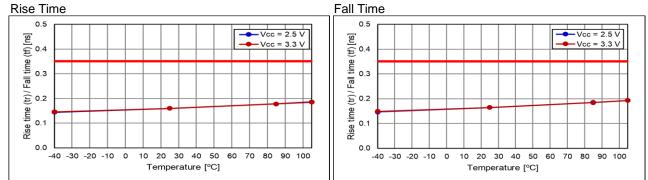
V_{CC} = 3.3 V, Temperature Characteristic



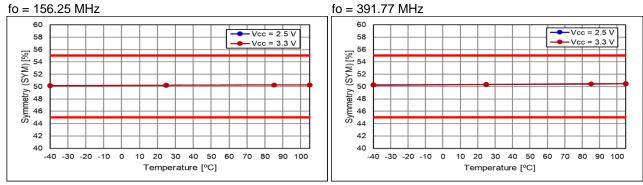


(7-3) Rise Time / Fall Time Temperature Characteristic

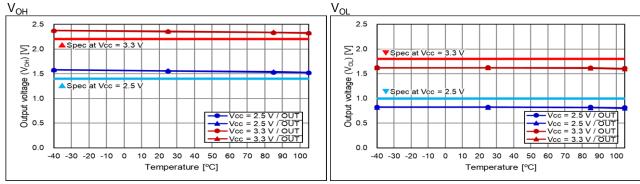
fo = 391.77 MHz

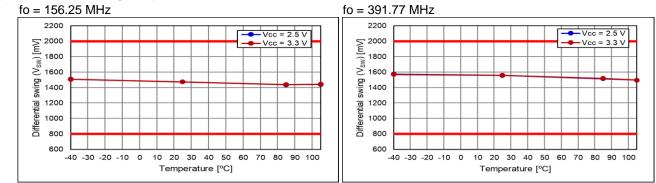


(7-4) Symmetry Temperature Characteristic



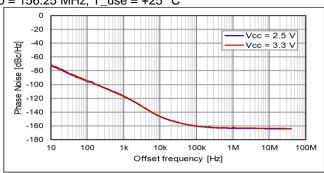






(7-6) Differential Swing Temperature Characteristic

(7-7) Phase Noise and Phase Jitter fo = 156.25 MHz, T_use = +25 °C

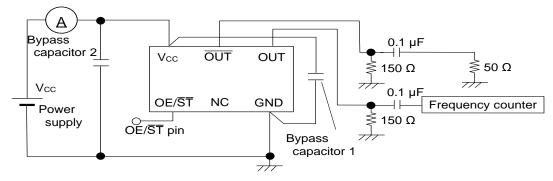


V _{CC}	Phase Jitter*
2.5 V	42 fs
3.3 V	44 fs

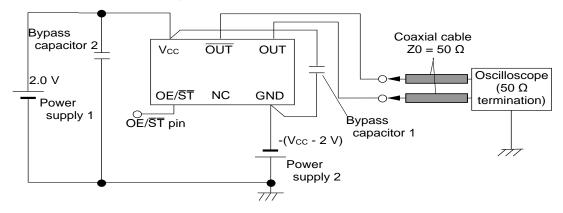
* Offset frequency: 12 kHz to 20 MHz

[8] Test Circuit

(8-1) Output Frequency and Current Consumption Test Setup



- * To measure Disable current or Stand-by current, OE/ST terminal is connected to GND
- (8-2) Waveform Observation Test Setup



* Each output trace should be same length

- (8-3) Conditions
 - (1) Oscilloscope

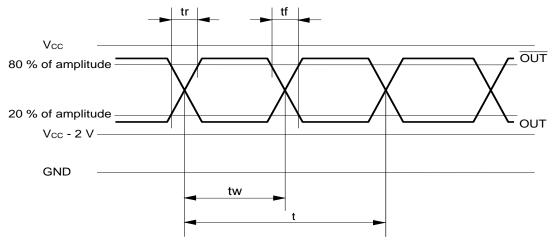
The bandwidth should be a minimum of 5 times the measurement frequency

- (2) A 0.1 μF and a 10 μF bypass capacitor should be connected between V_{CC} and GND pins located close to the device
- (3) Use a current meter with a low internal impedance
- (4) Power Supply

Power supply startup time (0 % $V_{CC}{\rightarrow}90$ % $V_{CC})$ should be more than 150 μs Power supply impedance should be as low as possible

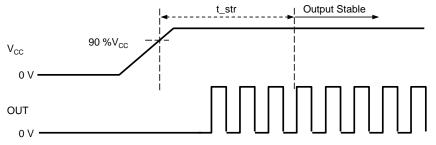
(8-4) Timing Chart

(1) Output Waveform and Level

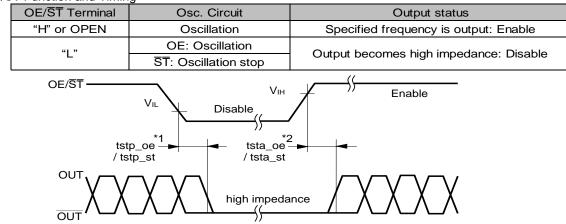


SYM = tw / t x 100 [%]

(2) Output Frequency Timing

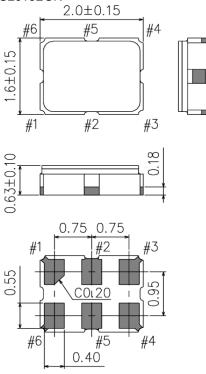


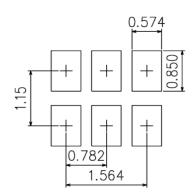
(3) OE/ST Function and Timing



- *1 The period from $OE/\overline{ST} = V_{IL}$ to OUT = High impedance (Disable)
- *2 The period from $OE/\overline{ST} = V_{IH}$ to OUT = Enable
- * OE/ST terminal voltage level should not exceed supply voltage when using OE/ST function. Please note that OE/ST rise time should not exceed supply voltage rise time at the start-up.

[9] Outline Drawing and Recommended Footprint (9-1) SG2016EGN





For stable operation, it is recommended that $0.1 \ \mu F$ and $10 \ \mu F$ bypass capacitors should be connected between V_{CC} and GND and placed as close to the V_{CC} pin as possible.

Terminal coating: Au plating

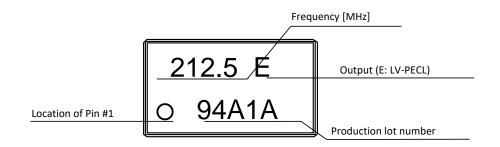
Reference Weight Typ.: 7.6 mg

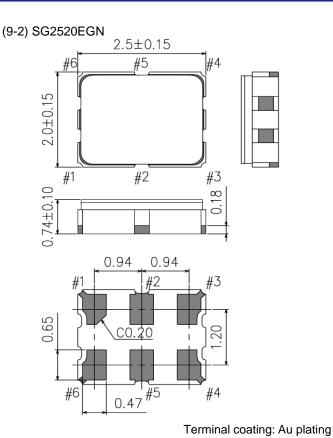
•	Terminal A	ssignment			
	Pin #	Connection			
			OE/ST te	erminal / active high	
				OE/ST Terminal	

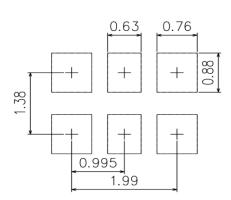
		OE/ST te	OE/ST terminal / active high					
			OE/ST Terminal	Osc. Circuit	Output status			
#1	OE/ST		"H" or OPEN	Oscillation	Specified frequency is output: Enable			
			"_"	OE: Oscillation	Output becomes high impedance: Disable			
			L	ST: Oscillation stop	Output becomes high impedance. Disable			
#2	NC	—	-					
#3	GND	GND terminal						
#4	OUT	Output terminal (Positive)						
#5	ŌŪŢ	Output terminal (Negative)						
#6	V _{cc}	V _{cc} terminal						

Function

Marking







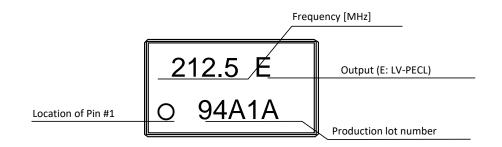
Units: mm

For stable operation, it is recommended that 0.1 μF and 10 μF bypass capacitors should be connected between V_{CC} and GND and placed as close to the V_{CC} pin as possible.

Reference Weight Typ.: 11.8 mg

Terminal A	Assignment					
Pin #	Connection	Function				
		OE/ST te	DE/ST terminal / active high			
			OE/ST Terminal	Osc. Circuit	Output status	
#1	OE/ST		"H" or OPEN	Oscillation	Specified frequency is output: Enable	
			"["	OE: Oscillation	Output becomes high impedance: Disable	
			L	ST: Oscillation stop		
#2	NC	—	_			
#3	GND	GND terr	GND terminal			
#4	OUT	Output te	Output terminal (Positive)			
#5	ŌŪŦ	Output te	Output terminal (Negative)			
#6	V _{cc}	V _{CC} term	V _{CC} terminal			

Marking

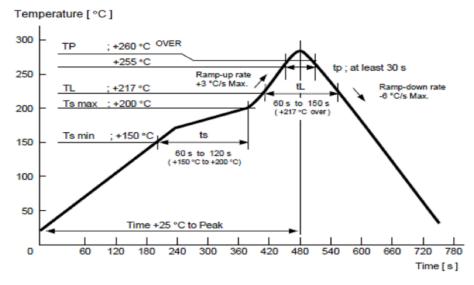


[10] Moisture Sensitivity Level

Parameter	Specification	Conditions
MSL	LEVEL 1	IPC/JEDEC J-STD-020D.1

[11] Reflow Profile

IPC/JEDEC J-STD-020D.1



[12] Packing Information

- (12-1) SG2016EGN
 - (1) Packing Quantity

The last two digits of the Product Number (X1G006131<u>xx</u>) are a code that defines the packing quantity. The standard is "15" for a 2 000 pcs/Reel.

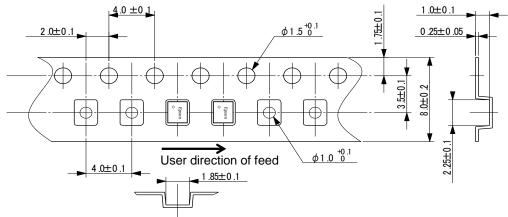
(2) Taping Specification

Subject to EIA-481, IEC-60286 and JIS C0806

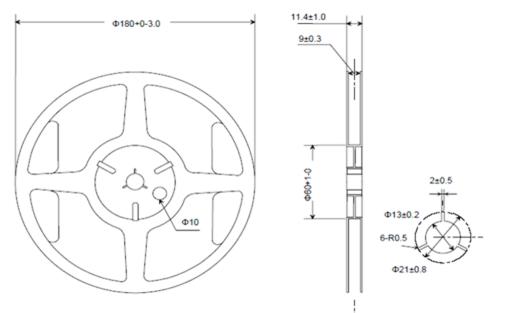
1) Tape Dimensions

Carrier Tape Material: Black conductive PS (Polystyrene)

Top Tape Material: Antistatic PET (Polyethylene Terephthalate) + PE (Polyethylene)



2) Reel Dimensions Reel Material: Black conductive PS (Polystyrene)



* The window shape of reel is a reference example

3) Storage Environment

We recommend to keep at normal temperature and normal humidity in a packed condition.

Units: mm

Units: mm

(12-2) SG2520EGN

(1) Packing Quantity

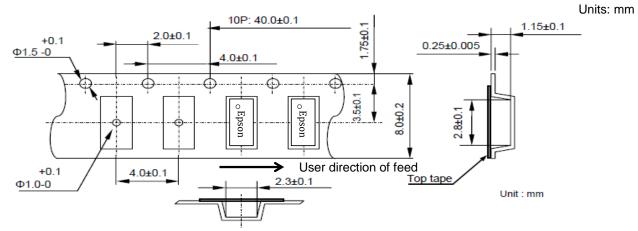
The last two digits of the Product Number (X1G005881xxxx \underline{xx}) are a code that defines the packing quantity. The standard is "15" for a 2 000 pcs/Reel.

(2) Taping Specification

Subject to EIA-481, IEC-60286 and JIS C0806

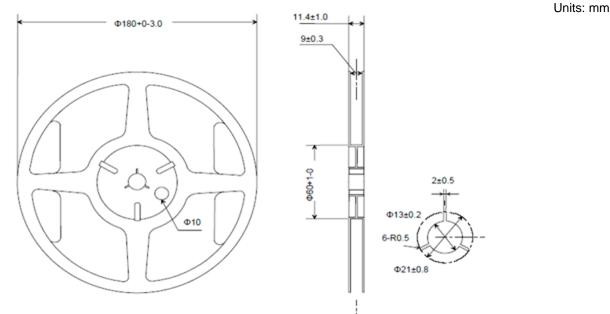
- 1) Tape Dimensions
 - Carrier Tape Material: Black conductive PS (Polystyrene)

Top Tape Material: Antistatic PET (Polyethylene Terephthalate) + PE (Polyethylene)



2) Reel Dimensions

Reel Material: Black conductive PS (Polystyrene)



* The window shape of reel is a reference example

3) Storage Environment

We recommend to keep at normal temperature and normal humidity in a packed condition.

[13] Handling Precautions

Prior to using this product, please carefully read the section entitled "Precautions" on our Web site (https://www5.epsondevice.com/en/information/#precaution) for instructions on how to handle and use the product properly to ensure optimal performance of the product in your equipment. Before using the product under any conditions other than those specified therein,

please consult with us to verify and confirm that the performance of the product will not be negatively affected by use under such conditions.

In addition to the foregoing precautions, in order to avoid the deteriorating performance of the product, we strongly recommend that you DO NOT use the product under ANY of the following conditions:

- (1) Do not expose this product to excessive mechanical shock or vibration.
- (2) This product can be damaged by mechanical shock during the soldering process depending on the equipment used, process conditions, and any impact forces experienced. Always follow appropriate procedures, particularly when changing the assembly process in any way and be sure to follow applicable process qualification standards before starting production.
- (3) These devices are sensitive to ESD, use appropriate precautions during handling, assembly, test, shipment, and installation.
- (4) The use of ultrasonic technology for cleaning, bonding, etc. can damage the Xtal unit inside this product.
- Please carefully check for this consideration before using ultrasonic equipment for volume production with this product.(5) Noise and ripple on the power supply may have undesirable affects on operation and cause degradation of phase noise
- characteristics. Evaluate the operation of this device with appropriate power supplies carefully before use.
 (6) When applying power, ensure that the supply voltage increases monotonically for proper operation.
 On power down, do not reapply power until the supplies, bypass capacitors, and any bulk capacitors are complete
- On power down, do not reapply power until the supplies, bypass capacitors, and any bulk capacitors are completely discharged since that may cause the unit to malfunction.
- (7) Aging specifications are estimated from environmental reliability tests and expected frequency variation over time. They do not provide a guarantee of aging over the product lifecycle.
- (8) The metal cap on top of the device is directly connected to the GND terminal (pin #2). Take necessary precautions to prevent any conductor not at ground potential from contacting the cap as that could cause a short circuit to GND.
- (9) Do not route any signal lines, supply voltage lines, or GND lines underneath the area where the oscillators are mounted including any internal layers and on the opposite side of the PCB. To avoid any issues due to interference of other signal lines, please take care not to place signal lines near the product as this may have an adverse affect on the performance of the product.
- (10) A bypass capacitor of the recommended value(s) must be connected between the V_{CC} and GND terminals of the product. Whenever possible, mount the capacitor(s) on the same side of the PCB and as close to the product as possible to keep the routing traces short.
- (11) Power supply connections to V_{CC} and GND pins should be routed as thick as possible while keeping the high frequency impedance low in order to get the best performance.
- (12) The use of a filter or similar element in series with the power supply connections to protect from electromagnetic radiation noise may increase the high frequency impedance of the power supply line and may cause the oscillator to not operate properly. Please verify the design to ensure sufficient operational margin prior to use.
- (13) Keep PCB routing from the output terminal(s) to the load as short as possible for best performance.
- (14) The Enable (OE or \overline{ST}) input terminal is high impedance and so susceptible to noise. Connect it to a low impedance source when used and when not used it is recommended to connect it to Vcc for active high inputs and GND for active low inputs.
- (15) Do not short the output to GND as that will damage the product. Always use with an appropriate load resistor connected.
- (16) This product should be reflowed no more than 3 times.
 If rework is needed after reflow, please correct it with a soldering iron with the tip set for a temperature of +350 °C or less and only contact each terminal once and for no more than 5 seconds.
 If this product is mounted on the bottom of the board during a reflow please check that it soldered down properly afterwards.

[Availability of mounting con	iditions]
Reflow on the board	Avallable
Reflow under the board	The parts may fall. Please judge whether it is possible to implement.
Soldering pot/bath (Dip soldering system, Flow soldering system)	Not Avallable
Soldering iron	Avallable

- (17) Product failures during the warranty period only apply when the product is used according to the recommended operating conditions described in the specifications. Products that have been opened for analysis or damaged will not be covered. It is recommended to store and use in normal temperature and humidity environments described in the specifications to ensure frequency accuracy and prevent moisture condensation. If the product is stored for more than one year, please confirm the pin solderability prior to use.
- (18) If the oscillation circuit is exposed to condensation, the frequency may change or oscillation may stop. Do not use in any conditions where condensation occurs.
- (19) Do not store or use the product in an environment where it can be exposed to chemical substances that are corrosive to metal or plastics such as salt water, organic solvents, chemical gasses, etc. Do not use the product when it is exposed to sunlight, dust, corrosive gasses, or other materials for long periods of time.
- (20) When using water-soluble solder flux make sure to completely remove the flux residue after soldering. Pay particular attention when the residues contain active halogens which will negatively affect the product and its performance.
- (21) Terminals on the side of the product are internally connected to the IC, be careful not to cause short-circuits or reduce the insulation resistance of them in any way.
- (22) Should any customer use the product in any manner contrary to the precautions and/or advice herein, such use shall be done at the customer's own risk.

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard. All of our major manufacturing and non-manufacturing sites, in

Japan and overseas, completed the acquisition of ISO 14001 certification.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major manufacturers as standard.

Explanation of marks used in this datasheet

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

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RoHS	Complies with EU RoHS directive. *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive (Contains Pb in sealing glass, high melting temperature type solder or other)

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