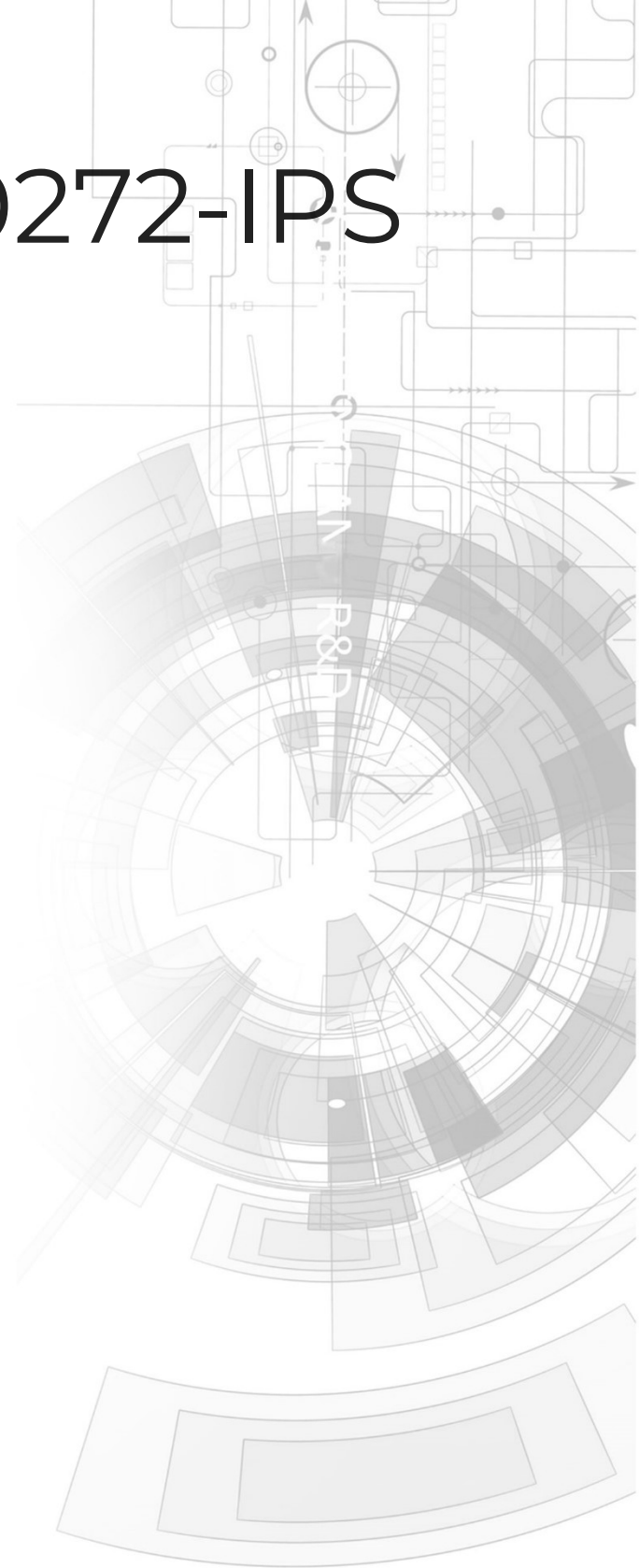


# 4DLCD-43480272-IPS Series

4DLCD-43480272-IPS  
4DLCD-43480272-IPS-RTP  
4DLCD-43480272-IPS-CTP  
4DLCD-43480272-IPS-CTP-CLB



## Datasheet

Revision 1.4

Copyright © 2023 4D Systems

Content may change at any time. Please refer to the resource centre for latest documentation.

# Contents

---

|   |    |
|---|----|
| 1. General Specification  | 3  |
| 2. TFT LCD Display Drawing (Non-Touch Version)                              | 5  |
| 3. TFT LCD Display Drawing (Resistive Touch Version)                        | 6  |
| 4. TFT LCD Display Drawing (Capacitive Touch Version)                       | 7  |
| 5. TFT LCD Display Drawing (Capacitive Touch Version with Cover Lens Bezel) | 8  |
| 6. Absolute Maximum Ratings   | 9  |
| 7. Electrical Characteristics   | 9  |
| 8. Electro-Optical Characteristics  | 10 |
| 9. Backlight Characteristics  | 11 |
| 10. Interface Descriptions  | 13 |
| 10.1. LCD Interface   | 13 |
| 10.2. CTP Interface   | 16 |
| 11. Backlight Example Circuit   | 17 |
| 12. LCD Timing Details  | 18 |
| 12.1. Timing Chart  | 18 |
| 12.2. Timing Characteristic   | 20 |
| 12.3. SYNC Mode Timing Diagram  | 20 |
| 12.4. SYNC-DE Mode Timing Diagram   | 21 |
| 12.5. Reset Timing  | 21 |
| 12.6. Power On Sequence   | 22 |
| 12.7. Power-off Sequence  | 22 |
| 13. Reliability Test  | 23 |
| 14. Precautions for Using LCD Modules                                       | 24 |
| 14.1. Handling Precautions  | 24 |
| 14.2. Storage Precautions   | 26 |
| 15. Revision History  | 27 |

# 1. General Specification

4DLCD-43480272-IPS is a colour active matrix LCD module incorporating amorphous silicon TFT IPS (Thin Film Transistor). It is composed of a colour TFT-LCD panel, driver IC, FPC and a backlight unit with/without a Resistive/Capacitive Touch Panel (RTP or CTP), and with/without Cover Lens Bezel (CLB). The module display area contains 480 x 272 pixels. This product accords with RoHS environmental criteria.

## Part Number Details:

4DLCD - 4D Systems LCD Display

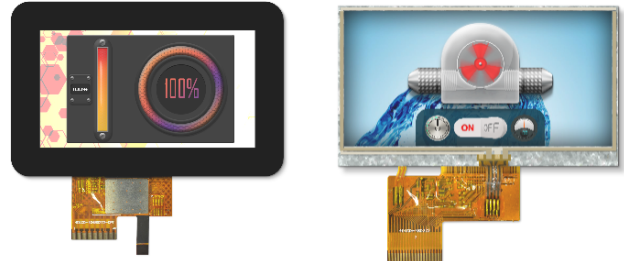
43480272 - 4.3-inch, 480 x 272  
Resolution

IPS - In-Plane Switching

RTP - Resistive Touch

CTP - Capacitive Touch

CLB - Cover Lens Bezel

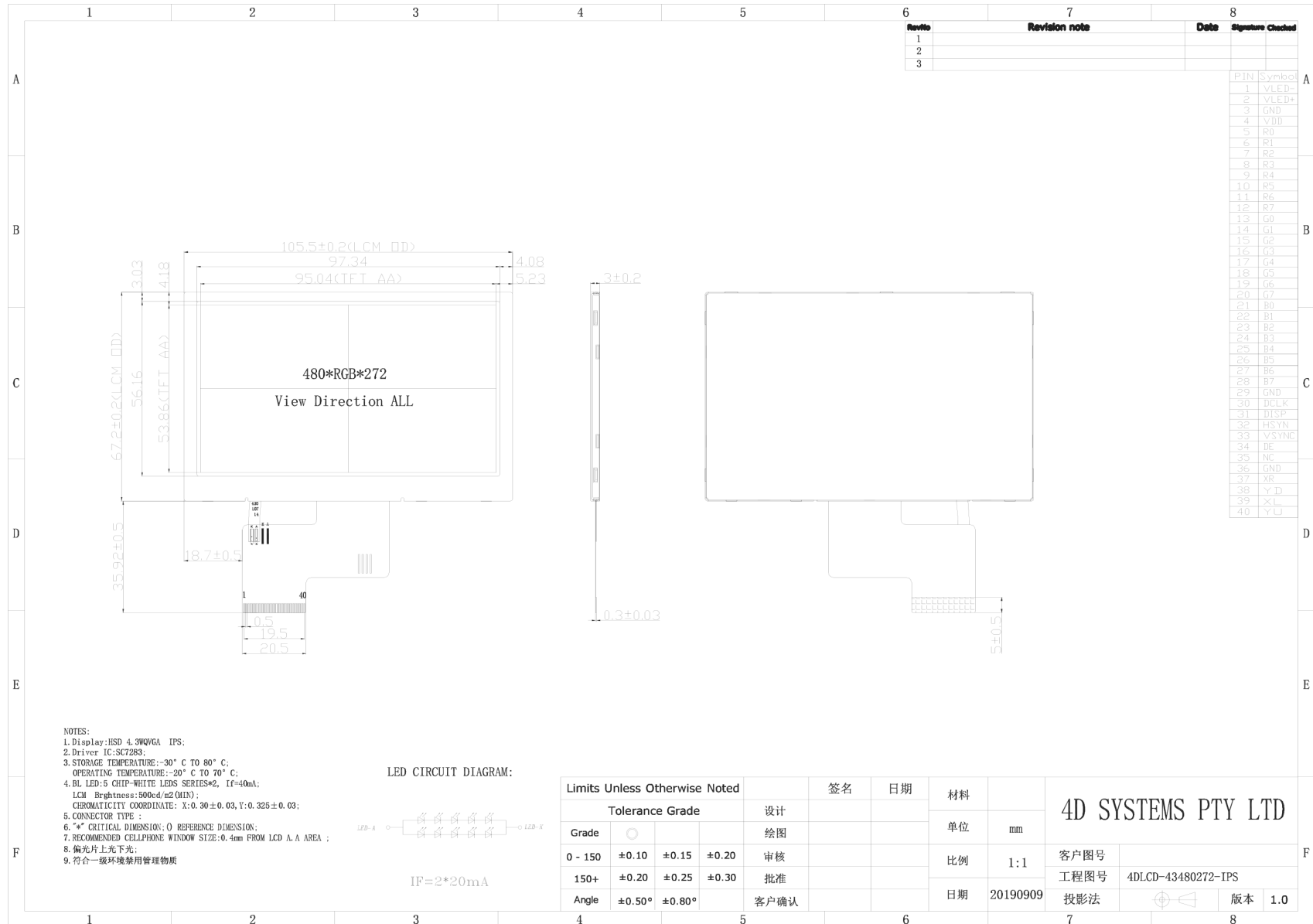


### Note

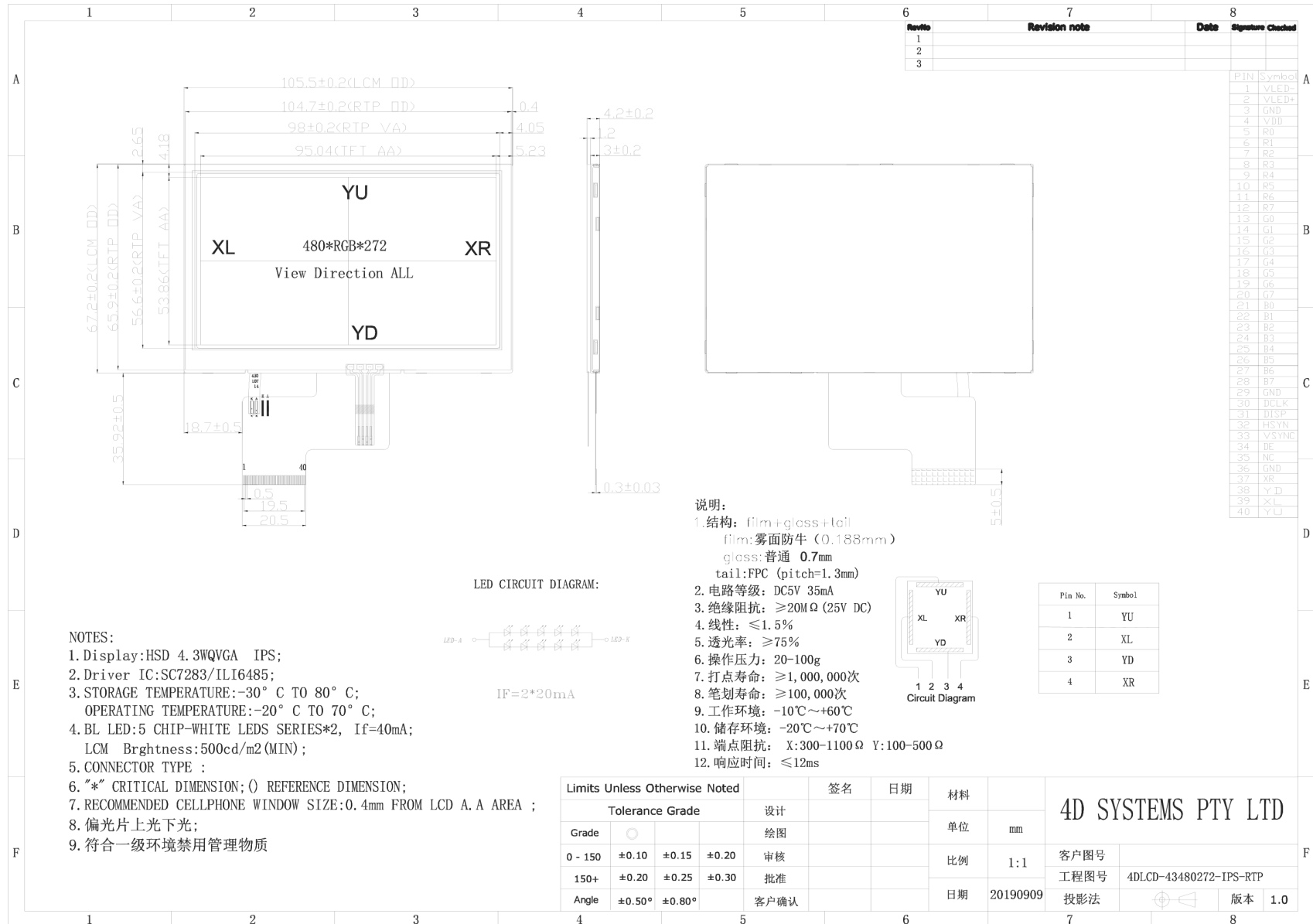
- RoHS compliant
- LCD weight tolerance:  $\pm 5\%$ .

| ITEM                          | CONTENTS  | UNIT  |
|-------------------------------|---|-------|
| LCD Type                      | TFT / Transmissive / IPS  |       |
| Size                          | 4.3   | Inch  |
| Viewing Direction             | ALL   |       |
| Display Mode                  | Normally Black  |       |
| LCD (W × H × T)               | 4DLCD-43480272-IPS: 105.50 × 67.20 × 2.90   | mm    |
|                               | 4DLCD-43480272-IPS-RTP: 105.50 × 67.20 × 4.10   |       |
|                               | 4DLCD-43480272-IPS-CTP: 105.50 × 67.20 × 4.42   |       |
|                               | 4DLCD-43480272-IPS-CTP-CLB: 123.04 × 84.46 × 4.42 (Including CLB)   |       |
| Active Area (W × H)           | 95.04 × 53.856  | mm    |
| Dot Pitch (W × H)             | 0.198 × 0.198   | mm    |
| Number of Dots (Pixels)       | 480 (RGB) × 272   |       |
| Driver IC                     | Source: SC7283  |       |
| Backlight Type                | 10 LEDs   |       |
| Surface Luminance             | 4DLCD-43480272-IPS: 600 (typical)   | cd/m2 |
|                               | 4DLCD-43480272-IPS-RTP: 510 (typical)   |       |
|                               | 4DLCD-43480272-IPS-CTP: 540 (typical)   |       |
|                               | 4DLCD-43480272-IPS-CTP-CLB: 540(typical)  |       |
| Interface Type                | Parallel RGB 16/24-bit  |       |
| Color Depth                   | 16.7M   |       |
| Pixel Arrangement             | RGB Vertical Stripe   |       |
| Surface Treatment             | AG  |       |
| Input Voltage                 | 3.3 (typical)   | V     |
| With/Without TP (Touch Panel) | 4DLCD-43480272-IPS – Without TP<br>4DLCD-43480272-IPS-RTP – With Resistive Touch<br>4DLCD-43480272-IPS-CTP – With Capacitive Touch<br>4DLCD-43480272-IPS-CTP-CLB – With Capacitive Touch and Cover Lens Bezel |       |
| Weight                        | 4DLCD-43480272-IPS: 48.0  | g     |
|                               | 4DLCD-43480272-IPS-RTP: 62.2  |       |
|                               | 4DLCD-43480272-IPS-CTP: 69.0  |       |
|                               | 4DLCD-43480272-IPS-CTP-CLB: 73.0  |       |

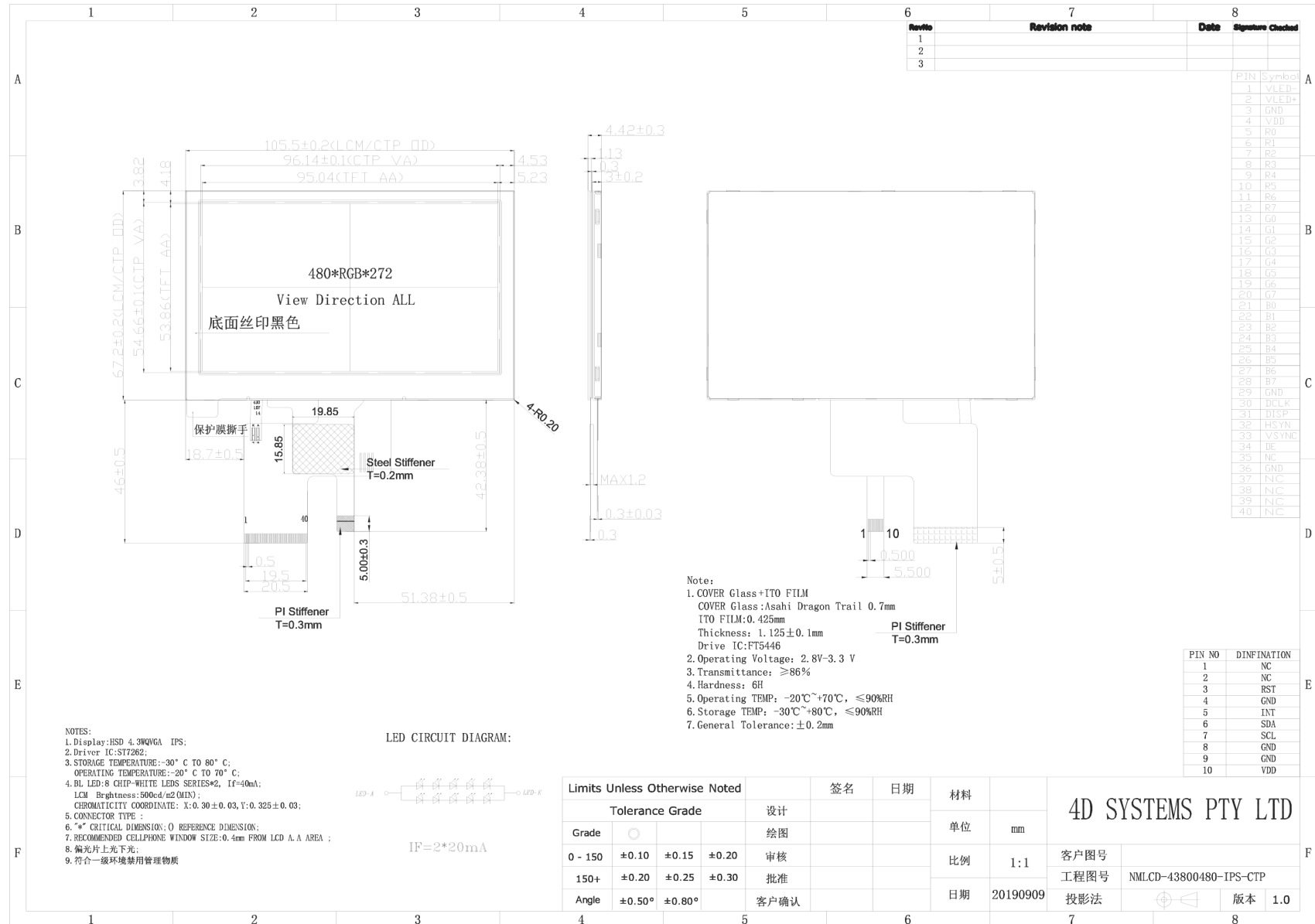
## 2. TFT LCD Display Drawing (Non-Touch Version)



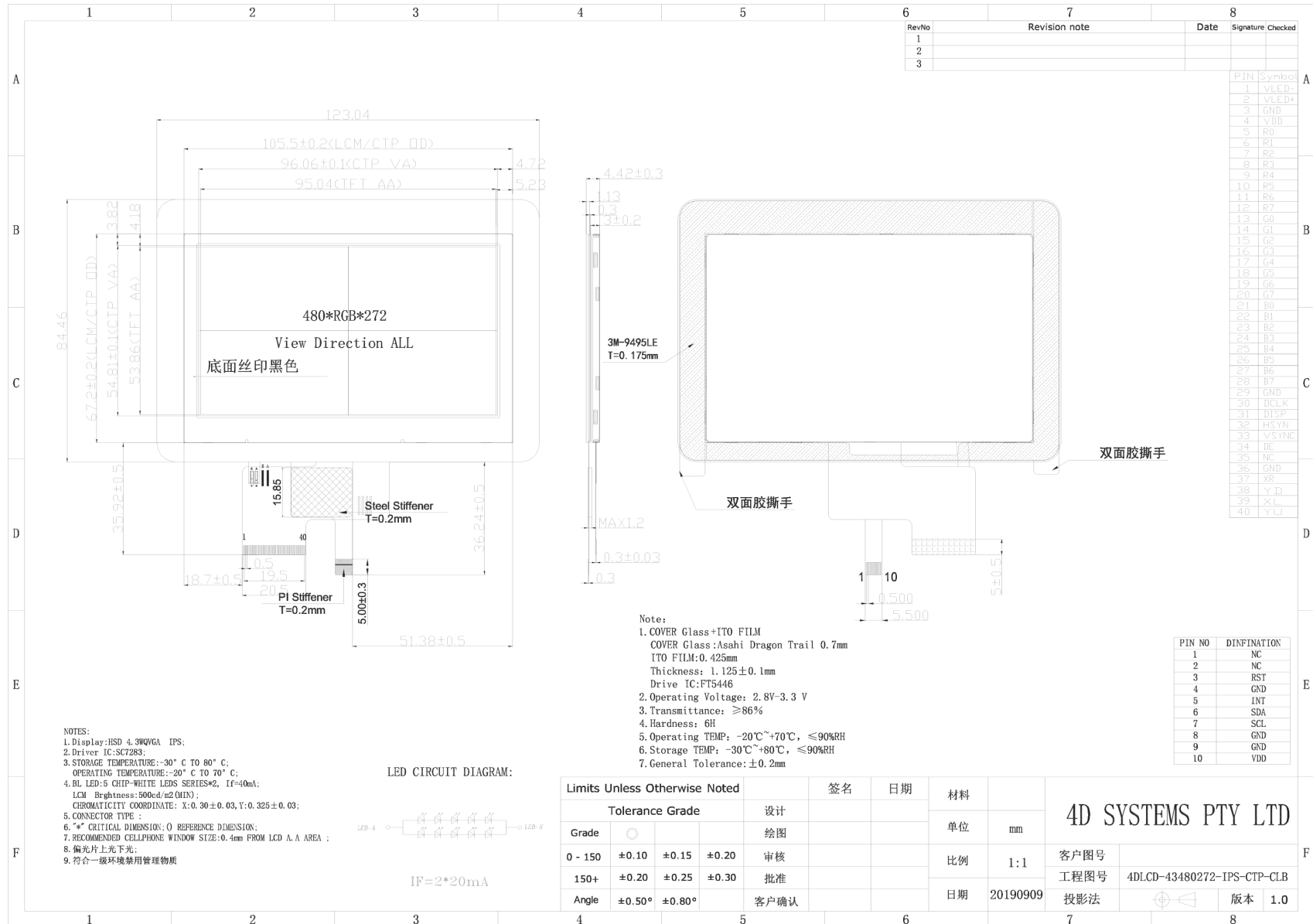
### 3. TFT LCD Display Drawing (Resistive Touch Version)



# 4. TFT LCD Display Drawing (Capacitive Touch Version)



# 5. TFT LCD Display Drawing (Capacitive Touch Version with Cover Lens Bezel)






## 6. Absolute Maximum Ratings

| Absolute Maximum Ratings       |             |         |               |      |  |
|--------------------------------|-------------|---------|---------------|------|--|
| PARAMETER                      | SYMBOL      | MIN     | MAX           | UNIT |  |
| Supply Voltage for LCD Logic   | VDD/VCC     | -0.3    | 4.0           | V    |  |
| Supply Voltage for TP Logic    | VDD/VCC-VSS | -       | 3.6           | V    |  |
| Input Voltage for Logic        | VIN         | VSS-0.5 | VDD           | V    |  |
| LED forward voltage (each LED) | IF          | -       | 25            | mA   |  |
| Operating Temperature          | TOP         | -20     | 70            | °C   |  |
| Storage Temperature            | TST         | -30     | 80            | °C   |  |
| Humidity                       | RH          | -       | 90% (Max60°C) | RH   |  |

## 7. Electrical Characteristics

| Electrical Characteristics |         |         |     |         |      |
|----------------------------|---------|---------|-----|---------|------|
| PARAMETER                  | SYMBOL  | MIN     | TYP | MAX     | UNIT |
| Power Voltage              | VDD/DCC | 2.6     | 3.3 | 3.6     | V    |
| Input Current              | IVDD    | -       | 13  | -       | mA   |
| Input Voltage 'H' Level    | VIH     | 0.7 VDD | -   | VDD     | V    |
| Input Voltage 'L' Level    | VIL     | 0       | -   | 0.3 VDD | V    |

## 8. Electro-Optical Characteristics

|  Electro-Optical Characteristics |                |                             |      |       |      |                   |            |
|---|----------------|-----------------------------|------|-------|------|-------------------|------------|
| ITEM  | SYM            | CONDITION                   | MIN  | TYP   | MAX  | UNIT              | REMARKS    |
| Response Time   | Tr+Tf          | $\theta=0$                  | -    | 30    | 40   | ms                | see figure |
| Contrast Ratio  | Cr             | $^{\circ}$                  | -    | 800   | -    | -                 | see figure |
| Luminance Uniformity  | $\delta$ WHITE | $\varnothing=0$             | -    | 80    | -    | %                 | see figure |
| Surface Luminance   | Lv             | 4DLCD-43480272              | -    | 600   | -    | cd/m <sup>2</sup> | see figure |
|   |                | 4DLCD-43480272-RTP          | -    | 510   | -    | cd/m <sup>2</sup> |            |
|   |                | 4DLCD-43480272-CTP          | -    | 540   | -    | cd/m <sup>2</sup> |            |
|   |                | 4DLCD-43480272-CTP-CLB      | -    | 540   | -    | cd/m <sup>2</sup> |            |
| Viewing Angle Range   | $\theta$       | $\varnothing = 90^{\circ}$  | 70   | 80    | -    | deg               | see figure |
|   |                | $\varnothing = 270^{\circ}$ | 70   | 80    | -    | deg               |            |
|   |                | $\varnothing = 0^{\circ}$   | 70   | 80    | -    | deg               |            |
|   |                | $\varnothing = 180^{\circ}$ | 70   | 80    | -    | deg               |            |
| CIE (x,y) Chromaticity  | Red: x         |                             |      | 0.629 |      |                   |            |
|   | Red: y         |                             |      | 0.326 |      |                   |            |
|   | Green: x       | $\theta=0^{\circ}$          |      | 0.337 |      |                   |            |
|   | Green: y       | $\varnothing=0^{\circ}$     | -0.1 | 0.546 | +0.1 |                   | see figure |
|   | Blue: x        | Ta=25                       |      | 0.136 |      |                   |            |
|   | Blue: y        |                             |      | 0.142 |      |                   |            |
|   | White: x       |                             |      | 0.320 |      |                   |            |
|   | White: y       |                             |      | 0.345 |      |                   |            |

## 9. Backlight Characteristics

| Backlight Characteristics                           |          |       |     |     |      |
|---|----------|-------|-----|-----|------|
| PARAMETER   | SYMBOL   | MIN   | TYP | MAX | UNIT |
| Voltage for LED backlight (Each LED)                | VI       | 3.0   | 3.2 | 3.4 | V    |
| Voltage for LED backlight (entire String – 10 LEDs) | VISTRING | 15    | 16  | 17  | mA   |
| Current for LED backlight (Each LED)                | II       | -     | 20  | 30  | mA   |
| Current for LED backlight (entire String – 10 LEDs) | IISTRING | -     | 20  | 30  | mA   |
| LED Lifetime (50% of original brightness)           | -        | 30000 | -   | -   | Hrs  |

### Note

The LED lifetime is defined as the module brightness decreasing to 50% original brightness at Ta=25°C.

1. Contrast Ratio(CR) is defined mathematically as below, for more information see [figure](#).

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

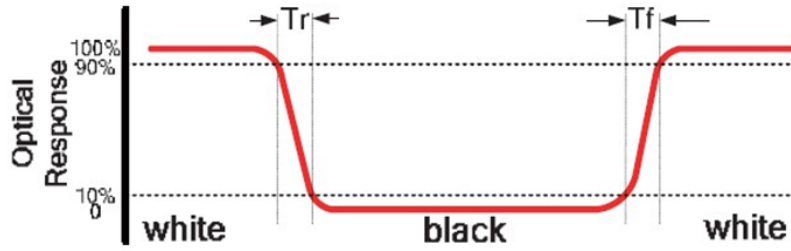
2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information, see [figure](#).

$$Lv = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

3. The uniformity in surface luminance  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by the minimum luminance of 5 points luminance. For more information, see [figure](#).

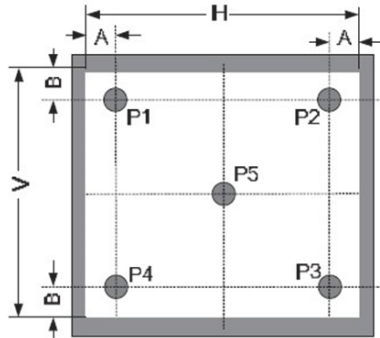
$$\delta^{\text{WHITE}} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see [figure](#). The test equipment is the Autronic-Melchers ConoScope series.
5. CIE (x, y) chromaticity, the x and y value is determined by measuring luminance at each test position 1 through 5, and then making the average value.
6. Viewing angle is the angle at which the contrast ratio is greater than 2. For the TFT module, the contrast ratio is greater than 10. The angles are determined for the horizontal or x-axis and the vertical or y-axis to the z-axis which is normal to the LCD surface. For more information, see [figure](#).
7. For viewing angle and response time testing, the testing data is based on the Autronic-Melchers ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, and CIE the test data is based on TOPCONS BM-5 photodetector.

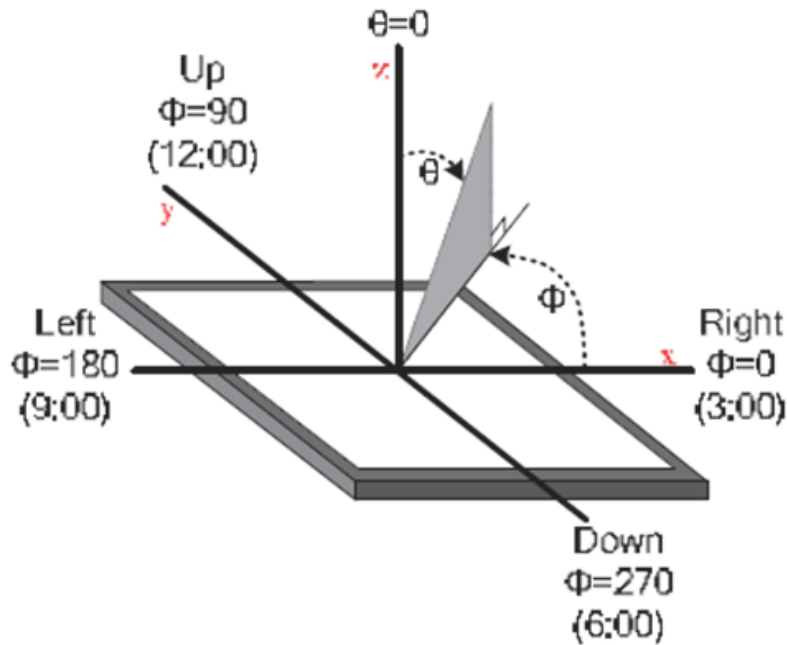


The definition of response time

A : 5 mm  
 B : 5 mm  
 H, V : Active Area  
 Light spot size  $\varnothing=5\text{mm}$ , 500mm distance from the LCD surface to detector lens  
 measurement instrument is TOPCON's luminance meter BM-5



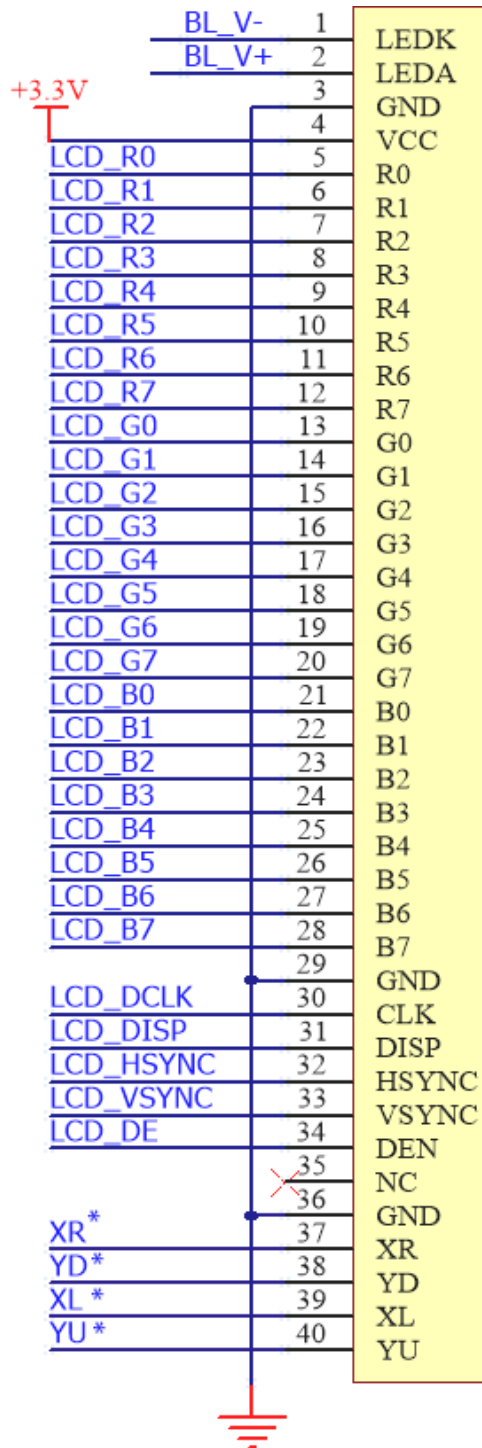
Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



The definition of viewing angle

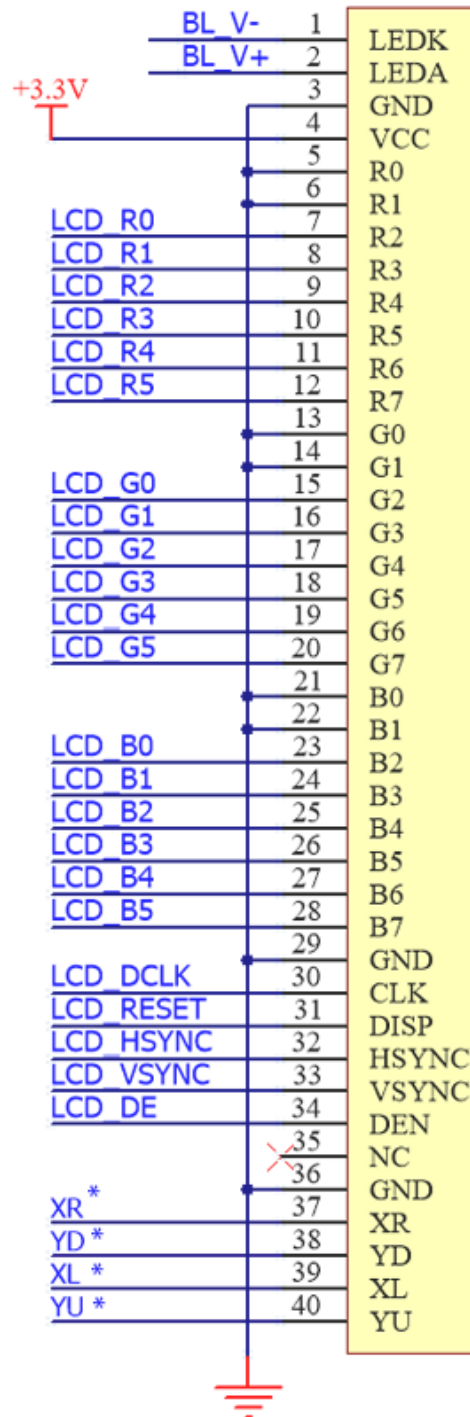
# 10. Interface Descriptions

## 10.1. LCD Interface



\* This has no connection (NC) for Non-touch displays

24 Bit mode



\* This has no connection (NC) for Non-touch displays

18 Bit mode



## LCD Interface

| PIN NO. | SYMBOL | DESCRIPTION              | REMARK |
|---------|--------|--------------------------|--------|
| 1       | LED-   | Cathode of LED Backlight |        |
| 2       | LED+   | Anode of LED Backlight   |        |
| 3       | GND    | Ground                   |        |
| 4       | DVDD   | Power supply             |        |
| 5       | R0     | Red data input R0        | Note 1 |
| 6       | R1     | Red data input R1        | Note 1 |
| 7       | R2     | Red data input R2        | Note 1 |
| 8       | R3     | Red data input R3        | Note 1 |
| 9       | R4     | Red data input R4        | Note 1 |
| 10      | R5     | Red data input R5        | Note 1 |
| 11      | R6     | Red data input R6        | Note 1 |
| 12      | R7     | Red data input R7        | Note 1 |
| 13      | G0     | Green data input G0      | Note 1 |
| 14      | G1     | Green data input G1      | Note 1 |
| 15      | G2     | Green data input G2      | Note 1 |
| 16      | G3     | Green data input G3      | Note 1 |
| 17      | G4     | Green data input G4      | Note 1 |
| 18      | G5     | Green data input G5      | Note 1 |
| 19      | G6     | Green data input G6      | Note 1 |
| 20      | G7     | Green data input G7      | Note 1 |
| 21      | B0     | Blue data input B0       | Note 1 |
| 22      | B1     | Blue data input B1       | Note 1 |
| 23      | B2     | Blue data input B2       | Note 1 |
| 24      | B3     | Blue data input B3       | Note 1 |
| 25      | B4     | Blue data input B4       | Note 1 |
| 26      | B5     | Blue data input B5       | Note 1 |
| 27      | B6     | Blue data input B6       | Note 1 |
| 28      | B7     | Blue data input B7       | Note 1 |

| PIN NO. | SYMBOL | DESCRIPTION  | REMARK |
|---------|--------|--|--------|
| 29      | GND    | Ground   |        |
| 30      | DCLK   | Clock for input data. Data latched rising/falling edge of this signal. Default is falling edge.  |        |
| 31      | DISP   | Standby mode control. (Normally pull high)<br>STBYB="L", enter standby mode for power saving. Timing controller source driver will turn off, all outputs are Hi-Z.<br>STBYB="H", normal operation. |        |
| 32      | HS     | Horizontal sync input  |        |
| 33      | VS     | Vertical sync input  |        |
| 34      | DE     | Input data enable control. When DE mode, active High to enable data input (Normally pull low)  |        |
| 35      | NC     | No Connection  |        |
| 36      | GND    | Ground   |        |
| 37      | XR/NC  | The touch panel X right pin (RTP only, NC for other touch types)   | Note 2 |
| 38      | YD/NC  | The touch panel Y down pin (RTP only, NC for other touch types)  | Note 2 |
| 39      | XL/NC  | The touch panel X left pin (RTP only, NC for other touch types)  | Note 2 |
| 40      | YU/NC  | The touch panel Y up pin (RTP only, NC for other touch types)  | Note 2 |

 **Note**

1. For applications that use less than 24 bits, pins are tied to the ground to reduce the total bits used.
2. Pins 37, 38, 39 and, 40 are only applicable to touchscreen displays (4DLCD-xxxxxxx-RTP).

## 10.2. CTP Interface

The Capacitive Touch is driven by a **Focaltech FT5446** capacitive touch driver IC, which utilizes an I2C interface, and is capable of 5-point touch.

| Capacitive Touch Interface |        |                           |  |
|----------------------------|--------|---------------------------|--|
| PIN NO.                    | SYMBOL | DESCRIPTION               | REMARK   |
| 1                          | NC     | No Connect                | Only connected to the CTP Panel, not connected to the LCD itself |
| 2                          | NC     | No Connect                |  |
| 3                          | RST    | Reset pin                 |  |
| 4                          | GND    | Ground                    |  |
| 5                          | INT    | Interrupt signal from CTP |  |
| 6                          | SDA    | I2C SDA                   |  |
| 7                          | SCL    | I2C SCL                   | N/A for Non-touch and RTP models.                                |
| 8                          | GND    | Ground                    |  |
| 9                          | GND    | Ground                    |  |
| 10                         | VDD    | Power Supply (3.3V)       |  |



## 11. Backlight Example Circuit

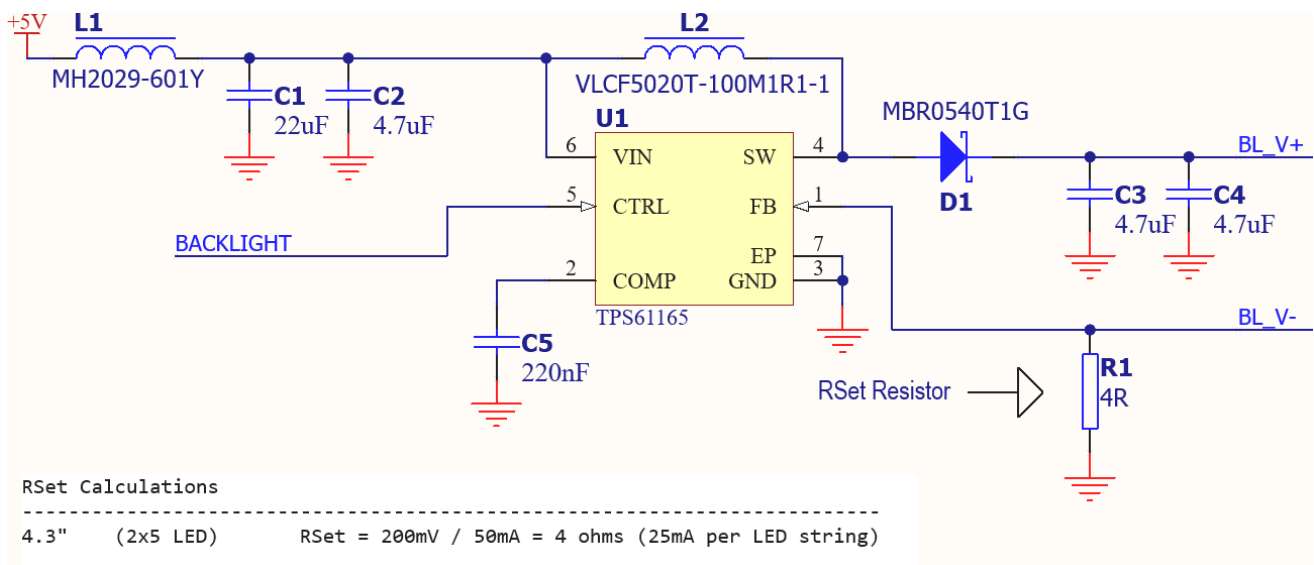
The backlight circuit of this LCD needs an appropriate backlight driver. It can not be simply driven directly by a 3.3V or 5.0V supply like smaller displays, this is because of the number of LEDs connected in series and parallel, which is known as an LED String. A backlight driver is required to boost the voltage from the input supply to the requirements for the LED string.

There are many backlight driver ICs on the market. Some examples are:

- Texas Instruments TPS61165
- Texas Instruments TPS61080
- On-Semi FAN5333B

On selecting the backlight driver, ensure it is capable of driving the number of LEDs in the string featured in this display, and it supports the input voltage you are looking to supply. Often 3.3V or 5V is possible to supply into the backlight drivers, and it will boost it to the required output based on the requirements of the leds. The current can then be set with a resistor. Please refer to the datasheet for the selected Driver IC of your choice.

In this example, the TPS61165 is depicted.

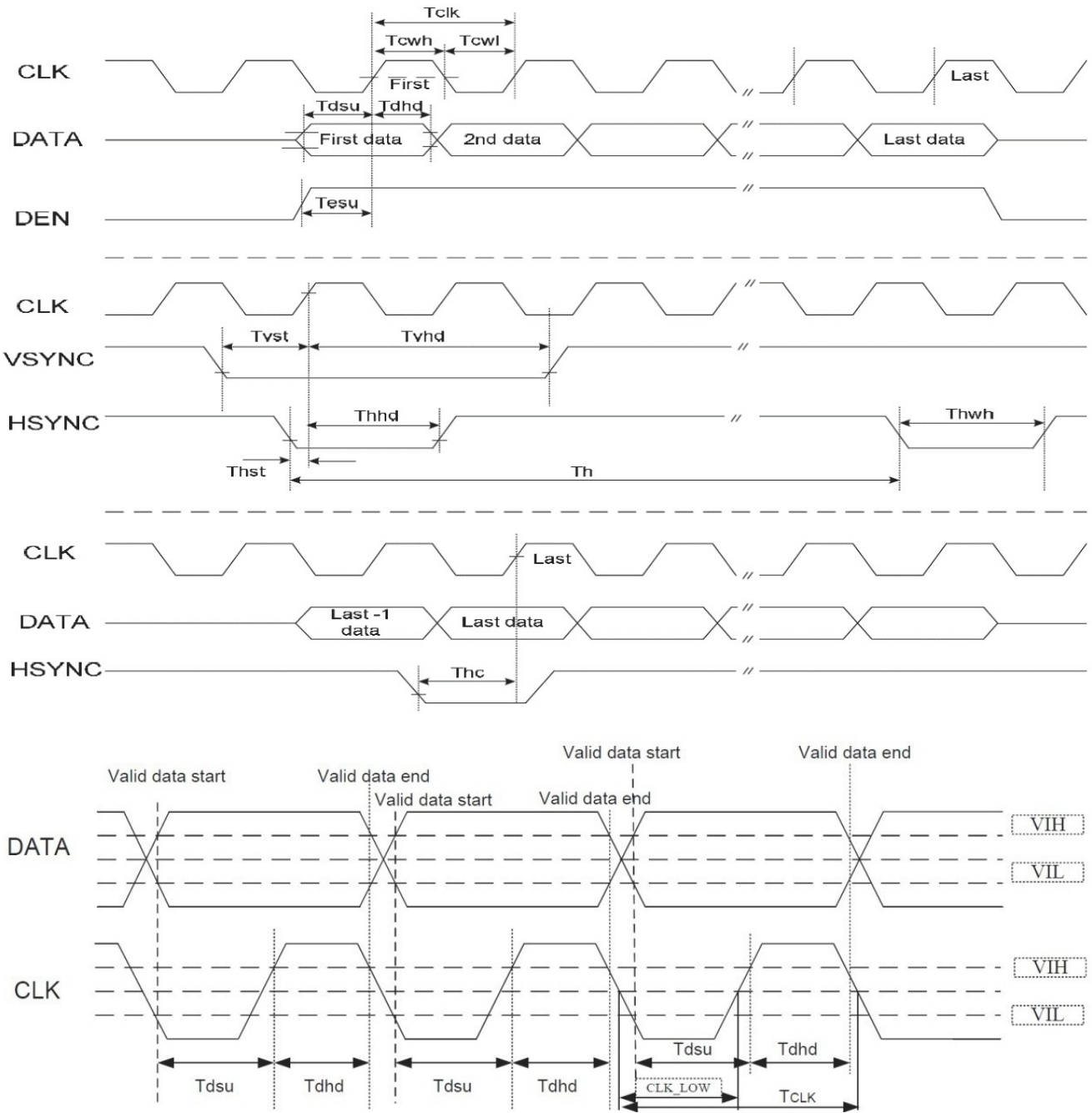


The RSet calculation is found in the Datasheet for the backlight driver. Each one will vary, as well as the circuit and the components required. Please refer to their datasheets.

Refer to the [Backlight Characteristics](#) section for information specific to the backlight requirements, as well as the drawing of the display to see the configuration of the backlight LED string.

## 12. LCD Timing Details

### 12.1. Timing Chart



**Note**

Timing parameter (VDD=3.3V, GND=0V, Ta=25 °C)

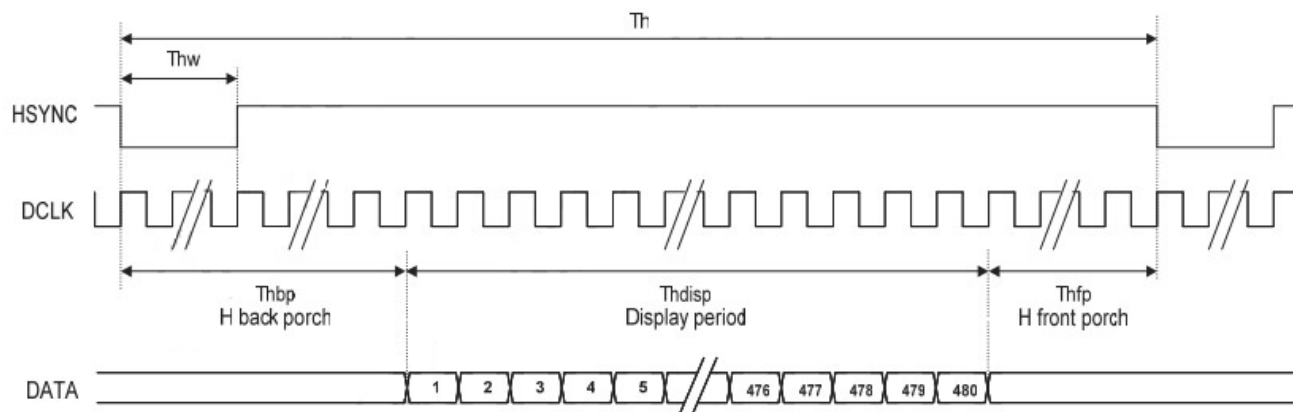
 **Timing Chart**

| PARAMETER         | SYMBOL | MIN         | TYP   | MAX         | UNIT | CONDITION      |
|-------------------|--------|-------------|-------|-------------|------|----------------|
| CLK Clock Time    | Tclk   | 1/Max(FCLK) | -     | 1/Min(FCLK) | ns   | -              |
| CLK Pulse Duty    | Tchw   | 40          | 50    | 60          | %    | TCLK           |
| HSYNC to CLK      | Thc    | -           | -     | 1           | CLK  | -              |
| HSYNC Width       | Thwh   | 1           | -     | -           | CLK  | -              |
| VSYNC Width       | Twwh   | 1           | -     | -           | ns   | -              |
| HSYNC Period Time | Th     | 60          | 63.56 | 67          | ns   | -              |
| VSYNC Set-up Time | Tvst   | 12          | -     | -           | ns   | -              |
| VSYNC Hold Time   | Tvhd   | 12          | -     | -           | ns   | -              |
| HSYNC Setup Time  | Thst   | 12          | -     | -           | ns   | -              |
| HSYNC Hold Time   | Thhd   | 12          | -     | -           | ns   | -              |
| Data Set-up Time  | Tdsu   | 12          | -     | -           | ns   | D00~D23 to CLK |
| Data Hold Time    | Tdhd   | 12          | -     | -           | ns   | D00~D23 to CLK |
| DEN Set-up Time   | Tesu   | 12          | -     | -           | ns   | DEN to CLK     |

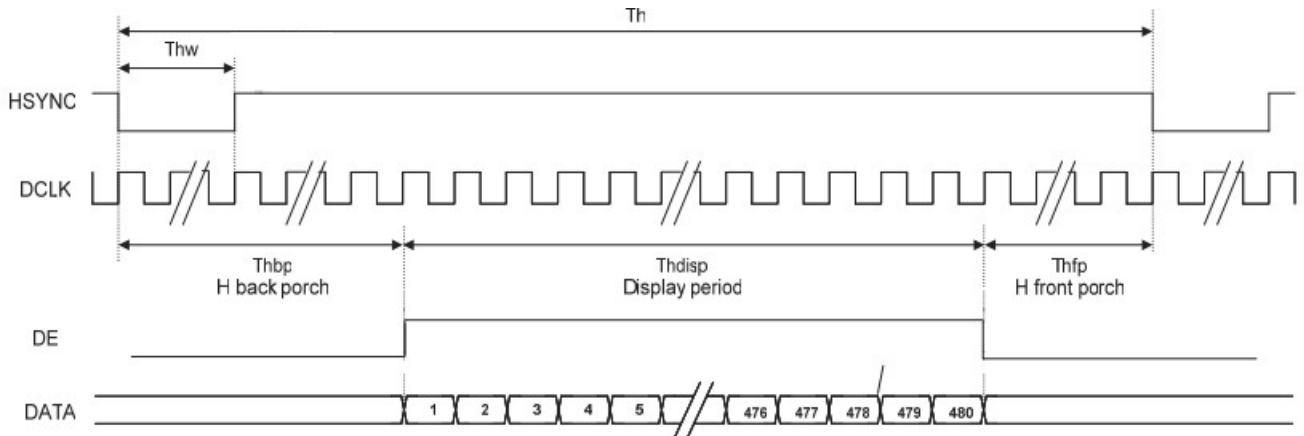
## 12.2. Timing Characteristic

| Timing Characteristic |                        |     |     |     |      |                       |
|-----------------------|------------------------|-----|-----|-----|------|-----------------------|
| ITEM                  | SYMBOL                 | MIN | TYP | MAX | UNIT |                       |
| DCLK Frequency        | Fclk                   | 8   | 9   | 12  | MHz  |                       |
| DCLK Period           | Tclk                   | 83  | 111 | 125 | Ns   |                       |
| Hsync                 | Period Time: Th        | 485 | 531 | 598 | DCLK |                       |
|                       | Display Period: Thdisp | -   | 480 | -   | DCLK | By H BLANKING setting |
|                       | Back Porch: Thbp       | 3   | 43  | -   | DCLK |                       |
|                       | Front Porch: Thfp      | 2   | 8   | 75  | DCLK |                       |
|                       | Pulse Width: Thw       | 2   | 4   | 43  | DCLK |                       |
| Vsync                 | Period Time: Tv        | 276 | 292 | 321 | H    |                       |
|                       | Display Period: Tvdisp | -   | 272 | -   | H    |                       |
|                       | Back Porch: Thbp       | 2   | 12  | 12  | H    | By V BLANKING setting |
|                       | Front Porch: Thfp      | 2   | 8   | 37  | H    |                       |
|                       | Pulse Width: Thw       | 2   | 4   | 12  | H    |                       |

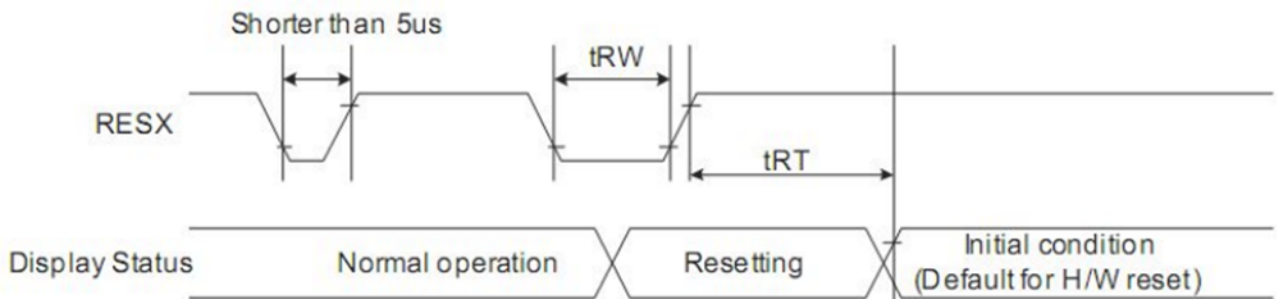
## 12.3. SYNC Mode Timing Diagram



### 12.4. SYNC-DE Mode Timing Diagram



### 12.5. Reset Timing

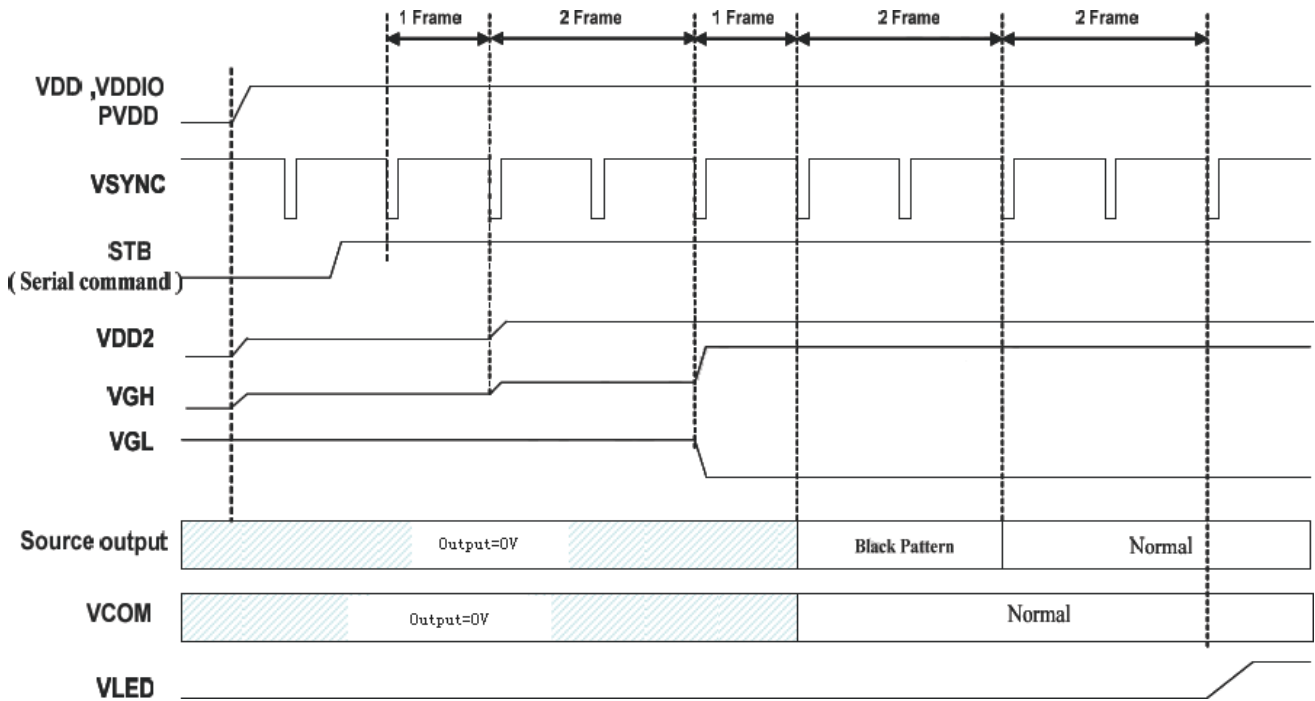


| Reset Timing |          |                       |     |             |      |
|--------------|----------|-----------------------|-----|-------------|------|
| SIGNAL       | SYMBOL   | PARAMETER             | MIN | MAX         | UNIT |
| RESET        | $t_{RW}$ | Reset low pulse width | 40  | -           | us   |
|              | $t_{RT}$ | Reset complete time   | -   | 5 (note1)   | ms   |
|              |          |                       | -   | 120 (note2) | ms   |

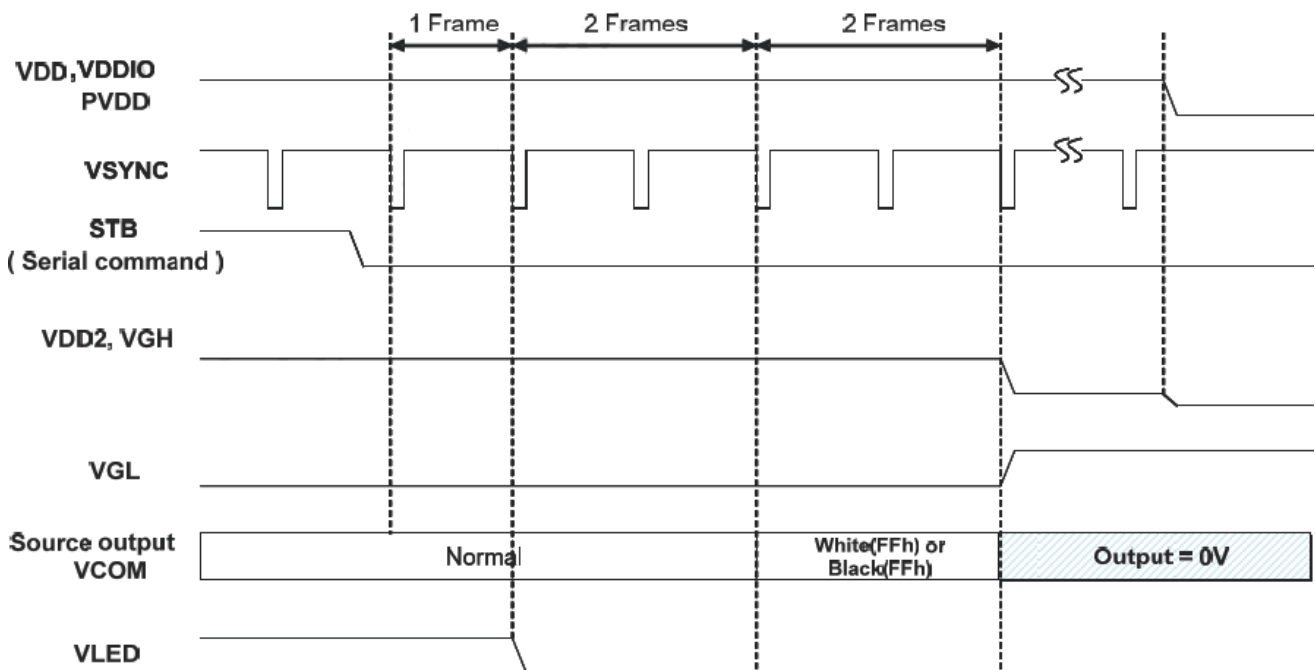
**Note**

- When reset applied during SLPIN mode
- When reset applied during SLPOUT mode.

### 12.6. Power On Sequence




### 12.7. Power-off Sequence



**Note**

When normally-black LC is used, please send a black pattern to discharge the panel.  
 When normally-white LC is used, please send a white pattern to discharge the panel.

## 13. Reliability Test

|  Reliability Test |                                       |   |   |
|--|---------------------------------------|---|---|
| No.  | SYMBOL                                | TEST CONDITION  | REMARK  |
| 1  | High Temperature Storage              | 80 °C±2 °C 96H<br>Restore 2H at 25 °C<br>Power off  |   |
| 2  | Low Temperature Storage               | -30 °C±2 °C 96H<br>Restore 2H at 25 °C<br>Power off   |   |
| 3  | High Temperature Operation            | 70 °C±2 °C 96H<br>Power on  |   |
| 4  | Low Temperature Operation             | -20 °C±2 °C 96H<br>Power on   |   |
| 5  | High Temperature & Humidity Operation | 60 °C±2 °C<br>90%RH 96H<br>Power on   | After test cosmetic and electrical defects should not happen. |
| 6  | Temperature Cycle                     | -20 °C↔25 °C↔70 °C<br>30min 5min 30min<br>After 10 cycles, restore 2H at 25 °C<br>Power off |   |
| 7  | Vibration Test                        | 10Hz~150Hz, 100m/s <sup>2</sup> , 120min  |   |
| 8  | Shock Test                            | Half-sinewave, 300m/s <sup>2</sup> , 11ms   |   |

### Note

The Displays are of the highest rated 'Grade A', which allows for 0-4 defective pixels. A defective pixel could be solid Black (Dead), White, Red, Green or Blue.

## 14. Precautions for Using LCD Modules

### 14.1. Handling Precautions

- The display panel is made of glass and a polarizer. The glass is fragile. It tends to be chipped during handling, especially on the edges. Please avoid dropping or jarring. Please be careful not subject it to a mechanical shock by dropping it on impact.
- If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any of it in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determined by the polarizer).
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizer with anything harder than an HB pencil lead (e.g., glass, tweezers, etc.). Do not put or attach anything to the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold temperatures will damage, stain or contaminate the polarizer. After products are tested at low temperatures they must be warmed up in a container before coming into contact with room-temperature air.
- If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten the cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol Do not scrub hard as it might damage the display surface.
- Solvents other than those mentioned above may damage the polarizer. Especially the following.
  - Water
  - Ketone
  - Aromatic solvents Wipe off saliva or water drops immediately, contact with water over a long period may cause deformation or color fading. Avoid contact with oil and fat.
- Take necessary precautions to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or current flow in a high-humidity environment.
- Install the LCD Module by using the mounting holes. When mounting the LCD module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- Do not attempt to disassemble or process the LCD module.
- NC terminal should be open. Do not connect anything to it.
- If the logic circuit power is off, do not apply input signals.



- 
- Control Electro-Static Discharge. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent the destruction of the elements by static electricity, ensure that an optimum work environment is maintained.
    - Before removing the LCM from its packing case or incorporating it into a set, be sure that the module and your body have the same electric potential. Be sure to ground your body when handling the LCD modules.
    - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity, please ensure that the air in the work environment is not too dry. Relative humidity of 50%-60% is recommended. As much as possible, make the electric potential of your work clothes and that of the workbench the ground potential.
    - The LCD module is coated with a film to protect the display surface. Be careful when peeling off this protective film since static electricity may be generated.
  - Since the LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
    - Do not alter, modify or change the shape of the tab on the metal frame.
    - Do not make extra holes on the printed circuit board, modify its shape or change the positions of the components to be attached.
    - Do not damage or modify the pattern writing on the printed circuit board.
    - Do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
    - Do not drop, bend or twist the LCM.


---

## 14.2. Storage Precautions

When storing the LCD modules, the following precautions are necessary.

- Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- The polarizer surface should not come in contact with any other objects. (We advise you to store them in an anti-static electricity container in which they were shipped. Some Liquid crystals solidify under low temperatures (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to low temperatures.
- If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from the destruction caused by static electricity etc., please avoid holding the following sections when handling the modules'
  - The exposed area of the printed circuit board
  - Terminal electrode sections

## 15. Revision History

|  Document Revision |            |   |
|---|------------|---|
| REVISION  | DATE       | COMMENT   |
| 1.0   | 10/09/2020 | Initial Version   |
| 1.1   | 22/02/2021 | Information clarification / improvement.<br>Updated LED voltage/current information |
| 1.2   | 09/06/2021 | Updating viewing angles and brightness values which were incorrect.                 |
| 1.3   | 24/06/2021 | Added logic current consumption data, added backlight circuit example.              |
| 1.4   | 20/01/2023 | Modified datasheet for web-based documentation                                      |