Taiwan Semiconductor

2A, 100V - 200V Ultra Fast Surface Mount Rectifier

FEATURES

TAIWAN

Glass passivated chip junction

EMICONDUCTOR

- Ideal for automated placement
- Low profile package
- Ultra Fast recovery time for high efficiency
- Moisture sensitivity level: level 1 per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- Converter

MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.090g (approximately)

UNIT	
А	
V	
А	
°C	
SMB)	
Single die	





DO-214AA (SMB)

Cathode Anode

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)					
PARAMETER	SYMBOL	ESH2B	ESH2C	ESH2D	UNIT
Marking code on the device		ESH2B	ESH2C	ESH2D	
Repetitive peak reverse voltage	V _{RRM}	100	150	200	V
Reverse voltage, total rms value	V _{R(RMS)}	70	105	140	V
Forward current	I _F	2		А	
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	60		А	
Junction temperature	TJ	- 55 to +175		°C	
Storage temperature	T _{STG}		- 55 to +175		°C





THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance	R _{θJL}	20	°C/W
Junction-to-ambient thermal resistance	R _{eJA}	75	°C/W

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	МАХ	UNIT
Forward voltage ⁽¹⁾	$I_{\rm F} = 2A, T_{\rm J} = 25^{\circ}{\rm C}$	V _F	-	0.9	V
Reverse current @ rated $V_R^{(2)}$	$T_J = 25^{\circ}C$	I _R	-	2	μA
	T _J = 125°C		-	50	μA
Junction capacitance	1MHz, V _R = 4.0V	CJ	25	-	pF
Reverse recovery time	$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t _{rr}	-	20	ns

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION	1	
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING
ESH2x	DO-214AA (SMB)	3,000 / Tape & Reel

Notes:

1. "x" defines voltage from 100V(ESH2B) to 200V(ESH2D)



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

 3

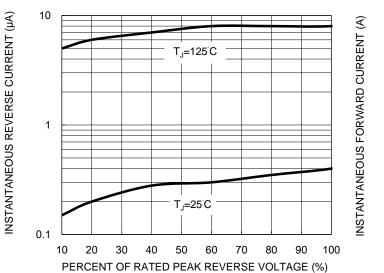
Fig.1 Forward Current Derating Curve

60 50 40 30 20 10 50mVp-p 0 0.1 1 10 100 REVERSE VOLTAGE (V)

Fig.2 Typical Junction Capacitance

Fig.3 Typical Reverse Characteristics

Fig.4 Typical Forward Characteristics



CAPACITANCE (pF)



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

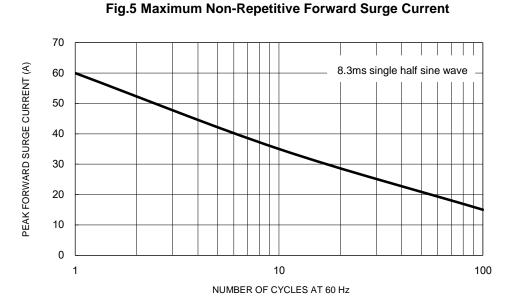
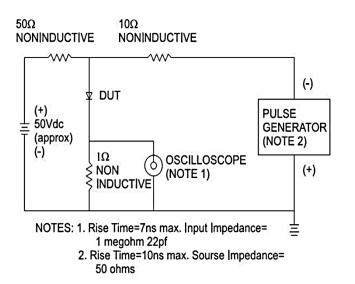
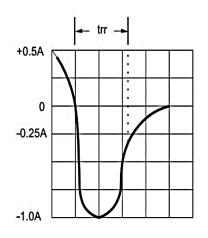


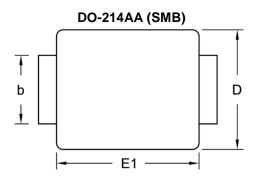
Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram

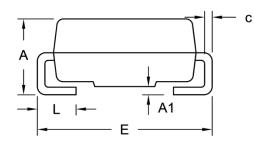






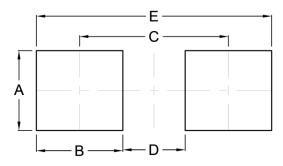
PACKAGE OUTLINE DIMENSIONS





DIM.	Unit (mm)		Unit	(inch)
	Min.	Max.	Min.	Max.
A	1.95	2.65	0.077	0.104
A1	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
с	0.15	0.31	0.006	0.012
D	3.30	3.95	0.130	0.156
E	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
L	0.75	1.60	0.030	0.063

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.30	0.091
В	2.50	0.098
С	4.30	0.169
D	1.80	0.071
E	6.80	0.268

MARKING DIAGRAM



P/N	= Marking Code
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G = Green Compound

YW = Date Code

F = Factory Code



ESH2B – ESH2D

Taiwan Semiconductor

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