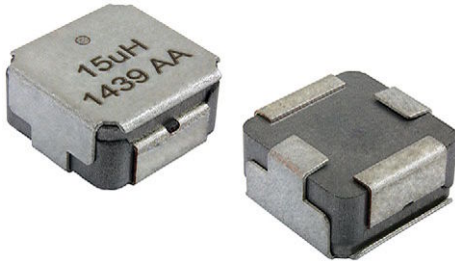


IHLE[®] High Current Inductors With E-Field Shield



LINKS TO ADDITIONAL RESOURCES



| STANDARD ELECTRICAL SPECIFICATIONS | | | | | |
|--|---------------------------------------|---------------------------------------|--|--|----------------------|
| L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μH) | DCR TYP. 25 °C (m Ω) | DCR MAX. 25 °C (m Ω) | HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾ | SATURATION CURRENT DC TYP. (A) ⁽²⁾ | SRF TYP. (MHz) |
| 0.22 | 1.68 | 1.86 | 36.0 | 32.0 | 117 |
| 0.47 | 2.38 | 2.55 | 27.0 | 19.0 | 77 |
| 0.68 | 3.30 | 3.53 | 21.5 | 16.2 | 51 |
| 1.0 | 4.58 | 4.90 | 19.0 | 16.2 | 45 |
| 2.2 | 11.70 | 12.50 | 11.5 | 14.0 | 32 |
| 3.3 | 15.40 | 16.48 | 10.6 | 11.8 | 23 |
| 4.7 | 26.60 | 28.46 | 7.2 | 9.1 | 18 |
| 5.6 | 29.60 | 31.67 | 6.9 | 9.0 | 18 |
| 10 | 50.00 | 53.50 | 5.1 | 5.2 | 13 |
| 15 | 62.00 | 66.34 | 4.8 | 3.6 | 10 |
| 22 | 103.00 | 110.21 | 3.7 | 3.8 | 9 |
| 33 | 149.00 | 159.43 | 3.1 | 3.2 | 6.1 |

Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage, across inductor (V1) = 50 V
- Rated isolation voltage, inductor lead to shield (V2) = 50 V
- ⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C
- ⁽²⁾ DC current (A) that will cause L_0 to drop approximately 20 %

FEATURES

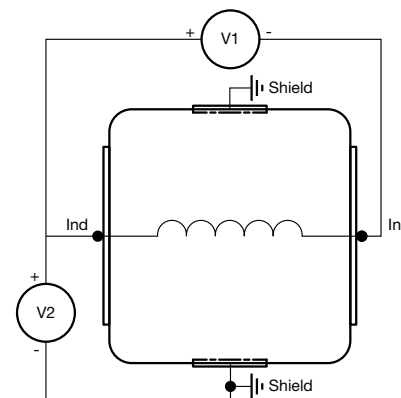
- High temperature, up to 155 °C
- Integrated E-Shield for maximum EMI reduction ⁽¹⁾
- Excellent DC/DC energy storage up to 1 MHz to 2 MHz. Filter inductor applications up the SRF (see standard electrical specifications table)
- Integrated E-Field shield eliminates need for separate shielding
- 20 dB E-Field reduction at 1 cm
 - Measured vertically from top center of device
- Lowest DCR/ μH , in this package size
- Handles high transient current spikes without saturation
- Coplanarity of the 4 terminals $\leq 100 \mu\text{m}$
- AEC-Q200 qualified
- IHLE design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

- ⁽¹⁾ Maximum E-Field reduction is realized when the IHLE shield is connected to ground

APPLICATIONS

- Engine and transmission control units
- Diesel injection drivers
- DC/DC converters for entertainment / navigation systems
- Noise suppression for motors
 - Windshield wipers
 - Power seats
 - Power mirrors
 - Heating and ventilation blower
 - HID lighting
- LED drivers



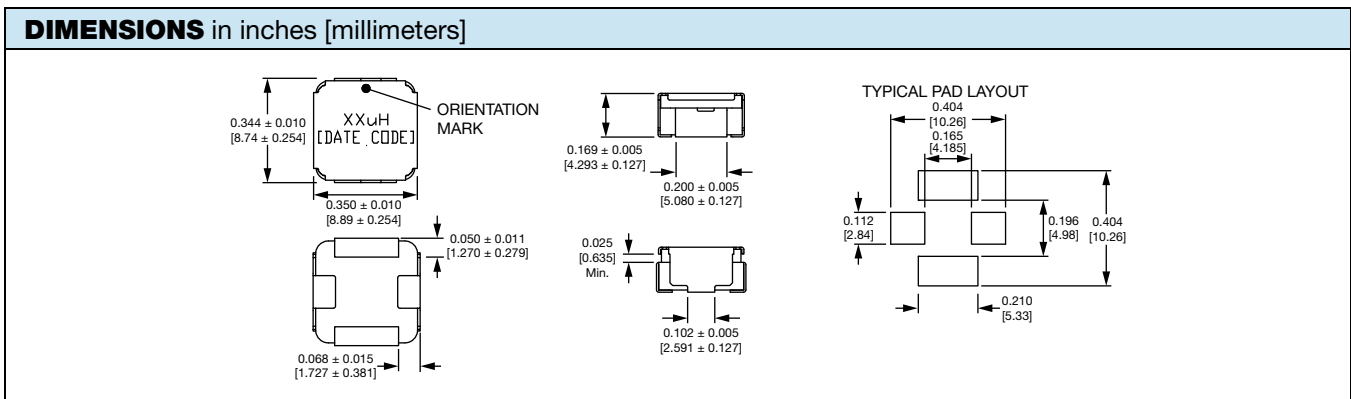
PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.



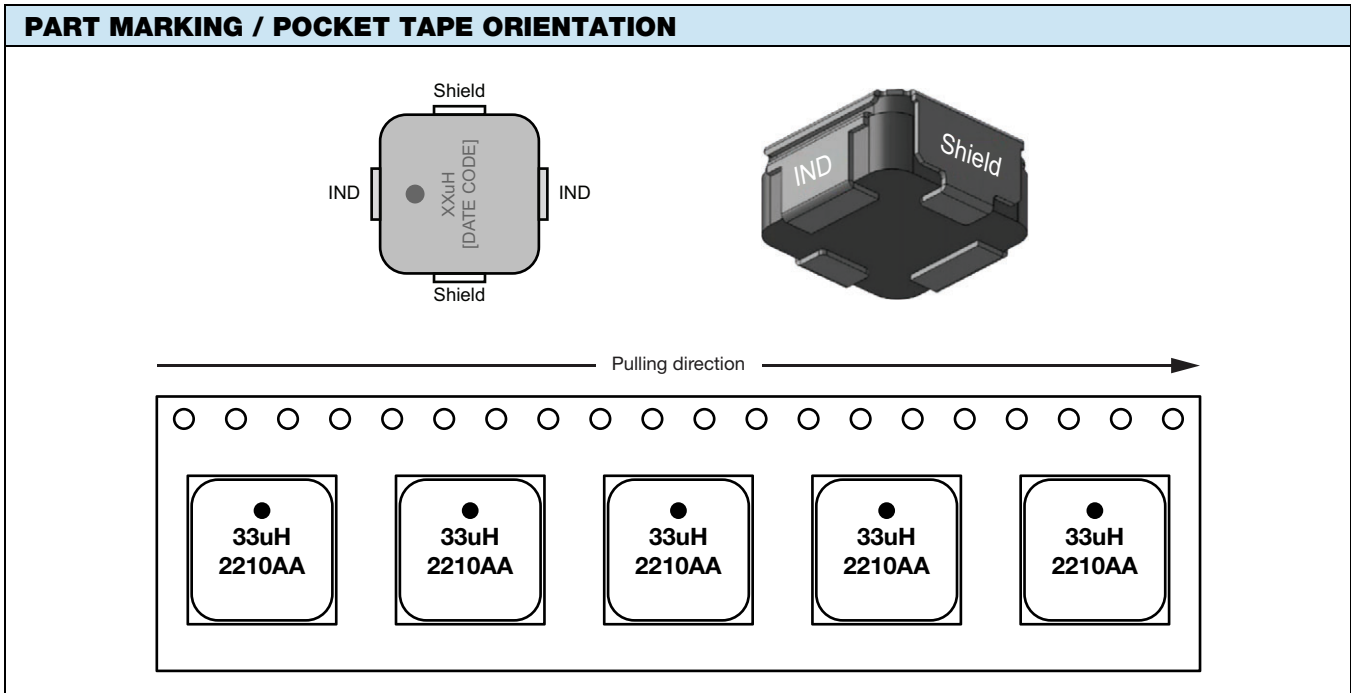
| DESCRIPTION | | | | |
|----------------|------------------|----------------------|--------------|--------------------------------|
| IHLE-3232DD-5A | 15 μ H | $\pm 20\%$ | ER | e3 |
| MODEL | INDUCTANCE VALUE | INDUCTANCE TOLERANCE | PACKAGE CODE | JEDEC® LEAD (Pb)-FREE STANDARD |

| GLOBAL PART NUMBER | | | | | | | | | | | | | | | | | |
|--------------------|---|---|---|------|---|---|---|--------------|---|------------------|---|---|------|--------|---|---|---|
| I | H | L | E | 3 | 2 | 3 | 2 | D | D | E | R | 1 | 5 | 0 | M | 5 | A |
| PRODUCT FAMILY | | | | SIZE | | | | PACKAGE CODE | | INDUCTANCE VALUE | | | TOL. | SERIES | | | |
| | | | | | | | | | | | | | | | | | |



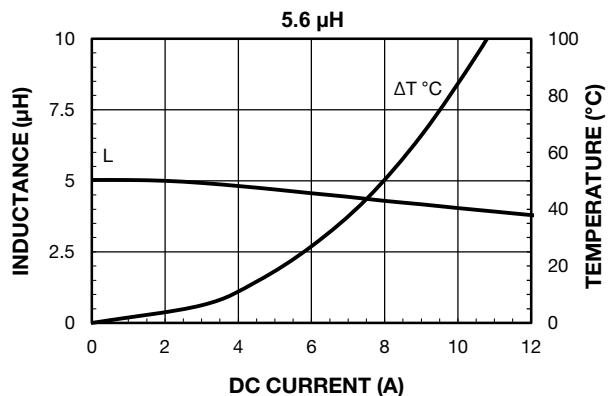
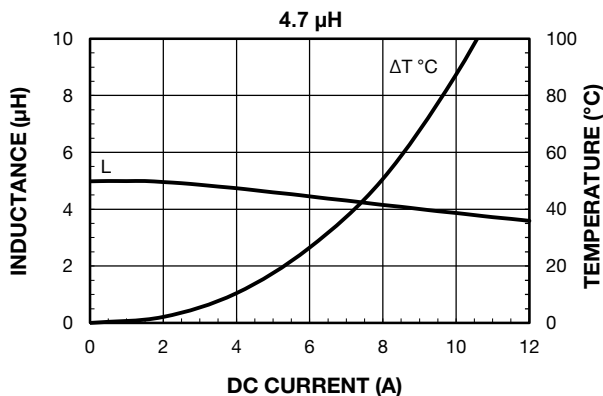
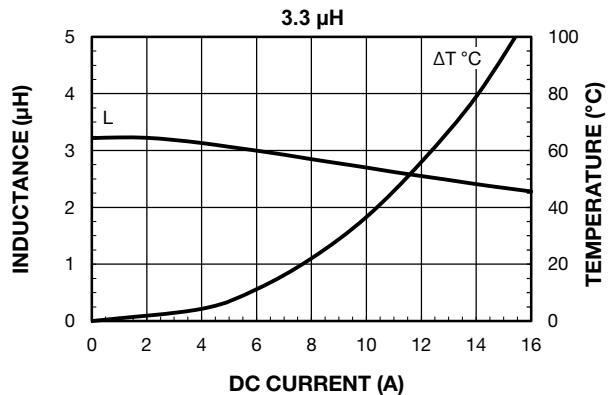
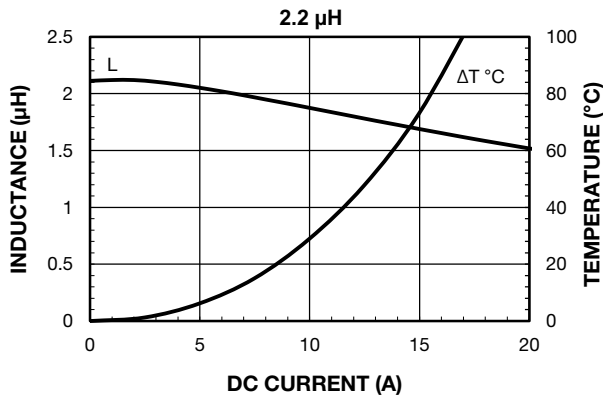
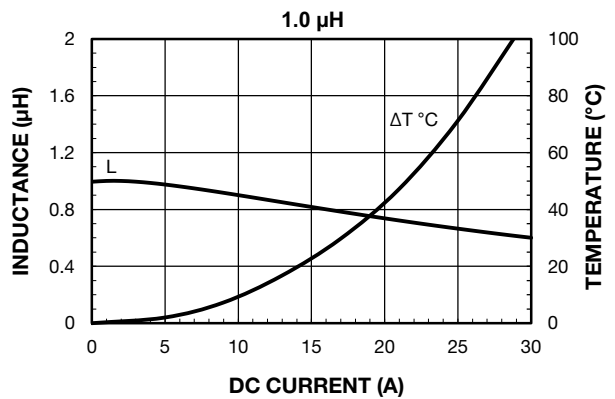
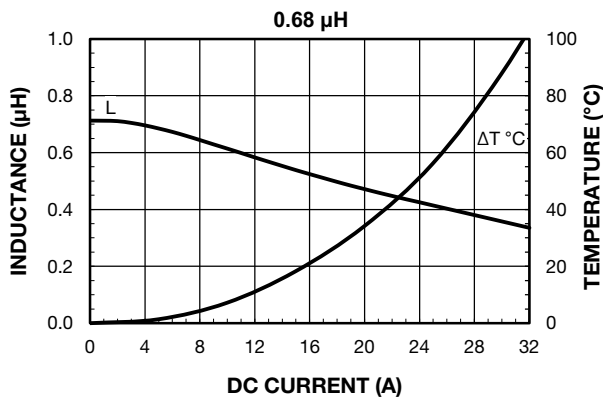
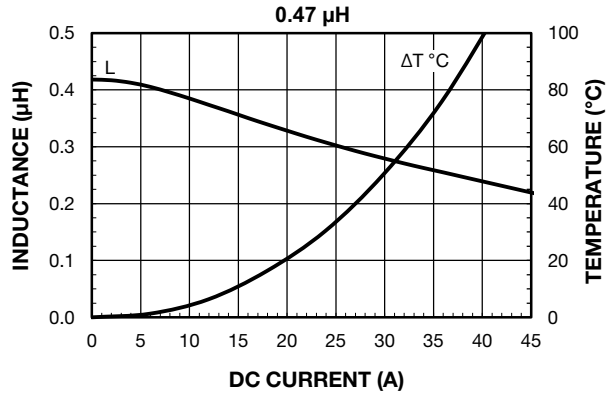
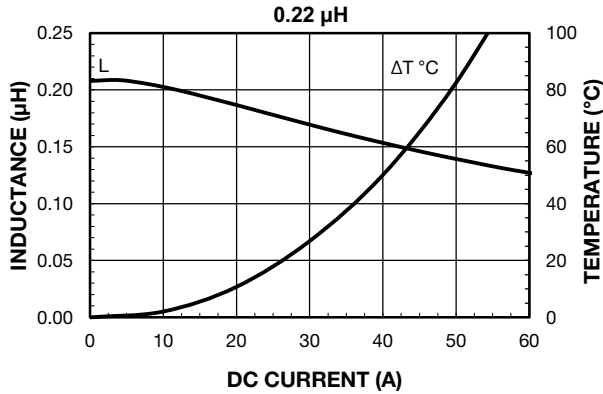
Notes

- Dot indicate the coil termination
- Coplanarity of 4 terminals: 0.004" [0.10]



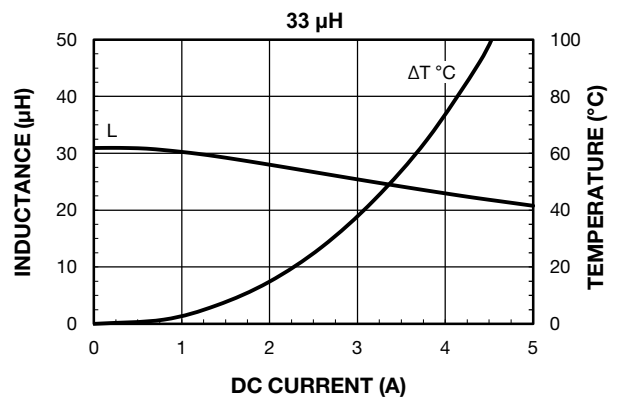
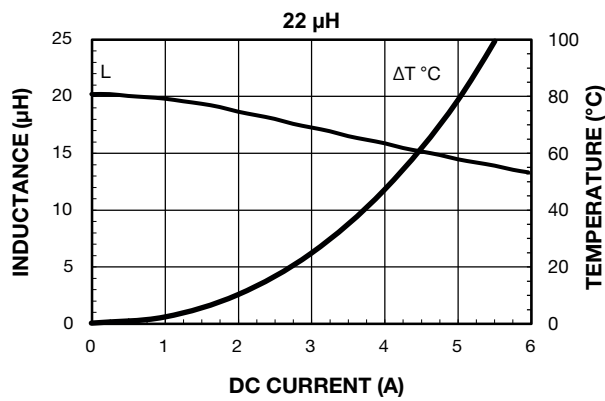
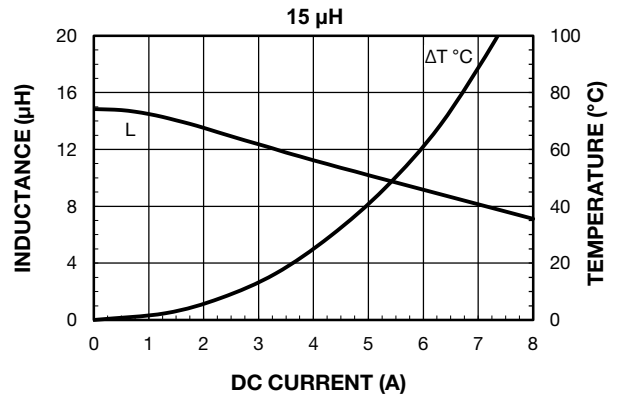
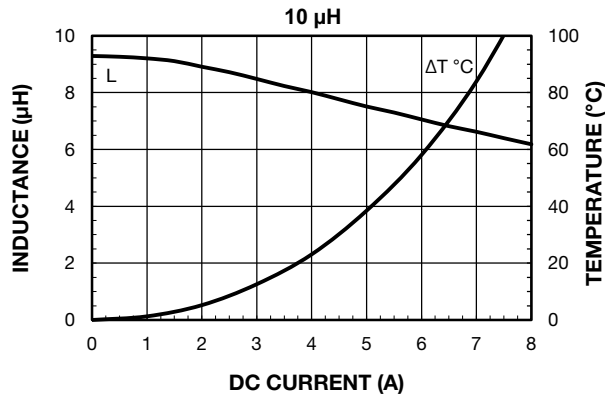


PERFORMANCE GRAPHS

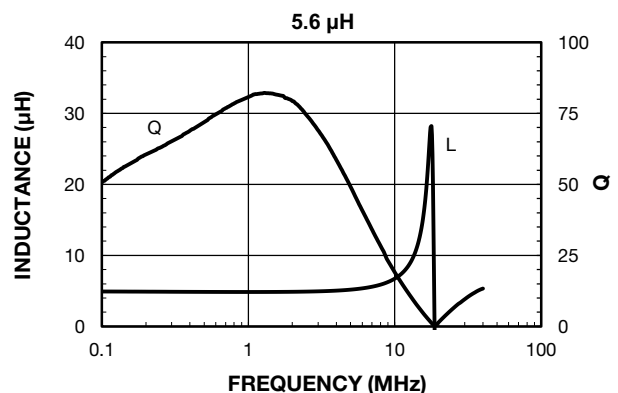
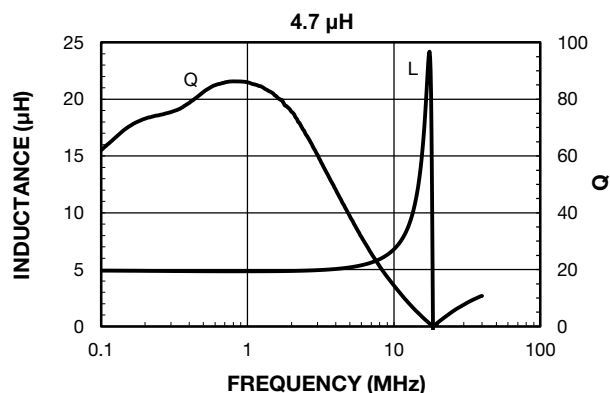
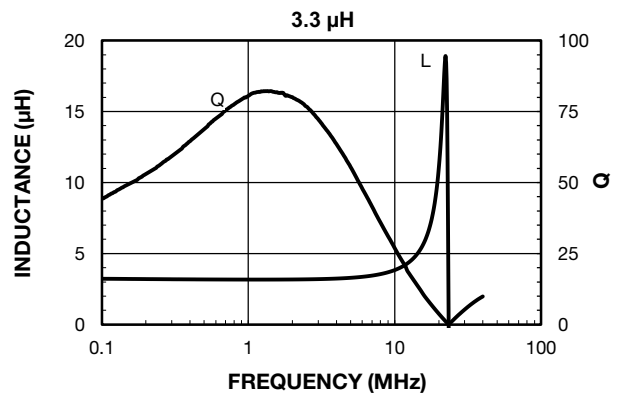
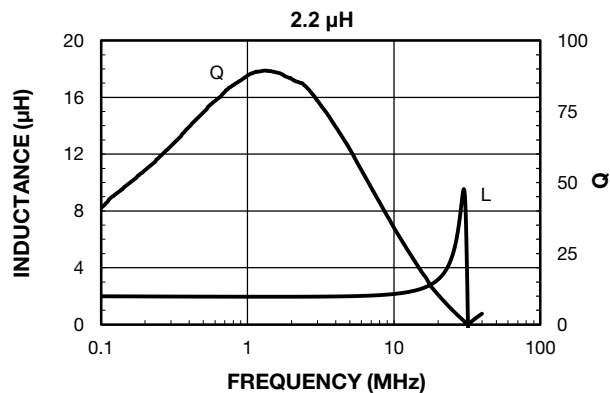
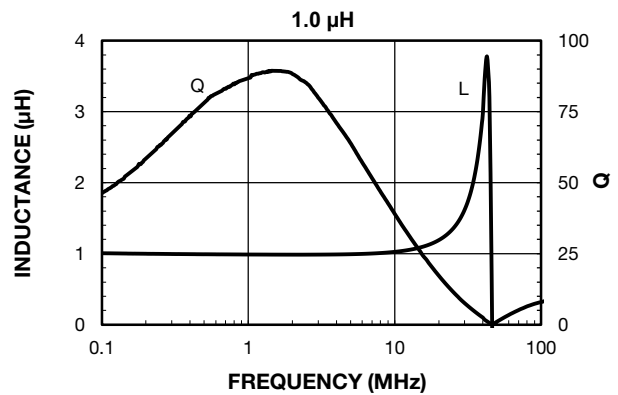
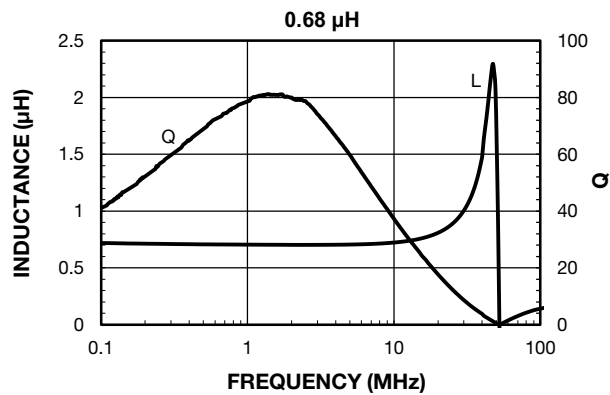
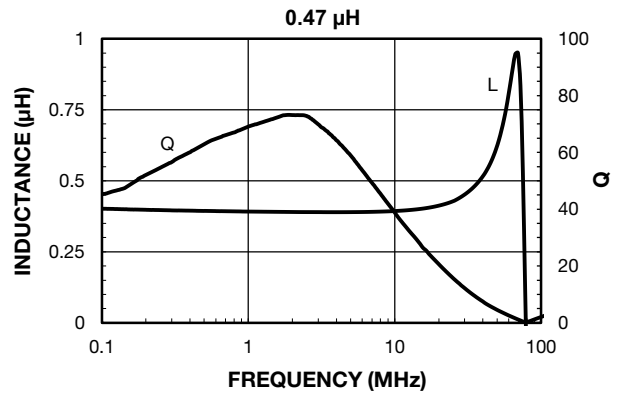
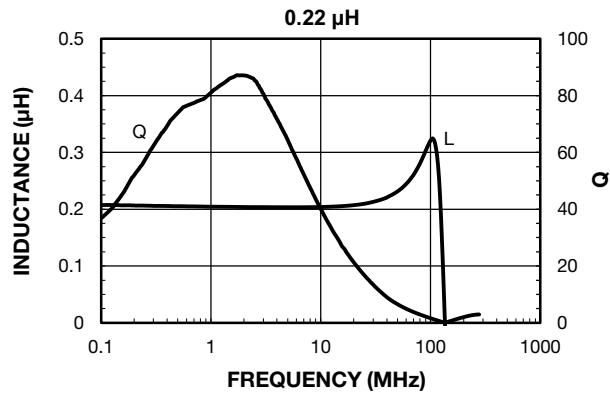




PERFORMANCE GRAPHS

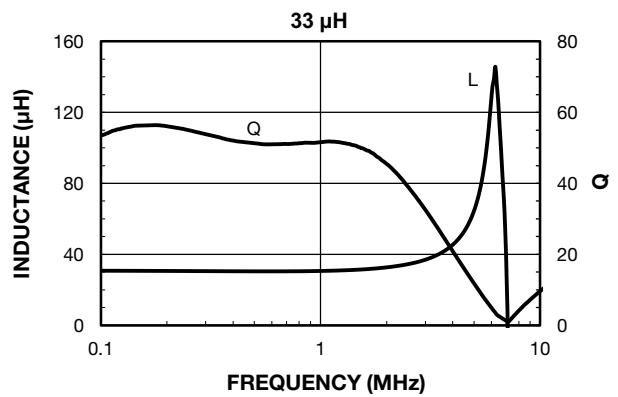
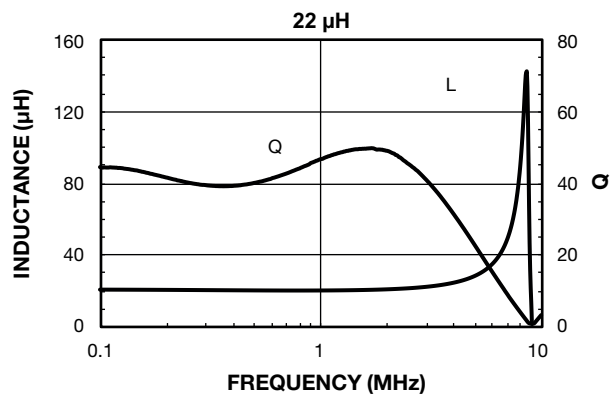
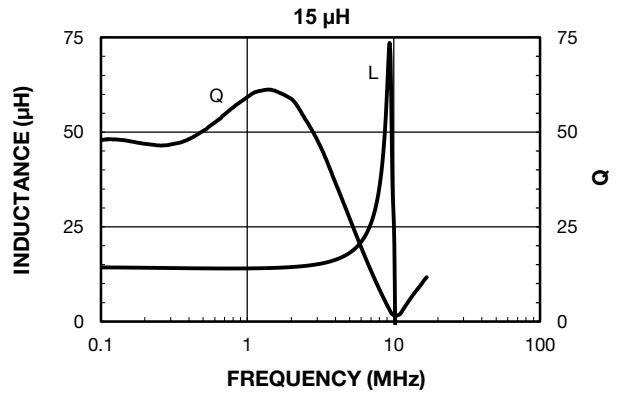
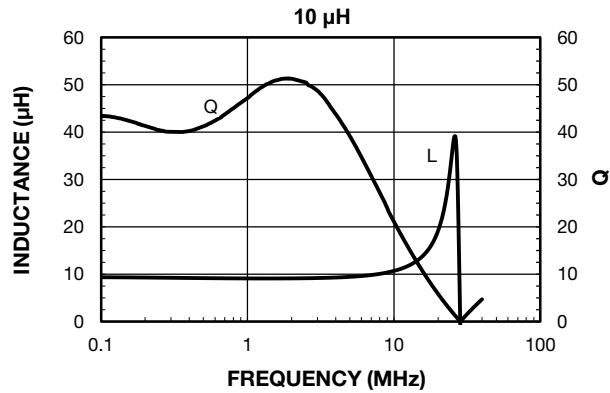


PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





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