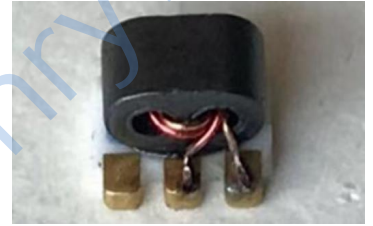


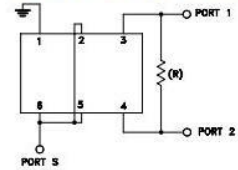
# Power Splitter/Combiner

## HT-TCP-2-25+



2 Way-0° 50Ω 200 to 2500 MHz

### electrical schematic



### Features

- low insertion, 0.8 dB typ.
- excellent amplitude unbalance, 0.2 dB typ.
- very good phase unbalance, 1.2 deg. typ.
- external resistor required
- aqueous washable
- leads for excellent solderability
- low cost

### Applications

- cellular
- PCN
- GPS

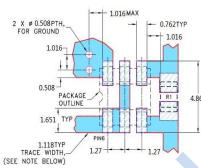
### Electrical Specifications

Freq. range (MHz)	Isolation(dB)		Insertion Loss (dB) Above 3.0 dB		Phase Unbalance (Degrees) Max.	Amplitude Unbalance(dB) Max.
	min	max	min	max		
200-2500	18	10	0.8	1.5	6.0	0.8

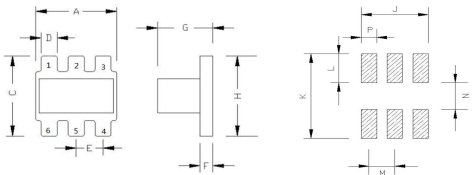
### Typical Performance Data( TEST CONDITIONS: INPUT POWER = 0dBm @Temperature = +25°C)

Freq.(MHz)	Total Loss (dB)		Amplitude Unbalance (dB)	Isolation(dB)	PhaseUnbal.(deg.)	VSWRS		
	S-1	S-2				S	1	2
200.0	-3.59	-3.60	0.01	19.18	0.16	1.96	1.56	1.56
300.0	-3.59	-3.62	0.03	20.14	0.19	1.96	1.59	1.60
450.0	-3.61	-3.65	0.04	21.32	0.24	1.95	1.63	1.64
550.0	-3.62	-3.67	0.05	22.10	0.25	1.95	1.66	1.67
650.0	-3.62	-3.69	0.07	22.96	0.31	1.94	1.68	1.69
850.0	-3.62	-3.72	0.10	25.08	0.32	1.92	1.74	1.75
950.0	-3.63	-3.74	0.11	26.37	0.35	1.92	1.76	1.77
1050.0	-3.63	-3.76	0.13	27.89	0.38	1.91	1.79	1.80
1150.0	-3.63	-3.78	0.15	29.69	0.44	1.89	1.82	1.83
1250.0	-3.63	-3.79	0.16	32.01	0.48	1.88	1.84	1.86
1400.0	-3.64	-3.82	0.18	37.48	0.56	1.85	1.88	1.90
1800.0	-3.88	-4.37	0.27	33.95	0.78	1.79	1.98	2.03
2000.0	-4.10	-4.79	0.31	28.83	0.86	1.75	2.03	2.09
2200.0	-4.15	-4.32	0.37	25.56	1.14	1.73	2.08	2.16
2500.0	-3.82	-4.56	0.43	22.27	1.32	1.71	2.15	2.27

### PCB Land Pattern



### Outline Drawing



### Outline Dimensions ( mm )

Dimension	Value	Dimension	Value
A	3.81	N	1.53
B	-	M	1.27
C	3.81	P	0.76
D	0.76	H	3.81
E	1.27	J	3.3
F	0.61	K	4.83
G	2.61	L	1.65

### Pin Connections

SUM PORT	2, 5, 6 (input)
PORT 1	3 (output1)
PORT 2	4 (output2)
GND	1
EXT.RESISTOR	475Ω 3,4

### Maximum Ratings

Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Power Input (as a splitter)	0.5W max.
Permanent damage may occur if any of these limits are exceeded.	

