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Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

Hyperfast Rectifier, 2 A FRED Pt®

eSMP® Series



SlimSMAW (DO-221AD)

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 A				
V_R	100 V, 200 V				
V _F at I _F	0.69 V				
I _{FSM}	60 A				
t _{rr} (typ.)	15 ns				
T _J max.	175 °C				
Package	SlimSMAW (DO-221AD)				
Circuit configuration	Single				

FEATURES

- Low profile package
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Class 2 whisker test
- Compatible to SOD-128 package case outline
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber industrial, and automotive applications.

MECHANICAL DATA

Case: SlimSMAW (DO-221AD)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse	VS-2EYH01-M3	V		100	V	
voltage	VS-2EYH02-M3	V_{RRM}		200	V	
Average rectified forward current		I _{F(AV)} (1)	T _C = 151 °C	2	Α	
Non-repetitive peak surge current		I _{FSM}	T _J = 25 °C, 10 ms sine pulse wave	60		
Operating junction and storage temperatures		T _J , T _{Stg}		-55 to +175	°C	

Note

(1) Mounted on infinite heatsink

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER		SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking	VS-2EYH01-M3	V _{BR} , V _R	I _R = 100 μA	100	-	-	V	
voltage	VS-2EYH02-M3			200	-	-		
Forward voltage, per diode		V _F	I _F = 2 A	-	0.86	0.93		
			I _F = 2 A, T _J = 150 °C	-	0.69	0.75		
Reverse leakage current, per diode		I _R	V _R = V _R rated	-	-	2	μA	
			$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	20		
Junction capacitance		C _T	V _R = 200 V	-	12	-	pF	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	$I_F = 1.0 \text{ A}, dI_F/dt =$	50 A/μs, V _R = 30 V	-	22	-	
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	15	-	ns
		$I_F = 0.5 \text{ A}, I_R = 1 \text{A}, I_{rr} = 0.25 \text{ A}$		-	-	28	
		T _J = 25 °C	$I_F = 2 \text{ A},$ $dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 100 \text{ V}$	ı	16	-	A
		T _J = 125 °C		1	26	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.7	-	
		T _J = 125 °C		ı	3.4	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	20	-	
		T _J = 125 °C		-	43	-	110

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55	-	175	°C	
Thermal resistance, junction to mount		R _{thJM} ⁽¹⁾	Infinite heatsink	-	12	15		
Thermal resistance, junction to ambient		R _{thJA}	Device mounted on FR4 PCB, 2 oz. standard footprint	-	120	150	°C/W	
Marking daviso	VS-2EYH01-M3		Coop at the ClimCNANA (DO 201AD)	2H1				
Marking device	VS-2EYH02-M3		Case style SlimSMAW (DO-221AD)		2H2			

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

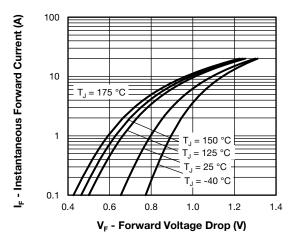


Fig. 1 - Typical Forward Voltage Drop Characteristics

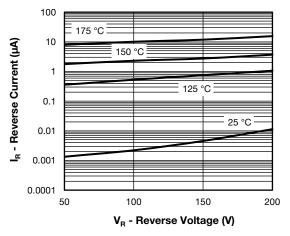


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

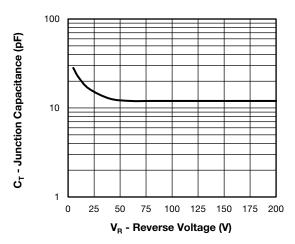


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

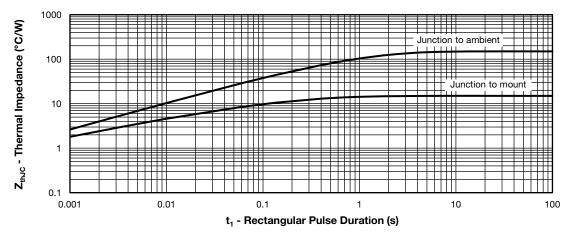


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

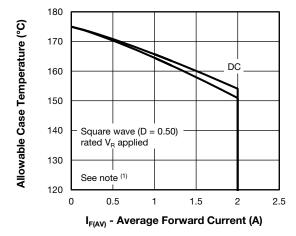


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

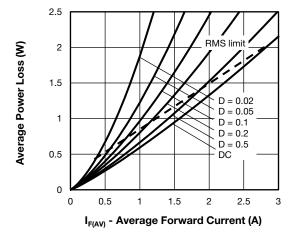


Fig. 6 - Forward Power Loss Characteristics

Note

Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R

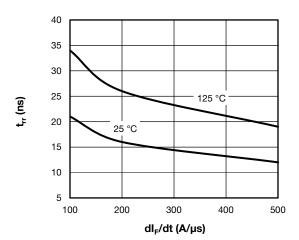


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

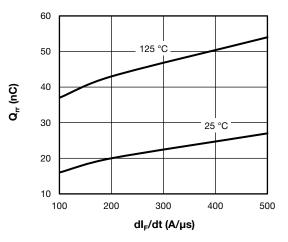
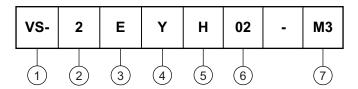


Fig. 8 - Typical Stored Charge vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (2 = 2 A)

3 - Circuit configuration:

E = single diode

4 - Y = SlimSMAW (DO-221AD)

5 - Process type,

H = hyperfast recovery

6 - Voltage code (02 = 200 V)

7 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-2EYH01-M3/H	0.033	Н	3500	7"diameter plastic tape and reel				
VS-2EYH01-M3/I	0.033	1	14 000	13"diameter plastic tape and reel				
VS-2EYH02-M3/H	0.033	Н	3500	7"diameter plastic tape and reel				
VS-2EYH02-M3/I	0.033	1	14 000	13"diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96582</u>					
Part marking information	www.vishay.com/doc?95562				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96585				

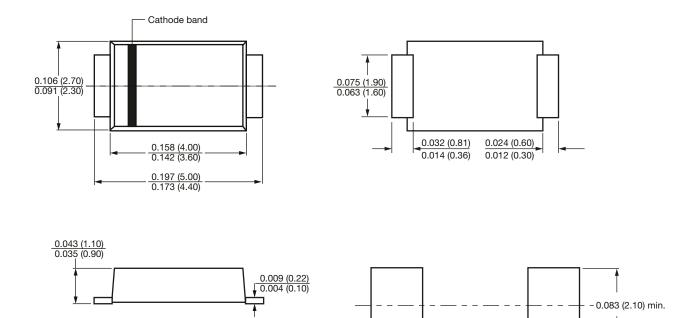


0.055 (1.40) min.

SlimSMAW (DO-221AD)

DIMENSIONS in inches (millimeters)

SlimSMAW (DO-221AD)



0.055 (1.40) min.

Mounting pad layout

0.118 (3.00) max.

0.228 (5.80) ref.



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