

5A, 50V - 1000V High Efficient Surface Mount Rectifier

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low forward voltage drop
- Low profile package
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- High frequency rectification
- Freewheeling application
- Switching mode converters and inverters in computer and telecommunication

MECHANICAL DATA

- Case: DO-214AB (SMC)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.210g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _F	5	Α		
V_{RRM}	50 - 1000	V		
I _{FSM}	150	Α		
T _{J MAX}	150	°C		
Package	DO-214AB (SMC)			
Configuration	Single die			









DO-214AB (SMC)



PARAMETER	SYMBOL	HSEA	HS5B	HEED	Heee	HSEC	LICE I	HEEK	HS5M	IINIIT
PARAMETER	STWIBUL	пээд	пээв	пээр	пээг	пээв	пэээ	пээк	пээм	UNII
Marking code on the device		HS5A	HS5B	HS5D	HS5F	HS5G	HS5J	HS5K	HS5M	
Repetitive peak reverse voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	140	210	280	420	560	700	V
Forward current	I _F	5					Α			
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I _{FSM}	150				А				
Junction temperature	TJ	- 55 to +150			°C					
Storage temperature	T _{STG}	- 55 to +150			°C					





THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	60	°C/W	

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
	HS5A HS5B HS5D HS5F		V _F	-	-	V
	HS5G	$I_F = 3A, T_J = 25^{\circ}C$		-	-	V
Forward voltage ⁽¹⁾	HS5J HS5K HS5M			-	1.35	V
	HS5A HS5B HS5D HS5F		V _F	-	1.00	V
	HS5G	I _F = 5A, T _J = 25°C		-	1.30	V
	HS5J HS5K HS5M			-	1.70	V
•		T _J = 25°C		-	10	μΑ
Reverse current @ rated V _R ⁽²⁾		T _J = 125°C	- I _R	-	250	μΑ
Junction capacitance	HS5A HS5B HS5D HS5F HS5G	1MHz, V _R = 4.0V	CJ	80	-	pF
	HS5J HS5K HS5M			50	-	pF
Reverse recovery time	HS5A HS5B HS5D HS5F HS5G	$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t _{rr}	-	50	ns
	HS5J HS5K HS5M			-	75	ns

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING		
HS5x	DO-214AB (SMC)	3,000 / Tape & Reel		

Notes:

1. "x" defines voltage from 50V(HS5A) to 1000V(HS5M)



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.1 Forward Current Derating Curve

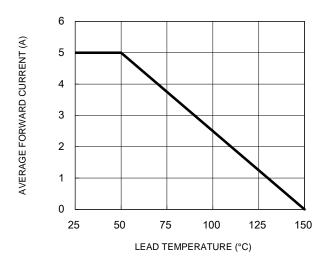


Fig.3 Typical Reverse Characteristics

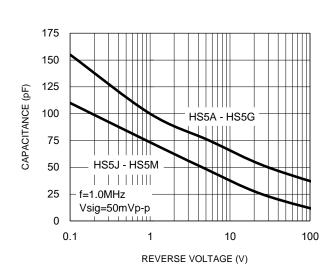
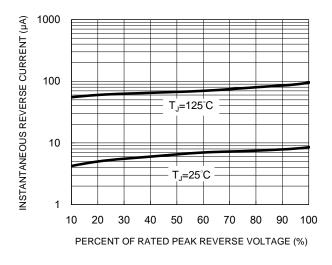


Fig.2 Typical Junction Capacitance

Fig.4 Typical Forward Characteristics



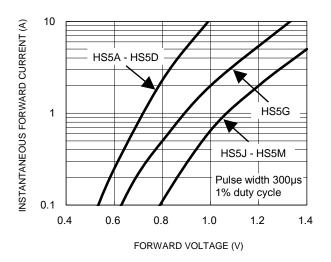
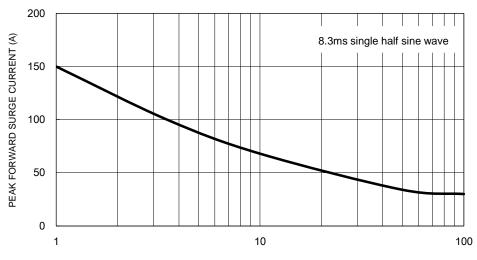


Fig.5 Maximum Non-Repetitive Forward Surge Current



NUMBER OF CYCLES AT 60 Hz

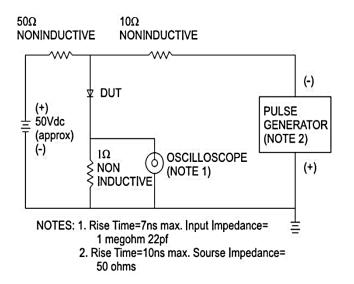


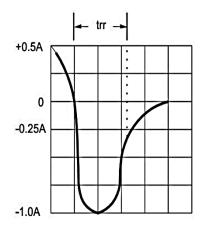


CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



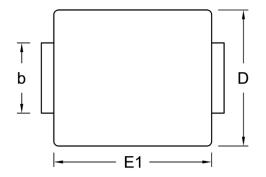


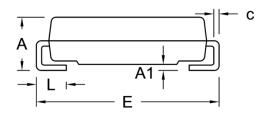




PACKAGE OUTLINE DIMENSIONS

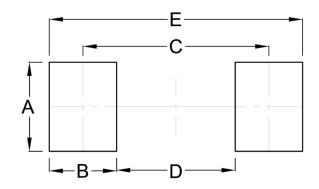
DO-214AB (SMC)





DIM. Unit		(mm)	Unit ((inch)
Dilvi.	Min.	Max.	Min.	Max.
Α	2.00	2.62	0.079	0.103
A1	0.10	0.20	0.004	0.008
b	2.90	3.20	0.114	0.126
С	0.15	0.31	0.006	0.012
D	5.59	6.22	0.220	0.245
E	7.75	8.13	0.305	0.320
E1	6.60	7.11	0.260	0.280
L	1.00	1.60	0.039	0.063

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	3.30	0.130
В	2.50	0.098
С	6.90	0.272
D	4.40	0.173
E	9.40	0.370

MARKING DIAGRAM



P/N = Marking Code G = Green Compound

ΥW = Date Code F = Factory Code

Taiwan Semiconductor

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