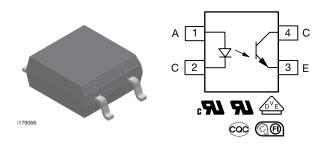
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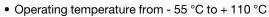
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# **Optocoupler Phototransistor Output, SOP-4,** Mini-Flat Package, 110 °C Rated



parts per reel.

#### **FEATURES**





· SOP (small outline package)

- Isolation test voltage, 3750 V<sub>RMS</sub> (1 s)
- · Low saturation voltage
- · Fast switching times
- Low coupling capacitance
- End-stackable, 0.100" (2.54 mm) spacing
- Material categorization: for definitions

# HALOGEN FREE

**GREEN** (5-2008)

compliance please see www.vishay.com/doc?99912

#### **LINKS TO ADDITIONAL RESOURCES**



#### **DESCRIPTION**

The 110 °C rated SFH1690AT, SFH1690BT, SFH1690CT, and SFH1690ABT family has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin 100 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits. The SFH1690 series is available only on tape and reel. There are 2000

#### **APPLICATIONS**

- PLCs
- Telecommunication

#### **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI
- CQC GB4943.1
- CQC GB8898
- FIMKO

ORDERING INFORMATION							
S F H 1 6 9 0 # # T - X 0 0 1  PART NUMBER TAPE AND REEL VDE OPTION 7.21 mm							
AGENCY CERTIFIED / PACKAGE		CTF	R (%)				
UL, cUL, BSI, FIMKO, CQC	50 to 300	50 to 150	100 to 300	100 to 200			
SOP-4, Mini flat	SFH1690ABT	SFH1690AT	SFH1690BT	SFH1690CT			
UL, cUL, BSI, FIMKO, CQC, VDE (option 1)	50 to 300	50 to 150	100 to 300	100 to 200			
SOP-4, Mini flat	-	-	SFH1690BT-X001	-			

#### Note

· For additional information on the available options refer to option information

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
DC forward current		I <sub>F</sub>	50	mA				
Reverse voltage		V <sub>R</sub>	6	V				
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	2.5	Α				
Power dissipation		P <sub>diss</sub>	80	mW				
Derate linearly from 25 °C			0.7	mW/°C				
OUTPUT								
Collector emitter voltage		V <sub>CEO</sub>	70	V				
Emitter collector voltage		V <sub>ECO</sub>	7	V				
Oullington		I <sub>C</sub>	50	mA				
Collector current	t <sub>p</sub> ≤ 1 ms	I <sub>C</sub>	100	mW				
Power dissipation		P <sub>diss</sub>	150	mW				
Derate linearly from 25 °C			1.5	mW/°C				
COUPLER								
Isolation test voltage between emitter and detector	t = 1 s	V <sub>ISO</sub>	3750	$V_{RMS}$				
Operating temperature range		T <sub>amb</sub>	-55 to +110	°C				
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C				
Soldering temperature	max. 10 s dip soldering distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C				

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

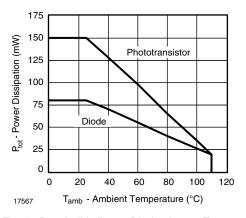


Fig. 1 - Permissible Power Dissipation vs. Temperature

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT	INPUT							
Forward voltage	$I_F = 5 \text{ mA}$		$V_{F}$	-	1.15	1.4	V	
Reverse current	V <sub>R</sub> = 6 V		$I_R$	-	0.01	10	μA	
Capacitance	$V_R = 0 V$ , $f = 1 MHz$		Co	-	14		pF	
OUTPUT								
Collector emitter leakage current	V <sub>CE</sub> = 20 V		I <sub>CEO</sub>	-	-	100	nA	
Collector emitter breakdown voltage	$I_C = 100 \mu A$		BV <sub>CEO</sub>	70	-	-	V	
Emitter collector breakdown voltage	I <sub>E</sub> = - 10 μA		BV <sub>ECO</sub>	7	-	-	V	
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 2.5 mA		V <sub>CEsat</sub>	-	0.25	0.4	V	
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		C <sub>CE</sub>	-	2.8	-	pF	
COUPLER								
Coupling capacitance	f = 1 MHz		C <sub>C</sub>	-	0.3	-	pF	
Capacitance (input to output)			C <sub>IO</sub>	-	0.5	-	pF	

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>		SFH1690ABT	CTR	50	-	300	%
	1	SFH1690AT	CTR	50	-	150	%
	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	SFH1690BT	CTR	100	-	300	%
		SFH1690CT	CTR	100	-	200	%

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Rise time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>r</sub>	-	3	-	μs	
Fall time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>f</sub>	-	4	-	μs	
Turn-on time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>on</sub>	-	5	-	μs	
Turn-off time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>off</sub>	-	3	-	μs	

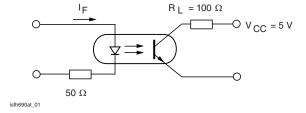


Fig. 2 - Switching Operation (without Saturation)

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SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)			-	55 / 110 / 21	-			
Pollution degree (DIN VDE 0109)			-	2	-	mm		
Comparative tracking index per DIN IEC112 / VDE 0303 part 1, group Illa per DIN VDE 6110 175 399			175	-	399			
V <sub>IOTM</sub>		V <sub>IOTM</sub>	6000	-	-	V		
V <sub>IORM</sub>		V <sub>IORM</sub>	707	-	-	V		
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	-	-	≥ 10 <sup>12</sup>	Ω		
isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	-	-	≥ 10 <sup>11</sup>	Ω		
P <sub>SO</sub>			-	-	350	mW		
I <sub>SI</sub>			-	-	150	mA		
T <sub>SI</sub>			-	-	165	°C		
Creepage distance			5	-	=	mm		
Clearance distance			5	-	-	mm		
Insulation thickness between emitter and detector			≥ 0.4	-	-	mm		

#### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

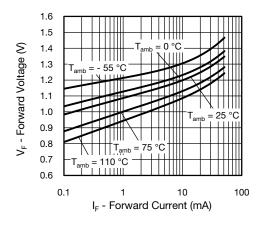


Fig. 3 - Forward Voltage vs. Forward Current

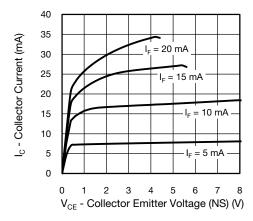


Fig. 4 - Collector Current vs. Collector Emitter Voltage (NS)

<sup>•</sup> As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

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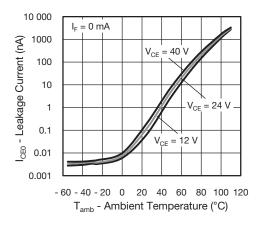


Fig. 5 - Leakage Current vs. Ambient Temperature

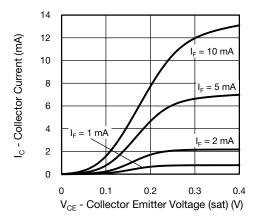


Fig. 6 - Collector Current vs. Collector Emitter Voltage (sat)

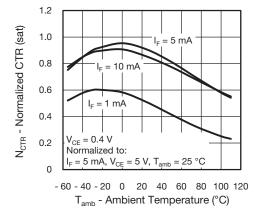


Fig. 7 - Normalized Current Transfer Ratio (sat) vs.
Ambient Temperature

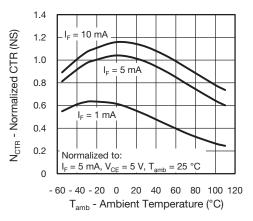


Fig. 8 - Normalized Current Transfer Ratio (NS) vs.
Ambient Temperature

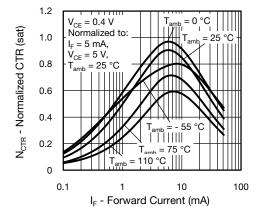


Fig. 9 - Normalized CTR (sat) vs. Forward Current

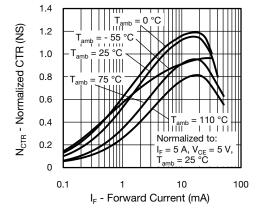


Fig. 10 - Normalized CTR (NS) vs. Forward Current

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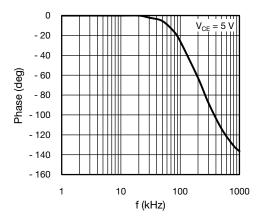


Fig. 11 - F<sub>CTR</sub> vs. Phase Angle

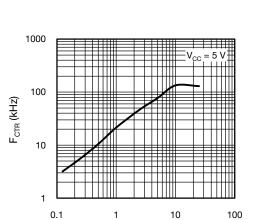


Fig. 12 - F<sub>CTR</sub> vs. Collector Current

 $I_C$  (mA)

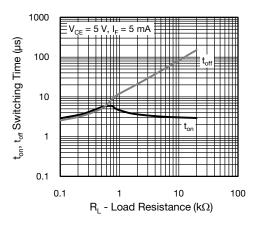
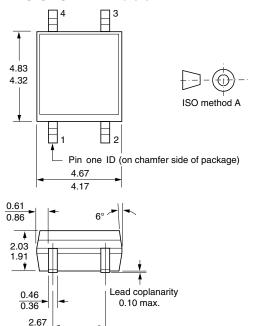


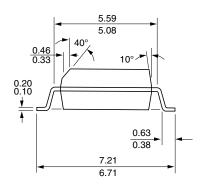
Fig. 13 - Switching Time vs. Load Resistance

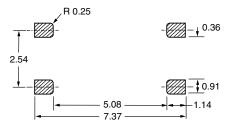
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### **PACKAGE DIMENSIONS** in millimeters

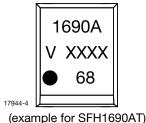






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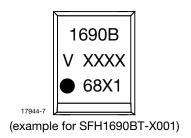
### **PACKAGE MARKING**



(example for SFH

### Notes

- XXXX = LMC (lot marking code)
- The marking of the SFH1690ABT will either show 1690A or 1690B on the first line
- Tape and reel suffix (T) is not part of the package marking





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